Memory Module (SIMM) Product Note

On SPARCcenter 2000 boards (which have two buses), always install eight SIMMs at a time, four in group x (where x = 0 or 1) on bus 0 and four in the same group number on bus 1. On SPARCserver 1000 boards, install SIMMs in groups of four.

Note – Fill the lowest-numbered SIMM groups first. For multiple-board configurations, fill group 0 on each board before filling group 1 (but see below for rules for optimizing system performance.)

SPARCserver 1000 System Board

The SPARCserver 1000 system board has one bus, and all SIMM groups are attached to this bus. The SIMM slots on each board are divided into four groups of four slots each. The SIMM groups are numbered from 0 to 3. The SIMM slot locations are shown in Figure 1, organized by groups.

The rules for the SPARCserver 1000 board are:

- Fill the lowest numbered groups first on every system board.

- Do not mix SIMM sizes within a group.

- To optimize memory performance, keep the numbers of memory groups balanced across boards, in multiples of two or four boards (This allows the system to interleave memory across boards.) For example, if there are three boards in the system, and a total of four SIMM groups, fill both group 0 and group 1 on the first two boards. Memory added to the third board is usable,