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This manual provides detailed procedures that describe the removal and replacement of field replaceable units (FRUs) in the SPARCstation™ 20 system. People that should use this book include technicians, advanced computer system end-users with experience in replacing hardware and troubleshooting, system administrators, and qualified service providers.

Document Organization

This book is comprised of seven parts. See the following table.

<table>
<thead>
<tr>
<th>TABLE P-1</th>
<th>Document Organization</th>
</tr>
</thead>
<tbody>
<tr>
<td>Part Number/Title</td>
<td>Chapter Number/Title</td>
</tr>
<tr>
<td>Part 1, “System Information”</td>
<td>Chapter 1, “Product Description”</td>
</tr>
<tr>
<td>Part 2, “Troubleshooting”</td>
<td>Chapter 2, “Troubleshooting Overview”</td>
</tr>
<tr>
<td>Chapter 3, “Power-On Self-Test (POST)”</td>
<td>Describes the execution of POST and provides troubleshooting errors displayed during POST</td>
</tr>
<tr>
<td>Chapter 4, “Troubleshooting Procedures”</td>
<td>Provides corrective actions to generalized hardware symptom casualty</td>
</tr>
</tbody>
</table>
TABLE P-1  Document Organization (Continued)

<table>
<thead>
<tr>
<th>Part Number/Title</th>
<th>Chapter Number/Title</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Part 3, “Preparing for Service”</td>
<td>Chapter 5, “Safety and Tool Requirements”</td>
<td>Explains how to work safely when servicing the SPARCstation 20 system</td>
</tr>
<tr>
<td></td>
<td>Chapter 6, “Power On and Off”</td>
<td>Provides step-by-step procedures to power on and power off the SPARCstation 20 system</td>
</tr>
<tr>
<td>Part 4, “Subassembly Removal and Replacement”</td>
<td>Chapter 7, “Internal Access”</td>
<td>Provides step-by-step procedures to remove the SPARCstation 20 system cover, attach the wrist strap, and replace the cover</td>
</tr>
<tr>
<td></td>
<td>Chapter 8, “Removing and Installing Subassemblies”</td>
<td>Provides step-by-step procedures to remove and install major subassemblies</td>
</tr>
<tr>
<td></td>
<td>Chapter 9, “Storage Devices”</td>
<td>Provides step-by-step procedures to remove and replace storage devices</td>
</tr>
<tr>
<td>Part 5, “System Board”</td>
<td>Chapter 10, “System Board Overview”</td>
<td>Provides system board description and damage prevention; identifies replaceable system board components</td>
</tr>
<tr>
<td></td>
<td>Chapter 11, “System Board and Component Replacement”</td>
<td>Provides step-by-step procedures to remove and replace the system board, and to remove and replace various components associated with system board operation</td>
</tr>
<tr>
<td>Part 6, “Illustrated Parts Breakdown”</td>
<td>Chapter 12, “Illustrated Parts Breakdown”</td>
<td>Lists replaceable parts for the SPARCstation 20 system</td>
</tr>
<tr>
<td>Part 7, “Appendixes, Glossary, and Index”</td>
<td>Appendix A, B, and C</td>
<td>Provides physical, electrical, and environmental specifications, a functional description of the SPARCstation 20 system, connector pinouts and signal descriptions, and information on SCSI targeting</td>
</tr>
<tr>
<td></td>
<td>Glossary</td>
<td>Provides a listing of acronyms, terms, and definitions</td>
</tr>
<tr>
<td></td>
<td>Index</td>
<td>Provides a quick reference to specific topics</td>
</tr>
</tbody>
</table>

---

**Warnings and Cautions**

**Caution** – This equipment contains lethal voltage. Accidental contact can result in serious injury or death.
Caution – The surface of the SuperSPARC™ and hyperSPARC™ CPU chips may be hot and cause personal injury if touched. Avoid contact.

Caution – Improper handling by unqualified personnel can cause serious damage to this equipment. Unqualified personnel who tamper with this equipment may be held liable for any resultant damage to the equipment.

Caution – Use proper ESD grounding techniques when handling components. Wear an antistatic wriststrap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

Individuals who service this equipment must observe all safety precautions and ensure compliance with skill level requirements, certification, and all applicable local and national laws.

Procedures in this document must be performed by trained maintenance providers. Only people who have been trained by the Sun Microsystems™ training facilities (or by Sun Microsystems affiliates) and have been certified as required by local and national laws are considered qualified.

Note – Prior to performing service or repair, carefully read the associated procedure. If a similar repair has not been performed on comparable equipment, do not attempt to perform the procedure.
Typographic Conventions

The following table lists typographic conventions used within this service manual.

<table>
<thead>
<tr>
<th>Typeface or Symbol</th>
<th>Meaning</th>
<th>Example</th>
</tr>
</thead>
<tbody>
<tr>
<td>AaBbCc123</td>
<td>The names of commands, files, and directories; on-screen computer output</td>
<td>Edit your .login file. Use <code>ls -a</code> to list all files. <code>machine_name% You have mail.</code></td>
</tr>
<tr>
<td>AaBbCc123</td>
<td>What you type, contrasted with on-screen computer output</td>
<td><code>machine_name% su Password:</code></td>
</tr>
<tr>
<td>AaBbCc123</td>
<td>Command-line placeholder: replace with a real name or value</td>
<td>To delete a file, type <code>rm filename</code>.</td>
</tr>
<tr>
<td>AaBbCc123</td>
<td>Book titles, new words or terms, or words to be emphasized</td>
<td>Read Chapter 6 in User's Guide. These are called class options. You must be root to do this.</td>
</tr>
</tbody>
</table>

Related Documentation

The following table lists related documentation supporting SPARCstation 20 operation:

<table>
<thead>
<tr>
<th>Document Title</th>
<th>Part number</th>
</tr>
</thead>
<tbody>
<tr>
<td>OpenBoot™ 2.x Command Reference Manual</td>
<td>802-3241</td>
</tr>
<tr>
<td>SunDiag™ User's Guide</td>
<td>801-6627</td>
</tr>
<tr>
<td>SunDiag™ User's Guide (Addendum)</td>
<td>801-7263</td>
</tr>
<tr>
<td>SunVTSTM 2.0 User's Guide</td>
<td>802-5331</td>
</tr>
<tr>
<td>SunVTSTM 2.0 Test Reference Manual</td>
<td>802-5330</td>
</tr>
</tbody>
</table>
UNIX Commands

This manual does not include specific software commands or procedures. Instead, it names software tasks and refers to operating system documentation or the handbook that was shipped with the hardware.

See one or more of the following:

- *Solaris 2.x Handbook for SMCC Peripherals* contains Solaris™ 2.x software commands.
- On-line AnswerBook for the complete set of documentation supporting the Solaris 2.x software environment.
- Other software documentation that you received with your system.

###TABLE P-3  Related Documentation (Continued)

<table>
<thead>
<tr>
<th>Document Title</th>
<th>Part number</th>
</tr>
</thead>
<tbody>
<tr>
<td><em>SunVTS™™ 2.0 Quick Reference Card</em></td>
<td>802-5329</td>
</tr>
<tr>
<td><em>Product Notes—SPARCstation 20</em></td>
<td>801-7464</td>
</tr>
<tr>
<td>System administration manual for your computer system</td>
<td>N/A</td>
</tr>
<tr>
<td>Operating system documentation</td>
<td>N/A</td>
</tr>
</tbody>
</table>
Shell Prompts

The following table lists the default system prompt and superuser prompt for the C shell, Bourne shell, and Korn shell.

<table>
<thead>
<tr>
<th>Shell</th>
<th>Prompt</th>
</tr>
</thead>
<tbody>
<tr>
<td>C shell prompt</td>
<td>machine_name%</td>
</tr>
<tr>
<td>C shell superuser prompt</td>
<td>machine_name#</td>
</tr>
<tr>
<td>Bourne shell and Korn shell prompt</td>
<td>$</td>
</tr>
<tr>
<td>Bourne shell and Korn shell superuser prompt</td>
<td>#</td>
</tr>
</tbody>
</table>
Product Description

The SPARCstation™ 20 system accommodates up to 512 megabytes (Mbytes) of memory using dynamic single in-line memory modules (DSIMMs) and video single in-line memory modules (VSIMMs). Installing a second VSIMM is done through the optional auxiliary video board (AVB).

Standard Features

The SPARCstation 20 system has four audio ports on the back panel and contains an internal speaker. A variety of internal storage devices are available:

- 535-Mbyte hard disk drive
- 1.05-Gbyte hard disk drive
- 2.1-Gbyte hard disk drive
- Diskette drive
- SunCD 2Plus™™ drive
- SunCD™™ 4 drive

Note – The SPARCstation 20 system is configured with one of two chassis types: a chassis that supports the 1-inch SunCD 2Plus drive and a chassis that supports the 1.6-inch SunCD 4 drive.

FIGURE 1-1 shows a typical SPARCstation 20 system. Appendix B provides detailed system information.
The SPARCstation 20 system contains the following subassemblies, boards, and components (see FIGURE 1-2):

- System board
- SIMMs
- DSIMMs (eight maximum)
- VSiMMs (two maximum)
- Non-volatile SIMM (NViSIMM) (one)
- SBus card (four maximum)
- MBus module (two maximum)
- Hard disk drive, 3.5-inch (88.0-mm), single connector (two maximum)
- SunCD 2Plus drive or SunCD 4 drive (optional)
- Diskette drive (3.5-inch) (optional)
- Power supply (150 watt, two-fan)
FIGURE 1-3 shows an interior view of the major subassemblies and the system board.

**Interior View**

FIGURE 1-3 shows an interior view of the major subassemblies and the system board.
FIGURE 1-3  SPARCstation 20 System Overhead Interior View

Rear View

FIGURE 1-4 shows a rear view of the system. Appendix C provides additional information. The appendix describes each connector on the back panel, lists pin assignments, signal descriptions, and shows detailed connector illustrations.
Legend:

1—Power-On/Standby switch
2—AC power outlet
3—Lock block
4—MBus
5—SBus
6—AVB (13W3 video)
7A—Audio, headphone
7B—Audio, microphone
7C—Audio, line-out
5 or 6
10A—Parallel
10B—AUI Ethernet
9—Twisted-pair Ethernet
11—Keyboard
12—Serial port A/B
13—13W3 video
14—AC power receptacle
7D—Audio, line-in
8—SCSI

FIGURE 1-4 SPARCstation 20 System Unit Rear View
Software Requirements

TABLE 1-1 lists the various system configurations and the corresponding software requirements.

<table>
<thead>
<tr>
<th>Configuration</th>
<th>Software Requirements</th>
</tr>
</thead>
<tbody>
<tr>
<td>Basic System</td>
<td>Solaris’ 1.1.1 Version B software environment (SunOS 4.1.3.1 Version B) or later Solaris 1.x releases Solaris 2.3 (SunOS 5.3) software environment and Solaris 2.3 Supplement, Revision B, or later Solaris 2.x releases</td>
</tr>
<tr>
<td>Multiprocessor versions</td>
<td>Solaris 2.3 (SunOS 5.3) software environment and Solaris 2.3 Supplement, Revision B, or later Solaris 2.x releases</td>
</tr>
<tr>
<td>Systems configured with VSIMMs</td>
<td>Solaris 2.3 (SunOS 5.3) software environment with Solaris 2.3 Supplement, Revision B, or later Solaris 2.x releases</td>
</tr>
</tbody>
</table>

Internal Options

TABLE 1-2 lists the internal options of the SPARCstation 20 system.

<table>
<thead>
<tr>
<th>Option</th>
<th>Quantity (max.)</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SBus card</td>
<td>4</td>
<td>System board contains four SBus slots, 32-bit-wide data bus, 25-MHz bus speed</td>
</tr>
<tr>
<td>MBus module</td>
<td>2</td>
<td>Provides additional microprocessor modules, 64-bit-wide data bus</td>
</tr>
<tr>
<td>Hard disk drive</td>
<td>2</td>
<td>Supports up to two 3.5-inch, 535-Mbyte or 1.05-Gbyte 2.1-Gbyte (formatted), single-connector disk drives</td>
</tr>
<tr>
<td>SunCD 2Plus</td>
<td>1</td>
<td>Internal, double-speed, 644-Mbyte CD-ROM drive</td>
</tr>
<tr>
<td>SunCD 4</td>
<td>1</td>
<td>Internal, quad-speed, 644-Mbyte CD-ROM drive</td>
</tr>
</tbody>
</table>
TABLE 1-3 lists the external SCSI options.

TABLE 1-3  External SCSI Options

<table>
<thead>
<tr>
<th>Unit</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Desktop backup pack</td>
<td>Tape drive: 150-Mbyte, .25-inch QIC-150, 2.5-Gbyte 5-Gbyte, 8-mm, 10-Gbyte, 14-Gbyte 5-Gbyte, 4-mm 20-Gbyte, 4-mm DAT</td>
</tr>
<tr>
<td>Desktop disk pack</td>
<td>Disk expansion unit with variety of SCSI disk drive capacities</td>
</tr>
<tr>
<td>Desktop SunCD 2Plus</td>
<td>External, double-speed, 644-Mbyte CD-ROM drive</td>
</tr>
<tr>
<td>Desktop SunCD 4</td>
<td>EXternal, quad-speed, 644-Mbyte CD-ROM drive</td>
</tr>
</tbody>
</table>
### External SCSI Options

<table>
<thead>
<tr>
<th>Unit</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Desktop storage module</td>
<td>Disk drive: 1.3-Gbyte disk drive. 5.0-Gbyte disk drive</td>
</tr>
<tr>
<td>SCSI expansion pedestal</td>
<td>Up to eight SCSI disks, CD-ROM drive, tape drive, other non-disk units. Requires a minimum of two SCSI controllers if all three trays are used. No more than seven SCSI devices on each SCSI bus.</td>
</tr>
<tr>
<td>Multiple disk pack</td>
<td>Desktop enclosure containing a disk array of SCSI disk drives</td>
</tr>
</tbody>
</table>
Troubleshooting Overview

This chapter describes how to diagnose a problem and execute diagnostic tests.

Default Boot Mode

FIGURE 2-1 is a flow diagram of the default boot mode for OpenBoot PROM Version 2.14 or later. The default boot mode initiates the power-on self-test (POST) diagnostics if any one of the following conditions exists:

- Keyboard Stop (L1) and D keys are pressed and held down
- diag-switch? parameter is set to true
- Keyboard is disconnected
FIGURE 2-1  Default Boot Mode (OpenBoot PROM Version 2.14 or Later)
Diagnostic Tools

Diagnostic tools include power-on self-test (POST), OpenBoot PROM diagnostics, SunDiag system exerciser, and Sun verification test suite (SunVTS). A description of the POST is provided in Chapter 3. TABLE 2-1 lists the diagnostic tools and provides a description of each. TABLE 2-2 lists switch settings and results for the autoboot switch and the diagnostic switch. Descriptions of the OpenBoot PROM diagnostics, the SunDiag system exerciser, and SunVTS are provided in the following sections.

<table>
<thead>
<tr>
<th>Diagnostic</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Power-On Self-Test</td>
<td>Locates and diagnoses trouble in: system board, DSIMM in slot 0, MBus modules, and NVRAM. Resides in the OpenBoot PROM. Initialized at power-on when the keyboard Stop (L1)-d keys are pressed and held, or if the diag-switch? parameter is set to true, or if the keyboard is disconnected. See Chapter 3.</td>
</tr>
<tr>
<td>OpenBoot PROM</td>
<td>Includes Ethernet, diskette drive controller, and other tests. Must be at the ok prompt to execute on-board diagnostics. See “OpenBoot PROM Diagnostics” on page 12.</td>
</tr>
<tr>
<td>SunVTS (Solaris 2.5 and later)</td>
<td>Executes multiple diagnostic hardware tests from a single user interface and is used to verifying configuration and functionality of most hardware controllers and devices. See SunVTS 2.0 User’s Guide for more information. See Section “Sun VTS” on page 2-15.</td>
</tr>
</tbody>
</table>
OpenBoot PROM Diagnostics

On-board diagnostics are executed from the **ok** prompt. If there is a problem with the operating system, the system will default to the **ok** prompt. The **ok** prompt can also be accessed by shutting down the operating system.

TABLE 2-3 describes selected OpenBoot PROM diagnostic tests and what must occur before each test can be executed. Some of the tests verify the proper operation of the network controller, while other tests verify the diskette drive system, memory, and system clock. Appendix E provides a detailed description of each test.

### TABLE 2-2  Autoboot and Diagnostic Switch Settings

<table>
<thead>
<tr>
<th>Autoboot Switch</th>
<th>Diagnostic Switch</th>
<th>Result</th>
</tr>
</thead>
<tbody>
<tr>
<td>False</td>
<td>Either true or false</td>
<td>ok prompt (OpenBoot PROM commands)</td>
</tr>
<tr>
<td>True</td>
<td>False</td>
<td>Boot operating system from disk (default)</td>
</tr>
<tr>
<td>True</td>
<td>True</td>
<td>Boot operating system from network (default)</td>
</tr>
</tbody>
</table>

### OpenBoot PROM Diagnostic Tests

- **test screen**: Tests the video graphics hardware and monitor. The `diag-switch?` NVRAM parameter must be set to true.
- **test floppy**: Tests the diskette drive response to commands. Insert a formatted diskette into the drive.
- **test scsi**: Tests the system board SCSI interface logic. The `diag-switch?` NVRAM parameter must be set to true.
- **test net-aui**: Performs an internal and external loopback test on the AUI (Thick) Ethernet interface. A cable must be connected between the rear panel AUI Ethernet port and the Ethernet transceiver.
- **test net-tpe**: Performs an internal and external loopback test on the TPE interface. A cable must be connected between the rear panel TPE port and a TPE hub. If the `tpe-link-test?` parameter is false (disabled), the external loopback test will appear to pass even if a cable is not connected.
- **test net**: Performs an internal and external loopback test on the auto-selected system Ethernet interface. A cable must be connected between the system and the Ethernet transceiver or hub.
### TABLE 2-3  OpenBoot PROM Diagnostic Tests  (Continued)

<table>
<thead>
<tr>
<th>Type of Test</th>
<th>Description</th>
<th>Preparation</th>
</tr>
</thead>
<tbody>
<tr>
<td>test disk</td>
<td>Tests internal or external SCSI disks that have self-test diagnostic programs on the drive controller</td>
<td>The disk drive must be spinning before this test is executed; enter a <code>boot disk alias</code> command to cause the drive to spin up.</td>
</tr>
<tr>
<td>test disk0</td>
<td>Performs a self-test diagnostic on the CD-ROM drive</td>
<td>The CD-ROM drive must be set to SCSI address 6 and have a CD inserted.</td>
</tr>
<tr>
<td>test disk1</td>
<td>Tests the SCSI tape drive by executing the drive self-test program; <code>tape 0</code> and <code>tape 1</code> are the first tape drive. <code>tape 1</code> is the second tape drive.</td>
<td>The tape drive must be set to SCSI address 4 (<code>tape 0</code>) or address 5 (<code>tape 1</code>) and a tape must be installed.</td>
</tr>
<tr>
<td>test disk2</td>
<td>Tests serial ports. Outputs an alphanumeric test pattern on the system serial ports</td>
<td>Attach a terminal to the serial port to observe the output.</td>
</tr>
<tr>
<td>test disk3</td>
<td>Executes the keyboard self-test; four keyboard LEDs should flash on once and the message, Keyboard Present, is displayed</td>
<td>Keyboard must be connected.</td>
</tr>
<tr>
<td>test-memory</td>
<td>Tests main memory</td>
<td>Set <code>diag-switch?</code> to true or set <code>selftest-#megs</code> to the desired parameter.</td>
</tr>
<tr>
<td>test-all</td>
<td>Tests all system devices (such as SBus cards) with built-in test programs; hard disks, tapes, and CD-ROMs are not tested</td>
<td>Set the <code>diag-switch?</code> parameter to true.</td>
</tr>
<tr>
<td>watch-clock</td>
<td>Displays seconds from the NVRAM/TOD chip</td>
<td>None.</td>
</tr>
<tr>
<td>watch-net</td>
<td>Monitors Ethernet packets on the auto-selected Ethernet cable connected to the system</td>
<td>A cable must be connected to the back panel Ethernet port.</td>
</tr>
<tr>
<td>watch-aui</td>
<td>Monitors Ethernet packets (10Base5—Thicknet) on the Ethernet cable connected to the system</td>
<td>A cable must be connected between the rear panel AUI Ethernet port and the Ethernet transceiver.</td>
</tr>
<tr>
<td>watch-tpe</td>
<td>Monitors Ethernet packets (10Base_T—twisted-pair Ethernet) on the Ethernet cable connected to the system</td>
<td>A cable must be connected between the rear panel TPE port and a TPE hub.</td>
</tr>
<tr>
<td>watch-net-all</td>
<td>Monitors Ethernet packets on all Ethernet interfaces installed in the system</td>
<td>Cables must be connected to all Ethernet ports being tested.</td>
</tr>
</tbody>
</table>
The SunDiag system exerciser is used to test and exercise system resources and peripheral equipment. The SunDiag system exerciser verifies that the system is functioning properly.

The SunDiag system exerciser program is located as follows:

- /usr/diag/sundiag (Solaris 1.1.x)
- /opt/SUNWdiag/bin (Solaris 2.x)

If the SunDiag system exerciser is not on the system hard disk or server, it can be loaded from either a CD or from a network server (for more information, see the SunDiag User’s Guide).

If SunDiag system exerciser passes, the system is operating properly. If SunDiag system exerciser fails, error messages are displayed, indicating the part of the system that failed.

Note – If a SunDiag system exerciser error message is not descriptive enough, execute the POST for a more descriptive explanation (see Chapter 3.)
Sun VTS

The SunVTS software executes multiple diagnostic hardware tests from a single user interface and is used to verifying configuration and functionality of most hardware controllers and devices. SunVTS operates primarily from the OpenWindows™ user interface, allowing test parameters to be set quickly and easily while a diagnostic test operation is being performed.

The SunVTS operation is provided in the documents listed in TABLE 2-4. These documents furnish detailed information about SunVTS and are available on the Solaris on Sun Hardware AnswerBook. The Solaris on Sun Hardware AnswerBook is provided on the SMCC Updates CD for the Solaris release being operated.

<table>
<thead>
<tr>
<th>TABLE 2-4</th>
<th>SunVTS Documentation</th>
</tr>
</thead>
<tbody>
<tr>
<td>Title</td>
<td>Part Number</td>
</tr>
<tr>
<td>SunVTS User’s Guide</td>
<td>802-5331</td>
</tr>
<tr>
<td>SunVTS Test Reference Manual</td>
<td>802-5330</td>
</tr>
<tr>
<td>SunVTS Quick Reference Card</td>
<td>802-5329</td>
</tr>
</tbody>
</table>

The main features of the SunVTS environment include:

- SunVTS kernel
- SunVTS user interface
- SunVTS teletype

SunVTS Kernel

The SunVTS kernel (vtsk) diagnostic tool controls all testing and operates in the OpenWindows background. When activated, vtsk probes the hardware configuration of the system tested and responds to vtsui. vtsk coordinates execution of individual tests and manages the messages sent by these tests.

SunVTS User Interface

The SunVTS user interface (vtsui) diagnostic tool operates in the OpenWindows background. Upon activation, vtsui provides vtsk control, various user options, tests, and read log files.
SunVTS Teletype

The SunVTS teletype (vtstty) diagnostic tool controls the vtsk from either a command shell or a terminal attached to a serial port. Most options available in vtstty have equivalent options in vtsui. vtstty is applicable to server configurations only.
Power-On Self Test (POST)

The Power-on self-test (POST) diagnostics reside in the OpenBoot PROM, which is located on the system board. There are three POST modes: normal, full diagnostic, and abbreviated diagnostic. The following is a list of the POST diagnostic topics presented in this chapter.

Normal Mode

When the system is turned on in normal mode, the POST is not executed and control is sent to the OpenBoot PROM. If autoboot is enabled (set to true), the operating system is loaded. If autoboot is disabled, system initialization stops when the ok prompt is displayed.

Full Diagnostic Mode

Full diagnostic mode tests the system board components and the installed MBus modules. POST is executed if one of two actions occur:

- Press and hold the Stop (L1) and D keys while system power is being applied.
- Set the diag-switch? NVRAM parameter to true in the OpenBoot PROM.
CODE EXAMPLE 3-1 provides a typical POST progress message that is displayed through serial port A.

**CODE EXAMPLE 3-1  Typical POST Progress Message**

```
SMCC SPARCstation 10/20 UP/MP POST version xxx (xx/xx/xxxx)

CPU_#0  TI, TMS390Z55(3.x) 1Mb External cache
CPU_#2  TI, TMS390Z55(3.x) 1Mb External cache

CPU_#1 ****** NOT installed ******
CPU_#3 ****** NOT installed ******

<<< CPU_00000000 on MBus Slot_00000000 >>> IS RUNNING (MID = 00000008)

MMU Context Table Reg Test
MMU Context Register Test
.
.
.
.
```

The uniprocessor POST tests one MBus module. The multiprocessor POST tests all installed MBus modules. POST detects only major failures. In a fully configured system with two or more processors, POST may take several minutes to be completed.

To view POST progress and error messages, connect a terminal to serial port A or use a tip connection to another Workstation. See “Setting Up a tip Connection.”

If a terminal is not connected to serial port A, or a tip connection is not implemented, the screen will be blank during the POST diagnostic test mode. In this case, the keyboard LEDs are used to determine error conditions. See “Status LEDs and Indicators.”
Abbreviated Diagnostic Mode

POST is executed if the keyboard is disconnected and the diag-switch? NVRAM parameter is set to false in the OpenBoot PROM. During the abbreviated diagnostics, no progressive test messages are displayed and are not printed. If errors occur during the abbreviated POST, error messages are transferred through serial port A and are viewed by connecting a terminal or tip connection. If no errors occur, a message similar to CODE EXAMPLE 3-2 is displayed through serial port A.

CODE EXAMPLE 3-2  Typical Abbreviated POST Error-Free Message

```
SMCC SPARCstation 10/20 UP/MP POST version 3.1 (11/19/93)
CPU_#0       TI, TMS390Z55(3.x)       1Mb External cache
CPU_#1       ******** NOT installed *******
CPU_#2       ******** NOT installed *******
CPU_#3       ******** NOT installed *******

>>>>> Power On Self Test (POST) is running .... <<<<<
```

Setting Up a tip Connection

The tip connection allows the use of a shell window on the Sun Workstation™ as a terminal to the system being tested. In setting up a tip connection, serial port A is used to connect to another Sun Workstation (either the same type of SPARC system or a different type of Sun Workstation or server system). The tip connection uses the SunOS™ to help in working with the OpenBoot PROM.

To set up a tip connection, proceed as follows:

1. Connect the SPARCstation 20 serial port A to another Sun Workstation serial port using a serial connection cable. This connection is made with a 3-wire null modem cable. Connect wires 3-2, 2-3, and 7-7.

2. At the other Sun Workstation, add the following line to the /etc/remote file:
Note – The example shows connection to serial port A (ttya). To use serial port B on the other Sun Workstation, enter ttyb.

3. At the system prompt, type the following: .

```
hostname% tip hardwire
```

The shell window is now a tip window directed to the Sun Workstation serial port. When powering on the SPARCstation 20 in diagnostic mode, messages will be displayed at this window.

4. When POST is completed, disconnect the tip window by typing ~. (tilde, period).

```
~.
```

POST Tests

CODE EXAMPLE 3-3 provides a sample listing of the full diagnostic mode tests executed by POST in a system with two MBus modules installed. Note that the beginning of the test indicates the number of MBus modules installed.
CODE EXAMPLE 3-3  Sample Listing of the Full Diagnostic Mode Test

SMCC SPARCstation 10/20 UP/MP POST version xxx (xx/xx/xxxx)

CPU_#0 TI, TMS390Z55(3.x) 1Mb External cache
CPU_#2 TI, TMS390Z55(3.x) 1Mb External cache

CPU_#1 ******* NOT installed *******
CPU_#3 ******* NOT installed *******

<<< CPU_00000000 on MBus Slot_00000000 >>> IS RUNNING (MID = 00000008)

MMU Context Table Reg Test
MMU Context Register Test
MMU TLB Bit Pattern Tests
MMU Flush Tests
D-Cache RAM Write/Read Test
D-Cache PTAG Write/Read Test
D-Cache STAG Write/Read Test
I-Cache RAM Write/Read Test
I-Cache PTAG Write/Read Test
I-Cache STAG Write/Read Test
I-Cache Flush Test
Cache Flashclear Test
MXCC Register Test
MXCC E-Cache Tag RAM Test
MXCC E-Cache Data RAM Test (1 MB E$DATA RAM, MXCC_CSR=00000000)
MXCC Non-Cache Block Zero Test
MXCC Non-Cache Block Copy Test
MXCC Cacheable Block Read Test
MXCC Cacheable Block Write Test
EMC/SMC Control Regs Tests
ECC Multiple UE Test
ECC Multiple CE Test
ECC Multiple CE, UE Test
FPU Register File Test
FPU Misaligned Reg Pair Test
FPU Single-precision Tests
FPU Double-precision Tests
FPU SP Invalid CEXC Test
FPU SP Overflow CEXC Test
FPU SP Underflow CEXC Test
FPU SP Divide-by-0 CEXC Test
FPU SP Inexact CEXC Test
FPU SP Trap Priority > Test
FPU SP Trap Priority < Test
FPU SP UE Trap Priority Test
FPU DP Invalid CEXC Test
FPU DP Overflow CEXC Test
FPU DP Underflow CEXC Test
FPU DP Divide-by-0 CEXC Test
FPU DP Inexact CEXC Test
FPU DP Trap Priority > Test
FPU DP Trap Priority < Test
FPU DP UE Trap Priority Test
FPU DP CE Trap Priority Test
Memory Address Pattern Test
System Interrupt Regs Tests
PROC0 Interrupt Regs Tests
Soft Interrupts OFF Test
Soft Interrupts ON Test
PROC0 User Timer Test
PROC0 Counter/Timer Test
System Counter Test
MSI/MSBI Control Reg Tests
IOMMU CAM NTA Pattern Test
IOMMU TLB NTA Pattern Test
IOMMU CAM TLB Comparator Test
IOMMU TLB Flush Tests
DMA2/MACIO ID Register Test
DMA2/MACIO E_CSR Reg. Test
LANCE Address Port Tests
LANCE Data Port Tests
DMA2/MACIO D_CSR Reg. Test
DMA2/MACIO D_ADDR Reg. Test
DMA2/MACIO D_BCNT Reg. Test
DMA2/MACIO D_NADDR Reg. Test
ESP Registers Tests
DMA2/MACIO P_CSR Reg. Test
DMA2/MACIO P_ADDR Reg. Test
DMA2/MACIO P_BCNT Reg. Test
PPORT Registers Tests
DMA2/MACIO PPORT IO Lpbck Tst
DMA2/MACIO PPORT XFR Lpbck Tst
TOD Registers Test
POST Error Messages

Most POST error messages are displayed at the system monitor and are self-explanatory. Use the error messages returned by POST to troubleshoot the system. CODE EXAMPLE 3-4 lists typical POST error messages.

CODE EXAMPLE 3-4  Typical POST Error Messages

```
STATUS : Power-On SelfTest FAILED ... SIMM <J0201> Not Installed
STATUS : Power-On SelfTest FAILED ... Replace MBUS0 Module
STATUS : Power-On SelfTest FAILED ... Replace MBUS1 Module
STATUS : Power-On SelfTest FAILED ... Replace Main Logic Board
STATUS : TMS390Z55 and TMS390Z50 Modules can NOT be mixed. No POST run.
```

Note – In CODE EXAMPLE 3-4, the last error message indicates that an MBus processor module with external cache has been installed into the system with an MBus module with no external cache. This is not a supported configuration.

Status LEDs and Indicators

The system LED is located on the right front side of the chassis and lights when the system is operating. FIGURE 3-1 shows the location of the system LED. TABLE 4-1 on page 27 suggests corrective actions when the system LED is not lit.
When POST fails at power-on, LED indicators on specific keyboard keys display a particular LED pattern. See FIGURE 3-2 and TABLE 3-1. Specific keyboard keys include:

- Scroll Lock
- Num Lock
- Compose

Note – During normal system operation, the keyboard key LEDs should not be interpreted as diagnostic error indicators.

Note – The Caps Lock key LED is not used as a power-on test failure indicator, but flashes on and off while POST is initialized.
An indication of POST failure is when the keyboard Stop (L1) and D keys are pressed and held when powering on the system and the Caps Lock key indicator does not flash on and off. See “System Board Test for troubleshooting information.

### Completing POST

When the POST is completed, determine if it passed or failed. If POST passed but operational integrity of the system is questionable, initiate the OpenBoot PROM diagnostics. If POST failed, use the keyboard LED indicator status or a displayed error message (if a terminal or tip window was established) to verify the failure.
Troubleshooting Procedures

This chapter describes how to troubleshoot possible problems and includes suggested corrective actions. TABLE 4-1 identifies system problems and corrective actions.

**TABLE 4-1** Troubleshooting Tips

<table>
<thead>
<tr>
<th>System Problem</th>
<th>Corrective Action</th>
</tr>
</thead>
<tbody>
<tr>
<td>LED on front of system not lit</td>
<td>Verify that the power switch is turned on and that the power cord is connected to the system and an AC receptacle. If the power switch is in the on position and the power cord is connected, shut down the system and verify that the LED cable is connected. If the LED cable is connected, see “Power Supply Test” on page 29 and “System Board Test” on page 31.</td>
</tr>
<tr>
<td>Disk drive fails to boot or does not respond</td>
<td>Shut down the system. The SCSI controller or disk drive may be defective. See “Disk Drive Errors” on page 33 and “Power-On Does Not Succeed” on page 29; see also Chapter 9.</td>
</tr>
<tr>
<td>Operating system does not recognize disk drive at boot</td>
<td>Shut down the system. Verify that every disk drive on the SCSI bus is set to a different SCSI address. System internal drives are automatically assigned SCSI addresses as follows: 3 (bottom drive), 1 (top drive). See Chapter 9.”</td>
</tr>
<tr>
<td>No video output on monitor</td>
<td>Verify that the power cord is connected to the monitor and the monitor is turned on. Use a VOM to check voltages. See “Video Output Failure” on page 28.</td>
</tr>
</tbody>
</table>
**Video Output Failure**

To check for a video output failure:

**Note** – This procedure is valid for primary graphics card only.

1. **Check that the power cord is connected to the monitor and to the power supply outlet.**
2. **Verify that the wall outlet is supplying AC power to the monitor and the power supply.**
   Use a VOM (volt-ohm-milliammeter).
3. **Verify that the video cable connection is secure between the monitor and the system video output port.**
   Use a VOM to perform the continuity test on the video cable.
4. **If the power cord and video cable connections are good and no video output occurs, reseat the video card for the monitor.**
   The video card may be installed in one of the four SBus slots or in a memory slot if it is a VSIMM. VSIMMs can be installed in one or two of the memory slots:
   - Slot J0304 is the default slot for the VSIMM in SPARCstation 20 systems with the SX graphics option. This enables video output on the monitor connector on the system back panel.
   - Slot J0305 may be used for a DSIMM, VSIMM, or NVSIMM in SPARCstation 20 systems. An auxiliary video board (AVB) must be installed to enable video output from a VSIMM in slot J0305.

---

**TABLE 4-1** Troubleshooting Tips

<table>
<thead>
<tr>
<th>System Problem</th>
<th>Corrective Action</th>
</tr>
</thead>
<tbody>
<tr>
<td>Read, write, or parity error reported by the operating system or applications</td>
<td>Replace the disk drive indicated by the failure message. The operating system identifies the bottom drive (SCSI address 3) as sd0 or c0t3d0. The top drive (SCSI address 1) is identified as sd1 or c0t1d0.</td>
</tr>
<tr>
<td>Power on does not succeed; keyboard key LEDs do not light and there is no tone from the keyboard when system power is applied</td>
<td>The power supply may be defective. See “Power Supply Test” on page 29.</td>
</tr>
<tr>
<td>Power on does not succeed; the system fails to initialize but the keyboard key LEDs light and there is a tone from the keyboard</td>
<td>The system board may be defective. See “System Board Test” on page 31.</td>
</tr>
</tbody>
</table>
5. If the monitor power supply is internally fused, check the fuse.

Power-On Does Not Succeed

To check for a power-on failure:

1. Remove the DC power from the system by pushing the back panel power switch to the standby position.
2. Check that the power cord is properly connected to the system back panel power receptacle and to the wall outlet.

Caution – This equipment contains lethal voltage. Accidental contact can result in serious injury or death.

3. Using a VOM, verify that the wall outlet is supplying AC power to the system.
4. Restore DC power to the system. Press the back panel power switch to ON and observe the keyboard.

The keyboard LEDs should briefly light. An audible tone from the keyboard should be heard.

- If no tone is heard or if no lights are seen, the power supply may be defective. See “Power Supply Test” on page 29.
- If a tone is heard and lights are seen, but the system still fails to initialize, see “System Board Test” on page 31.

Power Supply Test

To test the power supply:

1. Using a VOM, check the output voltages of the power supply (FIGURE 4-1, FIGURE 4-2, and TABLE 4-2).

Place the VOM negative probe on one of the logic ground pins in the connector and test the +12V, –12V, and +5V power pins individually with the positive probe (pins 1 through 8).

Note – The power supply connector must remain connected to the system board during this test. This allows the power supply to regulate the voltage.
2. Replace the power supply assembly if any of the voltages are not present.
See “Power Supply” on page 55.

**FIGURE 4-1** Power Supply Connector Pinouts
Note – All voltages are correct if they are within the +5% or –5% range.

System Board Test

To test the system board:
1. Connect a terminal to serial port A or use a tip connection to another workstation to receive additional POST failure information. See “Setting Up a tip Connection” on page 19.

2. Press and hold the keyboard Stop (L1) and D keys. While holding the keys down, turn the system power switch on. Release the keys. Watch the keyboard LEDs.
   - The Caps Lock key on the keyboard should flash, indicating that the system is executing the POST.
   - If the Caps Lock key fails to flash after the keyboard Stop (L1) and D keys have pressed and held, POST failed. MBus module 0 or the system board may be defective.
   - To further troubleshoot the system board, see “Caps Lock Key Fails to Flash During the POST” on page 32.

3. Observe the keyboard LEDs. If a failure occurs during POST, an LED may light up. TABLE 3-1 in Chapter 3 describes types of problems that occur when an LED on the keyboard lights up.

4. Observe serial port A diagnostic information. This may provide additional failure information.

5. Replace the defective part indicated.

Caps Lock Key Fails to Flash During the POST

If the Caps Lock key fails to flash during the POST:

1. Remove optional SBus cards and all NVSIMMs, VSIMMs, and DSIMMs (except J0201, which is required).

2. Initiate POST again with only: an MBus module in slot 0, a DSIMM in slot J0201, the power supply assembly, and the keyboard installed.

3. Observe serial port A diagnostic information. This may provide additional failure information.

4. Replace the defective part indicated.

5. If POST passes with the optional parts removed, install the optional parts one at a time. Initiate POST after each optional part is installed; observe the diagnostic output on serial port A and the keyboard LEDs.
Disk Drive Errors

The following procedure verifies the built-in SCSI controller. TABLE 4-3 lists disk drive error conditions and corrective actions.

To test the disk drive:

**Note** – To verify if the built-in SCSI controller is defective, test the disk drive response to the `probe-scsi` command. To test additional SCSI host adapters added to the system, use the `probe-scsi-all` command. Refer to “probe-scsi, probe-scsi-all” on page 226.

1. **At the ok prompt, type** `probe-scsi`.
   
   See the following example.

   ```
   ok probe-scsi
   Target 1
     Unit 0 Disk  SEAGATE ST31200W SUN1.05802200015063
     Copyright (c) 1993 Seagate
     All rights reserved 0000
   Target 3
     Unit 0 Disk  CONNER  CP30548  SUN0535B0BB93086K4S
   Target 6
     Unit 0 Removable Read Only device  TOSHIBA CD-ROM XM4101TA289310/16/93
   ```

   **Note** – If the disk drive responds and a message is displayed, the system SCSI controller has successfully probed the devices. This indicates that the system board is working correctly.

2. **If a disk does not respond:**

   a. **Ensure that every SCSI disk and other SCSI devices on the SCSI bus have a different SCSI address.**

   **Note** – Internal SCSI drives are automatically assigned SCSI address 3 (bottom drive) and 1 (top drive). SCSI address 6 is normally reserved for the optional internal CD-ROM drive. Do not assign SCSI address 7 to any device. This address is reserved for the SCSI controller on the system board.
b. If external devices are connected, verify that all SCSI cables and the SCSI terminator are connected securely. The terminator must be connected to the last physical device in the SCSI chain.

c. Check the SCSI cables to ensure there are no bent pins.

d. If after checking the previous causes, the problem is still not identified, replace the disk drive.

e. If the problem still occurs after replacing the drive, replace the system board.

f. If replacing the disk drive and the system board does not correct the problem, replace the internal disk drive cable assembly.

**TABLE 4-3  Troubleshooting Disk Drive Errors**

<table>
<thead>
<tr>
<th>Symptom</th>
<th>Corrective Action</th>
</tr>
</thead>
<tbody>
<tr>
<td>Read, write, or parity error reported by the operating system or applications</td>
<td>Replace the disk drive indicated by the failure message. The operating system identifies the Bottom drive (SCSI address 3) as <code>sd0</code> or <code>c0t3d0</code> and the top drive (SCSI address 1) as <code>sd1</code> or <code>c0t1d0</code>. SCSI controller or disk may be defective. See “Power-On Does Not Succeed” on page 29 or Chapter 9.</td>
</tr>
<tr>
<td>Drive fails to boot or does not respond to commands</td>
<td>The SCSI terminator is missing (if external devices are connected), or the drive is defective. Make sure that each SCSI device on the SCSI bus is assigned a unique address. Do not use SCSI address 7 for any device because it is reserved for the SCSI controller.</td>
</tr>
<tr>
<td>Slow disk drive response</td>
<td>If several SCSI devices are connected to the same SCSI bus and if some of these devices are “fast SCSI” devices, such as the external 1.05-Gbyte disk drive, install an FSBE/S SCSI host adapter card to the “fast SCSI” devices and put these “fast SCSI” devices on a separate SCSI bus to accelerate disk drive response.</td>
</tr>
</tbody>
</table>
SIMM Errors

If the operating system, diagnostic program, or POST does not display a SIMM location ("J" number) as part of the memory error message and the only available information is a physical memory address, use TABLE 4-4 to locate the failing SIMM.

<table>
<thead>
<tr>
<th>VSIMM or NVSIMM Slot Location</th>
<th>DSIMM Slot Location</th>
<th>Physical Memory Address Start</th>
<th>Physical Memory Address End</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>J0201 (bank 0)</td>
<td>0000 0000(16)</td>
<td>03ff ffff(16)</td>
</tr>
<tr>
<td></td>
<td>J0202 (bank 2)</td>
<td>0800 0000(16)</td>
<td>0bff ffff(16)</td>
</tr>
<tr>
<td></td>
<td>J0203 (bank 5)</td>
<td>1400 0000(16)</td>
<td>17ff ffff(16)</td>
</tr>
<tr>
<td></td>
<td>J0301 (bank 3)</td>
<td>0c00 0000(16)</td>
<td>0fff ffff(16)</td>
</tr>
<tr>
<td></td>
<td>J0302 (bank 6)</td>
<td>1800 0000(16)</td>
<td>1bff ffff(16)</td>
</tr>
<tr>
<td></td>
<td>J0303 (bank 1)</td>
<td>0400 0000(16)</td>
<td>07ff ffff(16)</td>
</tr>
<tr>
<td>VSIMM0, J0407</td>
<td>J0304 (bank 7)</td>
<td>1c00 0000(16)</td>
<td>1fff ffff(16)</td>
</tr>
<tr>
<td>VSIMM1 or NVSIMM, J0406</td>
<td>J0305 (bank 4)</td>
<td>1000 0000(16)</td>
<td>13ff ffff(16)</td>
</tr>
</tbody>
</table>

FIGURE 4-3 shows the SIMM locations in the system.

**Note** – The banks listed in TABLE 4-4 do not correspond to the physical order of the DSIMM slots on the system board. The physical order of the slots is based upon an alternating bank pattern: bank 0, bank 2, bank 5, bank 3, bank 6, bank 1, bank 7, and bank 4.

Each bank allows enough address space to accommodate a 64-Mbyte DSIMM. If a 16-Mbyte or 32-Mbyte DSIMM is installed in a bank, the unused addresses are “mapped out” by the memory management hardware. The physical memory starting address of any capacity DSIMM installed will always be as listed in TABLE 4-4. TABLE 4-4 lists SIMM slot layout and installation sequence.
FIGURE 4-3  System Board SIMM Slot Locations

**Note** – The order of the banks listed in TABLE 4-4 are not the same as the physical order of the DSIMM slots on the system board. TABLE 4-5 shows the physical order of the slots. A DSIMM must always be installed in J0201 (DSIMM0 slot). The first VSIMM is always installed in J0304 (DSIMM6 slot). If a second VSIMM is installed in J0305 (DSIMM7 slot), an Auxiliary Video Board (AVB) must be installed on the system board. Only one NVSIMM is supported. If used, it is installed in J0305 (DSIMM7 slot).

**TABLE 4-5**  SIMM Slot Layout and Installation Sequence

<table>
<thead>
<tr>
<th>VSIMMs and/or NVSIMM</th>
<th>Installation Sequence</th>
<th>Slot Layout</th>
</tr>
</thead>
<tbody>
<tr>
<td>DSIMM</td>
<td>J0201</td>
<td>DSIMM0</td>
</tr>
<tr>
<td>DSIMM</td>
<td>J0202</td>
<td>DSIMM1</td>
</tr>
<tr>
<td>DSIMM</td>
<td>J0203</td>
<td>DSIMM2</td>
</tr>
<tr>
<td>DSIMM</td>
<td>J0301</td>
<td>DSIMM3</td>
</tr>
<tr>
<td>DSIMM</td>
<td>J0302</td>
<td>DSIMM4</td>
</tr>
<tr>
<td>DSIMM</td>
<td>J0303</td>
<td>DSIMM5</td>
</tr>
<tr>
<td>J0407(VSIMM0)</td>
<td>J0304(default)</td>
<td>(DSIMM6)</td>
</tr>
<tr>
<td>J0406(VSIMM1 or NVSIMM)</td>
<td>J0305</td>
<td>(DSIMM7)</td>
</tr>
</tbody>
</table>

*J0304 can be used for a VSIMM or DSIMM
**J0305 can be used for a DSIMM, VSIMM, or NVSIMM
**Note** – A DSIMM must be installed in J0201 for the system to function properly.
Safety and Tools Requirements

This chapter provides a description of safety requirements, symbols, safety precautions, and required tools.

Safety Requirements

For your protection, observe the following safety requirements:

- Follow all cautions, warnings, and instructions marked on the equipment.
- Ensure that the voltage and frequency rating of the power outlet to be used matches the electrical rating labels on the system.
- Use properly grounded power outlets.

To protect both yourself and the equipment, observe the safety precautions listed in TABLE 5-1.
### Symbols

The following symbols are used throughout this manual:

<table>
<thead>
<tr>
<th>Item</th>
<th>Problem</th>
<th>Precaution</th>
</tr>
</thead>
<tbody>
<tr>
<td>AC power cord</td>
<td>Electric shock</td>
<td>Unplug the AC cord from the AC wall socket before working on the power supply.</td>
</tr>
<tr>
<td>Grounding</td>
<td></td>
<td>Leave the AC power cord plugged into the AC wall outlet when replacing drives, SIMMs, chips, or the system board. Leaving the AC power cord plugged into the wall outlet provides a grounding path for the wrist strap, which you must wear while servicing the system.</td>
</tr>
<tr>
<td>Power-On/ Standby switch</td>
<td></td>
<td>Remove DC power from the system by pressing the Standby switch before unplugging a working unit or connecting a replacement unit.</td>
</tr>
<tr>
<td>Wrist strap</td>
<td>ESD</td>
<td>Wear a grounded wrist strap when handling printed circuit boards, drives, or other components such as SIMMs.</td>
</tr>
<tr>
<td>ESD mat</td>
<td></td>
<td>An approved antistatic mat, when used with a wrist strap, provides protection from static damage.</td>
</tr>
<tr>
<td>Printed circuit boards</td>
<td>ESD</td>
<td>Handle a printed circuit board by the edges only. Store a board in an antistatic bag.</td>
</tr>
<tr>
<td>Cover</td>
<td>System damage and overheating</td>
<td>Replace the cover after performing service on the system.</td>
</tr>
<tr>
<td>SBus and MBus slot filler panels</td>
<td>System damage and overheating</td>
<td>Install filler panels in all unused SBus and MBus slots. Openings on the back panel reduce the cooling capability of the system.</td>
</tr>
<tr>
<td>Heat sinks on MBus module, and system board</td>
<td>Heat, burns</td>
<td>Do not touch the metal heat sinks on the MBus module or the system board. The heat sinks can be hot enough to cause personal injury.</td>
</tr>
</tbody>
</table>
System Precautions

Observe all safety precautions and ensure compliance with skill level requirements, certification, and all applicable local and national laws.

Procedures contained in this document must be performed by trained maintenance providers. Only people who have been trained by the Sun Microsystems training facilities (or by Sun Microsystems affiliates) and have been certified as required by local and national laws are considered qualified.

Caution – Before beginning a service task, carefully read each of the procedures in this manual. If similar operations on comparable equipment have not been performed, do not attempt to perform these procedures.
Lithium Battery

**Caution** – On SPARCstation 20 system boards, there is a lithium battery molded into the real-time clock, SGS No. MK48T18. Batteries are not customer replaceable parts. They may explode if mistreated. Do not dispose of the battery in fire. Do not disassemble it or attempt to recharge it.

Required Tools

You will need the following tools and equipment:

- Antistatic mat
- Wrist strap
- Long needlenose pliers
- Phillips screwdriver
- Flat-blade screwdriver
- SIMM installation tool
- Conductive foam to store chips
- DIP/IC extraction tool
- Fan installation tool
- Card extractor

Electrostatic Discharge (ESD) Precautions

**Caution** – The system chassis power must be set to standby, and the AC power cord must remain plugged in to ensure a proper ground.

To minimize electrostatic discharge, observe the following precautions:

- Hold the system board, SBus cards, MBus modules, SIMMs, or system components only by the edges.
■ When removing a board, card, or module from an antistatic bag, lay it on an antistatic surface such as a Sun ESD mat, an antistatic bag, or a disposable antistatic mat.

■ Transport boards, cards, or modules in an antistatic bag.

■ Always wear an antistatic wrist strap connected to a metal surface on the chassis when working on system components and parts.

■ Do not place the boards, cards, or modules on an unprotected surface. Use a cushioned antistatic mat or antistatic bag. Connectors and components have very thin pins that bend easily.

■ Do not use an oscilloscope or VOM (Volt/Ohm meter) probe on the components. The soldered pins can be damaged or shorted by the probe point.
Power On and Off

Removing Power from the System

To remove power from the system:

Caution – Failure to terminate the operating system can cause data loss.

Note – Before turning off the system power, terminate the operating system.

1. Notify users that the system is going down (if networked system).
2. Back up the system files and data.
3. Shut down the system.
4. Ensure that the power is turned off to the monitor and all peripherals connected to the system.
5. Remove DC power from the system by pressing the power-on/standby switch on the back panel (FIGURE 6-1).
Caution – When the power-on/standby switch is pressed and the AC power cord remains connected to a power receptacle, hazardous AC voltage is present in the power supply primary. When power is removed from the power supply, the power supply automatically returns to that state when power is restored, even if the system power-on/standby switch or the keyboard power-on key are not touched. Before servicing the power supply, press the standby switch prior to disconnecting the AC power cord.

Note – The AC power cord provides the grounding path to safely discharge static electricity. Take note where each cord and cable attaches to the system unit.

Restarting the System While Troubleshooting

When troubleshooting a problem and the POST power-on messages need to be viewed:

1. Connect a terminal to serial port A or set up a tip connection to another workstation.
   If necessary, see “Setting Up a tip Connection” on page 19.

2. Turn the power on as follows:
   a. SCSI peripheral farthest from the system (peripheral with the SCSI terminator attached)
   b. SCSI peripheral closest to the system (peripheral directly connected to the system)
c. Other peripherals, such as printers
d. Terminal (or graphics monitor if tip connection is used)
e. System unit (CPU)

3. Initiate the POST diagnostic mode by pressing and holding the keyboard Stop (L1) and D keys.

4. Watch the screen for any POST error messages.

At the conclusion of testing, the OpenBoot PROM automatically configures the system, omitting any devices that have failed diagnostics. After POST ends, the system boots using the new configuration.

**Note** – POST does not test drives or SBus cards. To test these devices, run OpenBoot PROM diagnostics before the system has booted. Appendix E, “Selected On-Board Diagnostics,” describes using the `test` command to test SBus cards and drives. Also refer to *OpenBoot™ 2.x Command Reference Manual*.

---

**Reading Boot Messages**

After POST completes the system self-test, a message similar to the following is displayed, listing hardware detected in the system.

```
SPARCstation 20 Series (2 X XXXXXX)
ROM Rev. ----, --- MB Memory installed, Serial #----.
Ethernet address ----:----:----:----:----:----, Host ID: ------.
```

---

**Turning On the Power**

1. Turn on the power as follows:
   a. External SCSI peripheral with the SCSI terminator attached
   b. External SCSI peripheral daisy-chained to the SCSI peripheral with the SCSI terminator attached
c. SCSI peripherals ending with the SCSI peripheral directly connected to the computer system

d. Monitor

e. Power on/Standby switch on the system back panel (FIGURE 6-2).

2. The operating system should load or the ok prompt should appear on the monitor.
Internal Access

This chapter explains how to remove the cover, attach the wrist strap, and replace the cover.

Removing the Cover

To remove the cover:

1. **Remove power from the system.**
   See “Removing Power from the System” on page 45

2. **Loosen the screw on the corner of the back panel** (FIGURE 7-1).

   **Note** – Do not loosen the screw securing the power supply to the rear panel.

3. **Loosen the screw securing the lock block to the cover. Remove the lock block and place it aside** (FIGURE 7-1).
4. Grasping the rear of the cover, lift the cover up and toward the system front. When the cover is at a 60-degree angle to the chassis, push the cover away from the front panel to free the cover hinge tabs from the chassis slots.

5. Put the cover aside (FIGURE 7-2).

![Rear of cover]

**FIGURE 7-2** Removing the Cover

---

**Attaching the Wrist Strap**

To attach the wrist strap:

---

**Caution** – Wear an antistatic wrist strap and use an ESD-protected mat when handling components. When removing components other than the power supply, leave the power cord connected to both the system and the wall receptacle. When removing the power supply, attach the copper end of the wrist strap to the system chassis. Store ESD-sensitive components in antistatic bags before placing it on any surface.

1. Unwrap the first two folds of the wrist strap. Wrap the adhesive side firmly around the wrist (FIGURE 7-3).

2. At the opposite end of the wrist strap, peel the liner from the copper foil. Attach the copper foil to the power supply metal casing.
Replacing the Cover

To replace the cover:

1. Remove the wrist strap.
2. Grasp the cover by the front and rear edges (FIGURE 7-4).
3. Align the cover hinge tabs with the chassis tab slots on the bottom front of the system. Ensure that the tabs are locked into the chassis slots.

4. Lower the cover onto the chassis (FIGURE 7-4).

5. Replace the lock block (FIGURE 7-5).

FIGURE 7-4  Replacing the Cover

FIGURE 7-5  Lock Block
Caution – Do not use a power driver to tighten the back panel screws. Do not overtighten the back panel screws.

6. Tighten the screw on the back panel corner and secure the cover (FIGURE 7-6).

![Corner screw](image)

FIGURE 7-6 Back Panel Corner Screw
Removing and Installing Subassemblies

This chapter contains procedures for removing and installing the major subassemblies of the system unit. The following is the list of the major subassemblies:

Power Supply

To remove and install the power supply, proceed as follows.

Removing the Power Supply

**Caution** – To avoid system board or component damage, press the standby side of the power-on/standby switch before disconnecting the power cord from the back panel. When the power supply power is restored, the power supply automatically returns to the previous power state.

1. Remove power from the system.
   See “Removing Power from the System” on page 45.

**Caution** – The AC power cord must be disconnected to replace the power supply. Do not reconnect the AC power cord while the power supply is outside of the chassis.
2. Disconnect the power cord from the back of the system unit and from the wall receptacle.

**Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

3. Remove the cover and attach a wrist strap.

See Chapter 7.

**Note** – The wrist strap is usually grounded to the power supply. When removing or installing the power supply, ground the wrist strap to the metal edge of the system back panel.

4. Remove the power supply as follows (FIGURE 8-1):

   a. Loosen the back panel captive screws that secure the power supply to the chassis.

   **Note** – Some systems use only one captive screw to secure the power supply.

   b. Disconnect the DC power connector from the system board.

   c. Slide the power supply toward the drive bracket and free of the chassis mounts.

   d. Lift the power supply from the chassis.
5. Disconnect the DC power harness from the rear of the power supply (FIGURE 8-2).

6. Set the power supply aside.
Installing the Power Supply

Caution – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

1. Grasp the power supply with one hand; with the other hand, connect the DC power harness to the rear of the power supply (FIGURE 8-2).

2. Lower the power supply into the chassis at an angle such so the DC power harness fits under the edge of the chassis drive bracket.

3. Slide the power supply toward the rear of the chassis until it locks in the power supply chassis mounts.

Caution – Do not use a power driver to tighten the captive screws. Do not overtighten the captive screws.

4. Tighten the captive screw(s) that secure the power supply to the chassis (FIGURE 8-3).

Note – Some systems use only one captive screw to secure the power supply.
5. Connect the DC power connector to the system board (FIGURE 8-4).

6. Remove the wrist strap and replace the cover.
   See Chapter 7.

7. Connect the power cord to the rear of the system unit and the wall receptacle.

8. Power on the system.
   See “Turning On the Power” on page 47.
LED

To remove and install the LED, proceed as follows.

Removing the LED

**Note** – The LED is attached to one end of the speaker/LED cable by an in-line connector.

1. **Remove power from the system.**
   See “Removing Power from the System” on page 45.
2. **Remove the cover and attach a wrist strap.**
   See Chapter 7.

**Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

3. **Locate the LED at the shrink-wrapped end of the LED cable and the in-line connector (FIGURE 8-5).**
Note – Three tabs on the LED light pipe secure the LED to the right front corner of the chassis (FIGURE 8-6).

4. Use the tip of a small screwdriver to work the cable and LED free from the light pipe tabs (FIGURE 8-6).

5. Disconnect the shrink-wrapped section of the LED cable from the in-line connector (FIGURE 8-7).
FIGURE 8-6  LED Light Pipe Detail
Installing the LED

**Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

1. Position the LED near the tabs and light pipe at the corner of the system (FIGURE 8-6).

2. Use the tip of a small flatblade screwdriver to work the shrink-wrapped LED wires through the tabs (FIGURE 8-6).

3. Route the LED wires along the top front edge of the chassis and connect them to the Speaker/LED in-line connector (FIGURE 8-7).
4. Detach the wrist strap and replace the cover.
   See Chapter 7.

5. Power on the system.
   See “Turning On the Power” on page 47.

---

Speaker

To remove and install the internal speaker, proceed as follows.

Removing the Speaker

1. Remove power from the system.
   See “Removing Power from the System” on page 45.

2. Remove the cover and attach a wrist strap.
   See Chapter 7.

   **Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

   **Caution** – Avoid damaging the speaker surface; use care when working near or handling the speaker. Do not touch the speaker diaphragm with tools or fingers. Grasp the speaker by the metal edges.

3. Remove the triangular-shaped speaker cover from the unit front as follows:

   **Note** – The speaker cover is secured by three tabs; one tab is on the cover top and two tabs are on the cover bottom (see FIGURE 8-8).

   a. Insert the tip of a small flat-blade screwdriver between the speaker cover and the chassis. Press the top cover tab outward slightly to release it from the chassis slot.
b. Insert the screwdriver tip in one of the slots on the bottom of the speaker cover and press the lower cover tab up to release it from the chassis slot. Repeat this step to release the second lower cover tab.

4. Remove the two screws that secure the speaker to the chassis. Set the screws aside (FIGURE 8-8).

5. Grasp the metal speaker rim and lift the speaker slightly to free it from the chassis.

6. Grasping the speaker in one hand, disconnect the spade connectors from the speaker terminals on the back of the speaker (FIGURE 8-9).
Installing the Speaker

**Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

**Caution** – Avoid damaging the speaker surface; use care when working near or handling the speaker. Do not touch the speaker diaphragm with tools or fingers. Grasp the speaker by the metal edges.

1. **Slide the spade connectors onto the speaker terminals** (FIGURE 8-9).

   **Note** – Connectors and terminals are keyed to ensure proper connection.

2. Install the speaker in the opening on the system front.

3. **Replace the two screws that secure the speaker to the chassis and proceed as follows** (FIGURE 8-8):
a. Align the tabs on the speaker cover with the slots in the chassis.
b. Press the speaker cover against the chassis; snap the speaker cover in place.

4. Detach the wrist strap and replace the cover.
   See Chapter 7.

5. **Power on the system.**
   See “Turning On the Power” on page 47.

---

**Fan Assembly**

The proceeding paragraphs provide instructions for removing and installing the fan and the fan DC power harness. To remove and install the fan or the fan DC power harness, proceed as follows.

---

**Note** – If installed, the CD-ROM drive must be removed *before* the fan assembly can be removed or installed (see “Removing a CD-ROM Drive” on page 123).

---

**Removing the Fan**

1. **Remove power from the system.**
   See “Removing Power from the System” on page 45.

2. **Remove the cover and attach a wrist strap.**
   See Chapter 7.

---

**Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

---

3. **Disconnect the fan DC power connector** *(FIGURE 8-10).*
4. **Detach the fan DC power cable free from beneath the metal cable restraint.**
5. **Remove the fan as follows:**
   a. Grasp the fan housing firmly.
b. Place the tip of a small flat-blade screwdriver under the fan side mounting clip and pry it up to free the fan clip from the chassis metal drive bracket.

c. While prying the clip up, pull the fan housing toward the center of the system to free the bottom clip from the chassis (FIGURE 8-11).

d. Set the fan aside.

FIGURE 8-10 Disconnecting the Fan DC Power Connector

Note: Cable restraints and SCSI data cables are not illustrated.
Caution – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.
Note – Some units have built-in fan DC power cables in the system DC power harness and the separate fan DC harness is not used. Systems having separate fan DC power harness must have the fan DC power harness connected to the CD-ROM drive DC power connector.

1. Insert the fan bottom mounting clip into the opening at the bottom of the system chassis. Place the side mounting clip over the edge of the chassis (FIGURE 8-12).

Note – Position the fan assembly so that it does not interfere with the removal of the system hard disk drives.

FIGURE 8-12 Fan Assembly

2. Push the fan side mounting clip against the vertical edge of the chassis (FIGURE 8-13).
3. Place the notched tip of the fan installation tool over the side mounting clip and press down (FIGURE 8-14).
4. Connect the fan assembly power connector to the two-pin fan DC power connector (FIGURE 8-15).

**Note** — Connectors and terminals are keyed to ensure proper connection.

5. Secure the fan DC harness beneath the cable restraint (FIGURE 8-15).

**Note** — Store any excess cabling under the cable restraint.

6. If a CD-ROM drive was removed, install it now.
   See “Installing a CD-ROM Drive” on page 124.
7. **Detach the wrist strap and replace the cover.**
   See Chapter 7.

8. **Power on the system**
   See Section “Turning On the Power” on page 6-47.

---

**FIGURE 8-15** Connecting the Fan Assembly DC Power Connector

Note: Cable restraints and SCSI data cables are not illustrated.
Removing the Fan DC Power Harness

**Note** – Some units have built-in fan DC power cables in the system DC power harness and the separate fan DC harness is not used. For systems having the fan DC connector built into the system DC power harness, see “DC Power Harness” on page 97. If the system has a separate fan DC power harness, the fan DC power harness must be removed from the CD-ROM drive DC power connector. The separate fan DC power harness has a two-wire fan DC power connector, a CD-ROM drive DC power connector, and a power connector for the optional CD-ROM drive connector (FIGURE 8-17).
1. **Remove power from the system.**
   See “Removing Power from the System” on page 45.

2. **Remove the cover and attach a wrist strap.**
   See Chapter 7.

   ![Caution - Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.]

3. **Detach the fan DC power harness from under the cable restraint near the CD-ROM drive data cable** (FIGURE 8-18).
4. Disconnect the fan power connector from the DC power connector (FIGURE 8-19).
5. Lift the excess fan DC cable from the cable trough along the front of the chassis; remove the fan DC power harness (FIGURE 8-20).

6. Disconnect the fan DC power connector from the CD-ROM drive power connector (FIGURE 8-21).
Note: Cable restraints and SCSI data cables are not illustrated.

FIGURE 8-20  Lifting Fan DC Cable from the Cable Trough
Installing the Fan DC Power Harness

**Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

1. Locate the CD-ROM drive DC power connector (FIGURE 8-22).
2. Connect the fan DC power connector to the CD-ROM drive DC power connector (FIGURE 8-23).

   **Note** – Connectors and terminals are keyed to ensure proper connection.

3. Place the excess cable into the cable trough along the front of the chassis (FIGURE 8-24).

   **Note** – Be sure an adequate amount of two-wire fan DC cable is exposed to allow fan connection.
FIGURE 8-23 Connecting the Fan DC Power Connector

Note: Cable restraints and SCSI data cables are not illustrated.
4. Connect the two-pin power connector to the fan assembly DC power connector (FIGURE 8-25).

**Note** – Connectors and terminals are keyed to ensure proper connection.

5. Detach the wrist strap and replace the cover.
   See Chapter 7.

6. Power on the system.
   See “Turning On the Power” on page 47.
To remove and install the SCSI backplane, proceed as follows.
Removing the SCSI Backplane

**Note** – The SCSI backplane is a printed circuit board located in the drive bracket area of the chassis. A bracket and two screws secure the SCSI backplane to the vented sheetmetal chassis. One or two single-connector hard drives plug into the SCSI backplane. The SCSI single-connector drive sockets provide SCSI data, SCSI addressing, and power.

1. **Remove power from the system.**
   See “Removing Power from the System” on page 45.

2. **Remove the cover and attach a wrist strap.**
   See Chapter 7.

   **Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

3. **Remove the hard disk drive(s).**
   See “Removing a Hard Disk Drive” on page 117.

4. **Disconnect the SCSI backplane sockets and connectors as follows (FIGURE 8-26):**
   a. Press the clip on the DC power connector.
   b. Disconnect the DC power connector from the SCSI backplane socket.
   c. Disconnect the DC power connector clip from the DC power socket.
   d. Disconnect the SCSI data connector from the SCSI data socket.
5. Remove the two Phillips-head screws that secure the SCSI backplane to the plastic mounting bracket (FIGURE 8-27).

**Note** – The DC power socket and the SCSI data socket are located on the right side of the SCSI backplane. This part of the SCSI backplane extends through a slot in the drive bracket.

6. Grasp the left edge of the SCSI backplane and rotate it away from the chassis.

7. Pull the end of the backplane with the SCSI data socket and the DC power socket out through the drive bracket area.

8. Remove the SCSI backplane from the chassis.
9. Remove the SCSI backplane bracket as follows (FIGURE 8-28):

a. Insert long needlenose pliers or a flat-blade screwdriver between the SCSI backplane bracket and the sheetmetal chassis.

Note – If there is difficulty in accessing the bottom plastic tab with needlenose pliers, use a flat-blade screwdriver to push the tabs toward the chassis corner.

b. Squeeze the plastic bracket tabs together and release the SCSI backplane bracket from the slots that secure it to the sheetmetal chassis.
Installing the SCSI Backplane

**Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

1. Replace the SCSI backplane bracket as follows (FIGURE 8-27):
   
   a. Position the plastic SCSI backplane bracket so that the bracket tabs face the vented sheetmetal chassis.

   b. Slide the right side of the SCSI backplane through the sheetmetal slot in the drive bracket.
Note – The two plastic legs on the right side of the SCSI backplane fit into the chassis sheetmetal slots.

c. Align the tabs on the left side of the SCSI backplane bracket with the sheetmetal slots near the chassis corner.
d. Firmly press the tabs into the chassis slots.

Note – Check to ensure that the bracket assembly snaps securely into place and that the two legs are in the chassis sheetmetal slots.

2. Connect the SCSI data connector and the DC power connector clip into the SCSI data socket and the DC power socket, respectively (FIGURE 8-26).

3. Replace the hard disk drive(s).
   See “Installing a Hard Disk Drive” on page 120.

4. Detach the wrist strap and replace the cover.
   See Chapter 7.

5. Power on the system.
   See “Turning On the Power” on page 47.

SCSI Data Cable

The SPARCstation 20 system is configured with one of two chassis types:
- A chassis that supports the SunCD 2Plus drive (1-inch drive)
- A chassis that supports the SunCD 4 drive (1.6-inch drive)

Removing and installing the SCSI data cable varies depending on the chassis. The following sections provide SCSI data cable removal and installation for each chassis type.

Removing the SCSI Data Cable
(SunCD 2Plus Drive-Type Chassis)

1. Remove power from the system.
   See “Removing Power from the System” on page 45.
2. Remove the cover and attach a wrist strap.

   See Chapter 7.

   \textbf{Caution} – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

3. Disconnect the following cables from the system board (FIGURE 8-29):
   - Diskette data cable (optional)
   - SCSI data cable
   - DC power connector
   - Speaker/LED cable

   \textbf{Note:} Figure illustrates SunCD 2Plus drive-type (1-inch drive) chassis only.

   \textbf{FIGURE 8-29} Internal Cables and DC Power Connector (SunCD 2Plus Drive-Type Chassis)
4. Remove any SBus cards.
   See “Removing an SBus Card” on page 152.

5. Loosen the two captive screws on the back panel that secure the system board to the chassis (FIGURE 8-30).

6. Press the plastic connector clip and disconnect the DC power connector from the SCSI backplane (FIGURE 8-31).

7. Slide the system board back slightly to access the internal cables.
8. Remove the SunCD 2Plus CD-ROM drive, if installed.
   See “Removing a CD-ROM Drive” on page 123.

9. Disconnect the SCSI data connector (P3) from the SCSI backplane.

10. Free the SCSI data cable from the metal cable guides on the chassis bottom and
    from the plastic hooks on the system board card guide.

11. Remove the SCSI data cable.

## Installing the SCSI Data Cable
(SunCD 2Plus Drive-Type Chassis)

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**Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

---

**Note** – Tape or tie-wrap markers on internal cables help in properly routing the cables through the metal cable guides. The part of each cable with a marker is to be visible between the metal cable guides on the chassis bottom.

---

1. **Route the SCSI data cable as follows** (FIGURE 8-32):
   a. Route the SCSI connectors (P3 and P1) through the drive bracket sheetmetal opening.

   b. Position the SCSI cable marker under the metal cable guide; route the SCSI data cable through the chassis metal guides and the system board guide plastic hooks.
2. Connect the following cables to the SCSI backplane (FIGURE 8-31):
   a. SCSI data connector (P3)
   b. DC power connector (P1).

3. Replace the SunCD 2Plus CD-ROM drive.
   See “Installing a CD-ROM Drive” on page 124.

4. Position the system board into place and tighten the two captive screws on the back panel that secure the system board to the chassis (FIGURE 8-34).

5. Replace any SBus cards (see FIGURE 8-37).
   See “Installing an SBus Card” on page 156.

6. Connect the following cables to the system board (FIGURE 8-40):
   - Speaker/LED cable
DC power connector
■ SCSI data cable
■ Diskette data cable (optional)

7. Detach the wrist strap and replace the cover.
   See Chapter 7.

8. Power on the system.
   See “Turning On the Power” on page 47.

Removing the SCSI Data Cable
(SunCD 4 Drive-Type Chassis)

1. Remove power from the system.
   See “Removing Power from the System” on page 45.

2. Remove the cover and attach a wrist strap.
   See Chapter 7.

   Caution — Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

3. Disconnect the following from the system board (FIGURE 8-33):
   ■ DC power cable
   ■ SCSI data cable
   ■ Diskette data cable
   ■ Speaker/LED cable
FIGURE 8-33 Internal Cables and DC Power Connector (SunCD 4 Drive-Type Chassis)

4. Loosen the two captive Phillips-head screws that secure the system board to the rear chassis (FIGURE 8-34).

5. Pull the system board from the back of the chassis until it clears the plastic card guide (FIGURE 8-34).

Caution – Place the system board on an antistatic surface.

6. Remove the SunCD 4 CD-ROM drive.
   See “Removing a CD-ROM Drive” on page 123.
7. Disconnect the following (FIGURE 8-33):
   a. The SCSI data cable from the metal cable clips on the chassis wall, and from the plastic cable clips on the card guide.
   b. The DC power harness from the SCSI backplane.
   c. The internal SCSI data cable from the SCSI backplane.

8. Remove the SCSI data cable.
Installing the SCSI Data Cable (SunCD 4 Drive-Type Chassis)

**Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

**Note** – The SCSI data cable is a three-connector pigtail cable with colored tape that joins two of the cables closest to the P2 connector. Visual markers are placed on the cables to aid cable alignment. When properly installed, the SCSI data cable P1 connector is connected to the SCSI backplane with the tape aligned between the card guide plastic cable clips (see FIGURE 8-38). The SCSI data cable P3 connector is connected to the system board and threaded through the chassis wall metal cable guides. The SCSI data cable P2 connector is routed through the gap between the hard disk drive and the chassis wall. P2 connects to the SunCD 4 drive (if installed), or is secured under the chassis floor metal cable clips.

1. Secure the SCSI data cable under the plastic card guide cable clips.

**Note** – The taped portion of the SCSI data cable should be positioned directly between the plastic card guide cable clips as shown in FIGURE 8-38.

2. Connect the SCSI data cable to the SCSI backplane.

3. Connect the DC power harness to the SCSI backplane.

4. Insert the front edge of the system board into the card guide. Lift the rear of the board slightly and slide it along the card guide until it is fully inserted (FIGURE 8-30).

5. Connect the DC power cable, SCSI data cable, diskette data cable, and speaker/LED cable to the system board.

**Note** – Thread the SCSI data cable through the chassis wall metal cable clips after the SCSI data cable is connected to the system board (FIGURE 8-33).

6. Replace the SBus card.

   See “Installing an SBus Card” on page 156.

7. Tighten the two captive Phillips-head screws that secure the system board to the chassis rear (FIGURE 8-30).
1. Detach the wrist strap and replace the cover.
   See Chapter 7.

2. **Power on the system.**
   See “Turning On the Power” on page 47.

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**DC Power Harness**

The SPARCstation 20 system is configured with one of two chassis types:

- A chassis that supports the SunCD 2Plus drive (1-inch drive)
- A chassis that supports the SunCD 4 drive (1.6-inch drive)

Removing and installing the DC power harness varies depending on the chassis. The following sections provide DC power harness removal and installation for each chassis type.

---

**Removing the DC Power Harness (SunCD 2Plus Drive-Type Chassis)**

1. **Remove power from the system.**
   See “Removing Power from the System” on page 45.

2. **Remove the cover.**
   See “Removing the Cover” on page 49.

---

**Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

3. Attach a wrist strap to the top metal edge of the system back panel **(FIGURE 8-35)**.
4. Remove any SBus cards.
   See “Removing an SBus Card” on page 152.

5. Disconnect the diskette data cable (optional), SCSI data cable, DC power connector, and the speaker/LED cable from the system board (FIGURE 8-36).
6. Loosen the two captive screws that secure the system board back panel to the chassis (FIGURE 8-37).

7. Slide the system board back slightly to access the cables.

8. Remove the power supply.
   See “Removing the Power Supply” on page 55.

9. Remove the CD-ROM drive.
   See “Removing a CD-ROM Drive” on page 123.
10. **Remove the diskette drive.**

   See “Removing a Diskette Drive” on page 127.

11. **Free the SCSI cable from the metal cable guides** (FIGURE 8-38).
12. Remove the DC power harness from the metal cable guides (FIGURE 8-39).
13. Disconnect the DC power connector (P3) from the SCSI backplane (FIGURE 8-39).
14. Remove the DC power harness from the chassis.
Installing the DC Power Harness (SunCD 2Plus Drive-Type Chassis)

**Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

**Note** – Tape or tie-wrap markers on internal cables help in properly routing the cables through the metal cable guides. The part of each cable with a marker is to be visible between the metal cable guides on the chassis bottom.

1. **Place the DC power harness and SCSI data cable as follows** (FIGURE 8-39):
   a. Route the end of the DC power harness with connectors P1 and P3 through the sheetmetal opening in the drive bracket.

   **Note** – Be sure the tie wrap markers are in the proper position beneath the metal cable guides.
b. Route the DC power harness through the metal cable guides.

c. Route the SCSI data cable through the metal cable guides.

d. Press the cable down between the drive bracket and the plastic card guide.

2. Connect P3 to the SCSI backplane (FIGURE 8-39).

3. Install the power supply.
   See “Installing the Power Supply” on page 58.

4. Install the diskette drive, if it was removed earlier.
   See “Installing a Diskette Drive” on page 132

5. Slide the system board completely into the chassis. Tighten the two captive screws that secure the system board back panel to the chassis (FIGURE 8-37).

6. Connect the diskette data cable, SCSI data cable, DC power connector, and speaker/LED cable to the system board (FIGURE 8-36).

7. Install any SBus cards.
   See “Installing an SBus Card” on page 156.

8. Install the CD-ROM drive.
   See “Installing a CD-ROM Drive” on page 124.

9. Detach the wrist strap and replace the cover.
   See Chapter 7.

10. Power on the system.
    See “Turning On the Power” on page 47.

Removing the DC Power Harness (SunCD 4 Drive-Type Chassis)

1. Remove power from the system.
   See “Removing Power from the System” on page 45.

2. Remove the cover and attach a wrist strap.
   See Chapter 7.

Caution – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.
3. Disconnect the DC power cable, SCSI data cable, diskette data cable, and speaker/LED cable from the system board (FIGURE 8-40).

4. Loosen the two captive Phillips-head screws that secure the system board to the chassis rear (FIGURE 8-41).

---

**FIGURE 8-40** Internal Cables and DC Power Connector (SunCD 4 Drive-Type Chassis)

**FIGURE 8-41** Back Panel Captive Screws
5. Pull the system board from the chassis back until the system board clears the plastic card guide (FIGURE 8-42).

![Card guide and System board diagram]

**FIGURE 8-42** Removing the System Board

**Caution** – Place the system board on an antistatic surface.

6. Remove the SunCD 4 drive.
   See “Removing a CD-ROM Drive” on page 123.

7. Disconnect the DC power harness from the diskette drive.

8. Remove the DC power harness from the SCSI backplane as follows (FIGURE 8-26):
   a. Free the SCSI data cable from the metal cable clips on the chassis wall, and from the card guide plastic cable clips.
   b. Disconnect the SCSI data cable from the SCSI backplane.
c. Remove the DC power harness (labeled P3) from the card guide clips and disconnect the DC power harness from the SCSI backplane.

9. Remove the power supply.
   See “Removing the Power Supply” on page 55.

10. Disconnect the DC power harness (labeled P1) from the power supply and carefully remove the harness from the system chassis.

Installing the DC Power Harness (SunCD 4 Drive-Type Chassis)

**Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

**Note** – Visual markers are placed on the DC power harnesses to aid cable alignment. When properly installed, the two colored tape marks on the P1 (power supply) harness should straddle the chassis wall metal cable clips that are nearest to the system board. The P2 (CD-ROM drive) harness should lie under the second chassis floor metal cable clip. The second colored tape mark (farthermost from the P3 connector) of the P3 (SCSI backplane) harness should be centered under the second plastic cable guide clip. The P4 (diskette drive) harness should be threaded through the chassis wall, with the diskette data cable, and located under the first metal chassis floor cable clip.

1. Place the power supply harness, SCSI backplane DC power harness, diskette drive DC power harness, CD-ROM DC power harness, and SCSI data cable as follows (FIGURE 8-29):

a. Route the power supply harness (labeled P1) through the metal cable clips on the chassis wall.

**Note** – The two colored tape marks on the DC power harness should straddle the cable clips.

b. Route the SCSI backplane DC power harness (labeled P3) under the plastic cable clips on the card guide to the SCSI backplane.

c. Thread the diskette drive DC power harness (labeled P4) through the hole in the chassis wall with the diskette data cable.
d. Locate the CD-ROM DC power harness (labeled P2) under the second metal cable clip on the chassis floor.

e. Route the SCSI data cable (labeled P1) between the plastic clips at the rear of the card guide. Connect SCSI data cable to the SCSI backplane.

f. Connect the DC power harness (labeled P3) to the SCSI backplane.

g. Connect the DC power harness (labeled P1) to the rear of the power supply.

2. Install the power supply.
   See “Installing the Power Supply” on page 58.

3. Connect the DC power harness (labeled P4) to the diskette drive.

4. Insert the front edge of the system board into the card guide, then lift the rear of the board slightly and slide it along the card guide until it is fully inserted (FIGURE 8-34).

5. Connect the DC power cable, diskette data cable, SCSI data cable, and speaker/LED cable to the system board (FIGURE 8-40).

   **Note** – Thread the SCSI data cable through the metal cable clips on the chassis wall after it is connected to the system board.

6. Install the SBus card.
   See “Installing an SBus Card” on page 156.

7. Tighten the two captive Phillips-head screws that secure the system board to the chassis rear (FIGURE 8-34).

8. Install the CD-ROM drive.
   See “Installing a CD-ROM Drive” on page 124.

9. Detach the wrist strap and replace the cover.
   See Chapter 7.

10. Power on the system.
    See “Turning On the Power” on page 47.

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**Diskette Drive Cable**

The SPARCstation 20 system is configured with one of two chassis types:
A chassis that supports the SunCD 2Plus drive (1-inch drive)
A chassis that supports the SunCD 4 drive (1.6-inch drive)

Removing and installing the diskette data cable varies depending on the chassis. The following sections provide diskette data cable removal and installation for each chassis type.

Removing the Diskette Drive Cable (SunCD 2Plus Drive-Type Chassis)

1. **Remove power from the system.**
   See “Removing Power from the System” on page 45.

2. **Remove the cover and attach a wrist strap.**
   See Chapter 7.

   **Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

3. **Remove the CD-ROM drive.**
   See “CD-ROM Drive” on page 123.

4. **Perform Step 1 through Step 3 in “Removing a Diskette Drive” on page 127.**

5. **Locate the diskette drive cable** (FIGURE 8-43).
6. Pull the plastic cable tab to disconnect the diskette drive cable from the system board (FIGURE 8-44).
7. Carefully pull the end of the diskette drive cable that connects to the system board through the sheetmetal opening into the drive bracket area (FIGURE 8-45).

8. Remove the diskette drive cable from the drive bracket area of the chassis.

FIGURE 8-45 Pulling the Diskette Drive Cable Through the Drive Bracket Opening

Installing the Diskette Drive Cable (SunCD 2Plus Drive-Type Chassis)

Caution – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.
Note – The replacement diskette drive cable should be folded at an angle to connect properly. See FIGURE 8-44.

1. Work the end of the diskette drive cable that connects to the system board through the sheetmetal opening in the drive bracket and connect it to the system board (FIGURE 8-45).

Note – Connectors and terminals are keyed to ensure proper connection.

2. Pick up the diskette drive and hold it above the drive area of the chassis.
3. Perform Step 1 through Step 2 of Section “Installing a Diskette Drive” on page 9-132.
4. Install the CD-ROM drive, if it was removed earlier.
   See Section “Installing a CD-ROM Drive” on page 9-124.
5. Remove the wrist strap and replace the cover.
   See Chapter 7.
6. Power on the system.
   See Chapter 6.

Removing the Diskette Drive Cable (SunCD 4 Drive-Type Chassis)

1. Remove power from the system.
   See “Removing Power from the System” on page 45.
2. Remove the cover and attach a wrist strap.
   See Chapter 7.

Caution – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

3. Remove the CD-ROM drive.
   See “Removing a CD-ROM Drive” on page 123.
4. Remove the diskette drive.
   See “Removing a Diskette Drive” on page 127.

5. Disconnect the diskette data cable from the system board (FIGURE 8-46).

6. Carefully note the diskette drive cable routing. Remove the diskette drive cable from the system chassis.

![Diagram of System Board Cable Routing](image)

Note: Figure illustrates SunCD 4 drive-type (1.6-inch drive) chassis only.

**FIGURE 8-46** System Board Cable Routing (SunCD 4 Drive-Type Chassis)

### Installing the Diskette Drive Cable (SunCD 4 Drive-Type Chassis)

**Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.
1. Route the diskette drive cable in the system chassis.

   **Note** – The diskette drive cable should pass under the first metal cable clip in the chassis floor.

2. Connect the diskette drive cable to the system board (FIGURE 8-46).

3. Install the diskette drive.
   
   See “Installing a Diskette Drive” on page 132.

4. Install the CD-ROM drive.
   
   See “Installing a CD-ROM Drive” on page 124.

5. Detach the wrist strap and replace the cover.
   
   See Chapter 7.

6. Power on the system.
   
   See “Turning On the Power” on page 47.
Storage Devices

This chapter contains procedures for removing and installing storage devices. The following is the list of storage devices.

Hard Disk Drive

The SPARCstation 20 system uses 535-Mbyte, 1.05-Gbyte, and 2.0-Gbyte single-connector hard disk drives. Each disk drive connects to the SCSI backplane. A handle on each single-connector drive helps to install the hard disk drive into the SCSI backplane socket.

To remove and install a hard disk drive, proceed as follows.

Note – Do not remove the handle from the hard disk drive; it is part of the field replaceable unit (FRU).

Removing a Hard Disk Drive

1. Remove power from the system.
   See “Removing Power from the System” on page 45.

2. Remove the cover and attach the wrist strap.
   See Chapter 7.
Caution – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

3. Determine which drive to remove (FIGURE 9-1); the system supports up to two internal hard disk drives.

Note – Remove the top disk drive before removing the bottom disk drive.

Note – The disk drive position determines the assigned SCSI address. When installed, the bottom disk drive is assigned SCSI address 3 and the top disk drive is assigned SCSI address 1.

![Location of Hard Disk Drives](image)

FIGURE 9-1 Location of Hard Disk Drives

Caution – When installing the hard disk drive, do not change the disk drive position. The system software expects the disk drive to be at a specific SCSI address.

4. Press the plastic latch located beneath the handle at the rear of the drive to release the handle (FIGURE 9-2).
5. Lift the drive handle up (FIGURE 9-3).

**Note** – When lifted, the drive handle pivots to disconnect the drive from the socket on the SCSI backplane.

6. Grasp the drive handle and lift the drive straight up and out of the chassis.

7. Place the drive on an antistatic surface.

8. Repeat Step 4 through Step 7 to remove the bottom disk drive (if installed).
Installing a Hard Disk Drive

**Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.
Note – The disk drive position determines the assigned SCSI address. When installed, the bottom disk drive is assigned SCSI address 3 and the top disk drive is assigned SCSI address 1.

Note – No jumper settings are required for the hard disk drives.

1. Position the hard disk drive as follows (FIGURE 9-4):
   a. Release the handle latch near the rear of the drive and lift the handle to the vertical position.
   b. While grasping the drive handle with one hand, use the other hand to hold the drive steady; carefully lower the drive into the chassis.
      
      Note – Ensure that the drive is properly seated in the mounting slots of the drive bracket and that the SCSI connector on the hard drive is pointed toward the SCSI backplane.

   c. Push the drive handle down away from the connector until the handle locks into the latch.

      Note – When lowered, the drive handle acts as a mechanism to engage the drive connector with the SCSI backplane socket.

      Note – Do not force the drive handle. If the drive handle does not move easily into the latched position, remove the drive and repeat Steps 3 and 4. If a hard disk drive is not installed in the top position, SCSI address 1 can be used by an external peripheral connected to the SCSI bus. However, if a hard disk drive is installed at the top position, the top position disk drive assumes SCSI address 1, and the external peripheral must either be re-jumpered to a new SCSI address or disconnected from the SCSI bus.

2. Remove the wrist strap and replace the cover.
   See Chapter 7.

3. Power on the system.
   See “Turning On the Power” on page 47.
FIGURE 9-4  Installing a Hard Disk Drive
CD-ROM Drive

The SPARCStation 20 system is configured with one of two types of CD-ROM drives: a 1-inch SunCD 2Plus drive and a 1.6-inch SunCD 4 drive.

To remove and install a CD-ROM drive, proceed as follows.

**Note** – After removing a SunCD 2Plus or a SunCD 4 CD-ROM drive, be sure that the same drive-type is installed.

Removing a CD-ROM Drive

1. **Remove power from the system.**
   See “Removing Power from the System” on page 45.

2. **Remove the cover and attach a wrist strap.**
   See Chapter 7.

   **Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

3. **Remove the CD-ROM drive as follows (FIGURE 9-5):**
   a. Push the CD-ROM drive forward and slide it toward the outer edge of the chassis to disengage it.
   b. Disconnect the SCSI data cable and DC power cable from the CD-ROM drive.

   **Note** – Leave the cables connected to the system board.

   c. Lift the CD-ROM drive up and toward the chassis edge and remove it from the chassis.
   d. Place the CD-ROM drive on an antistatic surface.
Installing a CD-ROM Drive

To install a CD-ROM drive:

**Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

**Note** – Ensure that the same drive-type is installed.

1. Ensure proper jumper installation as follows:
   a. For the SunCD 2Plus CD-ROM drive: at pins ID4, ID2, and Term power (FIGURE 9-6).
   b. For the SunCD 4 drive: at pins ID4, ID2, and Term power (FIGURE 9-7).
Note – The two ID select jumpers set the SCSI address of the drive to 6. A parity jumper may also need to be installed.

2. Place the CD-ROM drive above the drive bracket slots.
Note – The drive grommets slide into the drive bracket slots. The drive sits on a shelf above the diskette drive.

3. Install the CD-ROM drive as follows (FIGURE 9-8):

   a. Connect the SCSI data cable and the DC power cable to the CD-ROM drive.
   
   b. Push the CD-ROM drive down and toward the center of the unit so that the drive is properly seated in the drive bracket.

4. Detach the wrist strap and replace the cover.
   
   See Chapter 7.

5. Power on the system.
   
   See “Turning On the Power” on page 47.

FIGURE 9-8 Installing a CD-ROM Drive

Diskette Drive

The optional internal diskette drive is located below the CD-ROM drive, adjacent to the power supply.
To remove and install a diskette drive, proceed as follows.

**Note** – If a CD-ROM drive is installed, it must be removed before a diskette drive can be removed or installed.

## Removing a Diskette Drive

1. **Remove power from the system.**  
   See “Removing Power from the System” on page 45.

2. **Remove the cover and attach a wrist strap.**  
   See Chapter 7.

   **Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

3. **Remove the CD-ROM drive if installed** (FIGURE 9-10).  
   See “Removing a CD-ROM Drive” on page 123.

   **Note** – If a CD-ROM drive is not installed, the CD-ROM filler panel must be removed to access the diskette drive.

---

**FIGURE 9-9**  Diskette Drive Location
4. Remove the filler panel as follows (FIGURE 9-10):
   a. Press the filler panel tabs in.
   b. Rotate the filler panel outward.
   c. Remove the filler panel from the chassis.

![CD-ROM Drive Filler Panel](tab)

**FIGURE 9-10** CD-ROM Drive Filler Panel

5. Place the screwdriver tip into the drive bracket slot next to a diskette grommet.
   Use the screwdriver as a lever to move a diskette grommet forward, toward the center of the chassis (FIGURE 9-11).

6. Repeat Step 5 for other grommets to free the diskette drive.

7. Disconnect the power cable from the diskette drive (FIGURE 9-11).
8. Disconnect the diskette drive data cable (FIGURE 9-12).
9. Lift the diskette drive from the chassis; place the diskette drive on an antistatic surface (FIGURE 9-13).

10. Remove the Phillips screws and grommets; set screws and grommets aside (FIGURE 9-13).
FIGURE 9-13 Removing the Diskette Drive

FIGURE 9-14 Installing Grommets and Setting the Diskette Drive Switch
Installing a Diskette Drive

**Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

1. Install the grommets and Phillips screws that were set aside in Step 4 of “Removing a Diskette Drive” on page 127”.

2. Set the diskette drive switch; perform either Step 3a or Step 3b.
   
a. If the diskette drive has a switch on the side of the drive, set the diskette drive switch all the way to the rear (FIGURE 9-16).

   **Note** – This switch is not present on all models.

   b. If the diskette drive does not have a switch on the side of the drive, it has a microswitch (SW4) on the printed circuit board (PCB) side of the drive. Set SW4 to the 0 position by pressing it towards the rear of the diskette drive (FIGURE 9-16).
c. Locate the two-wire diskette drive power cable labeled P4. Move the SCSI disk cable and the CD-ROM drive power cable away from the diskette drive opening (FIGURE 9-17 and FIGURE 9-17).
3. Lift the diskette drive data cable and the diskette drive power cable up. Lower the diskette drive into position. Push the diskette drive forward to fully seat in the diskette drive mounts (FIGURE 9-19).

**Note** – The diskette drive data cable must be positioned between the diskette drive and the metal chassis.

**Note** – Push on the rear edge of the diskette drive chassis only. Do not push on exposed disk drive motors or connectors.
4. Attach the diskette drive power and data cables to the diskette drive in the following sequence:

   a. **Connect P4** (FIGURE 9-20).

      ![Diskette drive power cable (P4)](image)

      **FIGURE 9-20** Connecting the Diskette Drive Power Cable

      **Note** – The top faces up when connected to the drive.

   b. Connect the data cable by routing it over the diskette drive (FIGURE 9-21).
5. If applicable, replace the CD-ROM drive (see “Installing a CD-ROM Drive” on page 124) or replace the CD-ROM drive filler panel as follows:

a. Align the filler panel such that the filler panel base is on top of the diskette drive filler panel (FIGURE 9-22).

b. Verify that the two posts align with the chassis holes.

c. Squeeze the two tabs together and rotate the filler panel inward until the tabs snap into the chassis slots.
6. **Detach the wrist strap and replace the cover.**
   See Chapter 7.

7. **Power on the system.**
   See Chapter 6.
System Board Overview

This chapter contains descriptions of the system board. The following topics are presented in this chapter:

Preventing Damage

**Caution** – Use an antistatic mat when working with the system board. An antistatic mat contains the cushioning needed to protect the underside components, to prevent board flexing, and to provide antistatic protection.

Handling System Boards and Assemblies

**Caution** – The system chassis power-on/standby switch must be set to standby mode, and the AC power cord must be plugged in to ensure proper grounding.

**Caution** – Hot surface. Avoid contact. Surfaces are hot and may cause personal injury if touched.
Caution – The system board and related modules and cards have surface-mount components that may break when the board is flexed.

To minimize the amount of board flexing, observe the following precautions:

- Hold the system board only by the edges.
- Do not place the system board or any component of the system board (SBus card, MBus module, SIMM, or chip) on a hard surface. Use a cushioned antistatic mat. See Chapter 12.
- Transport the system board in an antistatic bag.

System Board Description

FIGURE 10-1 identifies the major components of the SPARCstation 20 system board.
System Board FRUs

The following are system board FRUs:

- DSIMM
- VSIMM
- NVSIMM
- AVB
- SBus card
- MBus module
- OpenBoot PROM
- NVRAM/TOD

FIGURE 10-1 System Board
Chapter 11 provides procedures for removing and replacing these components. See FIGURE 10-2.

**FIGURE 10-2** Exploded View of the System Board
System Board and Component Replacement

This chapter describes how to remove, replace, and configure the replacement parts.

MBus Module

The following paragraphs provide procedures for removing and installing a single-wide and a double-wide MBus module. To remove and install a single-wide and a double-wide MBus module, proceed as follows.

Removing a Single-Wide MBus Module

**Caution** – Hot surface. Avoid contact. Surfaces are hot and may cause personal injury if touched.

1. **Remove power from the system.**
   See “Removing Power from the System” on page 45.

2. **Remove the cover and attach the wrist strap.**
   See Chapter 7.

**Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.
3. Unlock both MBus module retainers (FIGURE 11-1).

4. If the system contains two MBus modules (slot 1, upper; slot 0, lower), and the MBus module being removed is in the lower slot (slot 0), remove the slot 1 MBus module first.

5. Install the card extractor on the MBus module (FIGURE 11-2).

6. Holding each side of the card extractor, pull the MBus module up until the connector is out of the slot (FIGURE 11-3).

7. Remove the card extractor from the MBus module.

   **Note** – Save the extractor for future use.

8. Place the MBus module on an antistatic surface.
FIGURE 11-2 Extractor Installation on a Single-Wide MBus Module

FIGURE 11-3 Removing a Single-Wide MBus Module
Installing a Single-Wide MBus Module

**Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

1. Push the MBus module retainers to the unlocked position.

2. Position the MBus module so that the MBus module connector faces the **appropriate slot** (FIGURE 11-4).

3. Insert the back edge of the module between the metal (lower) and plastic (upper) tabs on the back panel.

4. Hold the MBus module corners and press down firmly until the module is seated into the slot.

**FIGURE 11-4** Installing a Single-Wide MBus Module

**Caution** – A module must be installed in MBus module slot 0 (lower slot) or the system will not work.
**Note** – Be sure the MBus module is fully inserted into the MBus slot. If it is not, the card retainers may not lock over the edge of the module properly.

**Caution** – Excessive force may bend or damage the pins.

5. Lock the retainer arms over the edge of the MBus module (FIGURE 11-5).

**FIGURE 11-5** MBus Module and SBus Card Retainers

Removing a Double-Wide MBus Module

**Caution** – Hot surface. Avoid contact. Surfaces are hot and may cause personal injury if touched.

1. Remove power from the system.
   
   See “Removing Power from the System” on page 45.

2. Remove the cover and attach the wrist strap.
   
   See Chapter 7.
Caution – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

3. Unlock the MBus module retainers and the SBus card retainers closest to the MBus slot (FIGURE 11-5).

4. Remove the four screws and rectangular washers from the MBus module and the back panel (FIGURE 11-6).

Note – Save the screws and washers for future use.

5. Hold each edge of the MBus module and pull up on the module until the connector is out of the slot (FIGURE 11-7).

6. Place the MBus module on an antistatic surface.

FIGURE 11-6 MBus Module Back Panel Screws and Washers
Installing a Double-Wide MBus Module

**Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

1. Unlock the MBus module retainers and the SBus card retainers (FIGURE 11-8).
2. Insert the MBus module back plate into the system back panel; hook the MBus module back plate into the back panel slot (FIGURE 11-9).

**Note** – A double-wide MBus module installs and has a back plate like an SBus card. The connector on the MBus module should face the appropriate MBus module slot.

**Caution** – An MBus module must be installed in MBus module slot 0 or the system will not work.
FIGURE 11-9 Seating a Double-Wide MBus Module Back Plate, Side View

**Caution** – Fingers should be on either side of the MBus module connector. Do not press both ends of the MBus module because only one connector is on the module.

3. Place fingers over each side of the MBus module connector and press down firmly until the MBus module is fully inserted into the slot.

4. **Lock the retainers over the edge of the MBus module** (FIGURE 11-10).

**Note** – If the MBus module is not fully inserted into the MBus slot, the retainers may not lock over the edge of the MBus module. The arrows in FIGURE 11-10 point to the retainers that must lock.

5. **Remove the wrist strap and replace the cover.**
   
   See Chapter 7.

6. **Power on the system.**
   
   See Chapter 6.
To remove and install an SBus card, proceed as follows.

**Removing an SBus Card**

1. **Remove power from the system.**
   See “Removing Power from the System” on page 45.

2. **Remove the cover and attach the wrist strap.**
   See Chapter 7.

---

**Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.
3. Unlock the retainers at the back edge of the SBus card (FIGURE 11-11).

![FIGURE 11-11 SBus Card Retainers](image)

4. Remove the screws and washers from the SBus card back plate (FIGURE 11-12).

**Note** – Save the screws and washers for later use.

![FIGURE 11-12 SBus Card Back Plate Screws and Washers](image)
5. Insert the extractor into the SBus card (FIGURE 11-13) or insert two extractors into double-wide SBus card (FIGURE 11-14).

**FIGURE 11-13** Installing the Card Extractor

**FIGURE 11-14** Removing a Double-Wide SBus Card
6. Hold each end of the extractor and pull up slowly until the connector is removed from the slot.

Note – There are single-wide and double-wide SBus cards. FIGURE 11-15 shows a double-wide SBus card being removed.

7. Remove the card extractor from the SBus card as follows:
   a. Bend one leg of the extractor slightly to the outside until the hook clears the hole in the SBus card (FIGURE 11-15).
   b. Remove the other side of the extractor, which should come out easily without bending.

8. Place the SBus card on an antistatic surface.

FIGURE 11-15 Removing the SBus Card Extractor
Installing an SBus Card

Caution – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

1. Place the SBus card on an antistatic surface.

2. If required, set the card jumpers or switches.
   See the manual for the SBus product.

3. If the SBus card has tabs on the back plate, remove the two small Phillips screws that attach the adapter bracket to the back plate and remove the adapter bracket (FIGURE 11-16).

4. Remove the card extractor from the SBus card as follows:
   a. Bend one leg of the extractor outward slightly until the hook clears the hole in the SBus card (FIGURE 11-17).
   b. Remove the other side of the extractor, which should come out easily without bending.

5. Push the rear card retainers back on the system board (FIGURE 11-17 and FIGURE 11-18).
Note – FIGURE 11-17 and FIGURE 11-18 show a double-wide SBus card and a single-wide SBus card, respectively, being installed.

FIGURE 11-17 Installing a Double-Wide SBus Card

FIGURE 11-18 Installing a Single-Wide SBus Card
6. Install the Sbus card as follows (FIGURE 11-18):

7. Install the screws and washers into the SBus card back plate (FIGURE 11-19).
   a. Slide the card at an angle into the back panel of the system. Hook the card back plate under the lip on the back panel. Be sure the SBus card back plate shows through the slots on the back panel.

   **Note** – The back plate connector(s) may not align with other connectors in the back panel.

   **Caution** – Do not use force. Align the card connector and slot properly or card pin damage may occur.

   b. Align the SBus card connector with the SBus slot on the system board. Gently press the corners of the card to seat the connector into the slot.

   c. Push the retainers forward over the edge of the SBus card.

8. Remove the wrist strap and replace the cover.
   See Chapter 7.

9. Power on the system.
   See Chapter 6.
DSIMMs, VSIMMs, and NVSIMMs

The SPARCstation 20 system supports as many as eight dynamic single in-line memory modules (DSIMMs). DSIMMs are available in three capacities; 16-Mbyte, 32-Mbyte, and 64-Mbyte. In addition, the SPARCstation 20 system supports up to two video single in-line memory modules (VSIMMs) and one non-volatile SIMM (NVSIMM). VSIMMs are available in two capacities: 4-Mbyte and 8-Mbyte.

Each SIMM card has horizontally mounted integrated circuits (ICs) on both sides of the card. The part of the card that plugs into the memory slot has a scalloped edge with a conductive surface, that is known as gold fingers.

Removing and installing all capacities and types of SIMMs (DSIMM, VSIMM, NVSIMM) is identical. The following sections provide a brief discussion of DSIMMs, VSIMMs, and NVSIMMs and is followed by procedures for removing and installing a SIMM.

---

**Caution** – A SIMM is made of delicate electronic components that are extremely sensitive to static electricity. Handle a SIMM only by the edges. Ordinary amounts of static from clothes or work environment can damage a SIMM. Do not touch the components on a SIMM or any metal parts. Wear a grounding strap before unpacking a SIMM and while handling a SIMM.

---

**DSIMM**

FIGURE 11-20 and FIGURE 11-21 show front and back views, respectively, of a DSIMM. When installed, the key notch should be near the DSIMM ejector levers.

![Key notch](image-url)
SPARCstation 20 systems with the SX graphics option are equipped with a minimum of 4 Mbytes of video random access memory (VRAM). Physically, VRAM chips are grouped together as VSIMMs.

Each VSIMM contains 4 or 8 Mbytes of video memory. Each VSIMM is a complete high-speed, high-resolution frame buffer that plugs into a memory slot located on the system board. A maximum of two VSIMMs may be installed. With two VSIMMs installed, a maximum of six DSIMMs (384 Mbytes) may be installed.

Note – If a second VSIMM is installed, an auxiliary video board (AVB) must also be installed to provide a second 13W3 video output connector. See “Auxiliary Video Board” on page 168.

A VSIMM differs in appearance from a DSIMM. FIGURE 11-22 shows the front view of a VSIMM. FIGURE 11-23 shows the back view of a VSIMM.
NVSIMM

Each NVSIMM contains 2 Mbytes of memory, a battery, and power controller circuitry to ensure memory is not lost when the system is shut down or halts because of abnormal conditions. The NVSIMM is a card with a round, non-replaceable battery mounted on one side. The NVSIMM plugs into a memory slot located on the system board. FIGURE 11-25 shows the battery side of the NVSIMM.

Removing a SIMM

Refer to FIGURE 11-25 and proceed as follows:

Caution – The installed SIMMs are specifically designed to operate in the SPARCstation 20 system. Do not remove DSIMMs, VSIMMs, or NVSIMMs from different Sun systems and install them in a SPARCstation 20 system.
Caution – Do not disconnect the power cord from the system unit back panel connector. Be sure the power cord is connected to a grounded wall receptacle and that the system unit power switch is set to standby. Verify that the green LED at the chassis front is off.

FIGURE 11-25 SIMM Slot Locations

1. Remove power from the system.
   See “Removing Power from the System” on page 45.

2. Remove the cover and attach the wrist strap.
   See Chapter 7.

Caution – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

3. Place an antistatic surface next to the system.

Caution – Hot surface. Avoid contact. Surfaces are hot and may cause personal injury if touched.
4. Using the eraser end of a pencil, move the ejection lever away from the SIMM to be removed (FIGURE 11-26).

![SIMM Ejection Levers](image)

**FIGURE 11-26 SIMM Ejection Levers**

**Note** – Ejection levers are enlarged to show detail.

5. Grasp the upper corners of the SIMM and lift to remove the SIMM from the slot.

6. Place the SIMM on an antistatic surface.

**Installing a SIMM**

**Note** – Inspect the contents of the shipping box. Verify that a SIMM is in an antistatic bag and an antistatic wrist strap is supplied. After unpacking the contents of the shipping box, store the shipping box and packing material for future use.

**Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

1. Locate the SIMM slots on the system board (FIGURE 11-25).
   - Slot J0201 must have a DSIMM installed for the system to function properly. Other DSIMMs may be installed in any available slot.
Slot J0304 is reserved for the single factory-installed VSIMM with the SX graphics option. If no VSIMM is present, the slot may be used for a DSIMM.

Slot J0305 may be used for a DSIMM, VSIMM, or NVSIMM.

**Note** – Slot J0201 must be used for the first 64-Mbyte DSIMM when installing a combination of 64-Mbyte, 32-Mbyte, and 16-Mbyte DSIMMs. If a 16-Mbyte or 32-Mbyte DSIMM is previously installed in slot J0201, it must be removed from slot J0201 and installed in another unused DSIMM slot.

**Caution** – The NVSIMM is specifically designed to operate in the SPARCstation 20 system. Do not remove a NVSIMM from a different Sun system and install it in a SPARCstation 20 system. The SPARCstation 20 NVSIMM can only be used on systems executing the Solaris 2.3 (SunOS 5.3) operating environment (or later) and Prestoserve™ 2.4.1 or later. The SPARCstation 20 VSIMM can only be used on systems executing the Solaris 2.3 (SunOS 5.3) or later operating environment.

2. Remove the SIMM from the protective packaging and place it on an antistatic surface.

3. Hold the SIMM edges and insert it into the SIMM plastic guides.

4. If installing a DSIMM, position the DSIMM with the key notch near the ejection lever (FIGURE 11-27).

![Key notch](image)

**FIGURE 11-27** DSIMM, Back View

5. If installing a VSIMM, position the VSIMM with the plastic cover facing the system unit front panel. The notched portion of the VSIMM should be near the center of the system board (FIGURE 11-28).
6. If installing an NVSIMM, proceed as follows (FIGURE 11-29):
   a. Turn on the battery.
   b. Locate the jumper on the battery side of the NVSIMM.
   c. Move the jumper to the battery On position.

Caution – Once the battery is turned on, do not move or remove the jumper. Any data in the NVSIMM memory will be lost if the jumper is moved.
7. Hold the NVSIMM by the edges and, align the notch with the area between the two SIMM slots. The surface with the battery must face the system unit back panel (FIGURE 11-30).

![Diagram showing NVSIMM slot and notch location]

**FIGURE 11-30** Aligning the NVSIMM in the Slot, Back View

- **Note** – The notched part of the NVSIMM should be near the center of the system board.

- **Caution** – Use care when aligning the NVSIMM center notch with the connector frame. If the center notch is not properly aligned, the NVSIMM could be damaged when inserted.

8. Place the grooved surface of the insertion tool at a 90-degree angle on the upper edge of the SIMM (FIGURE 11-31 and FIGURE 11-32).
9. Put the heel of each hand on each side of the insertion tool pushing surface. Press firmly with both hands on the tool until the SIMM snaps into place.

**Note** – When installing a VSIMM in slot J0305, an AVB must also install. See “Auxiliary Video Board” on page 168.

10. Remove the insertion tool.
11. Remove the wrist strap and replace the cover.
See Chapter 7.

12. Turn on system power.
See Chapter 6.

---

**Auxiliary Video Board**

To remove and install an auxiliary video board (AVB), proceed as follows.

---

**Note** – If installing a VSIMM in slot J0305, an AVB must be installed to provide video output for the second VSIMM. The SPARCstation 20 system can accommodate one AVB.

---

**Removing an AVB**

1. **Remove power from the system.**
   See “Removing Power from the System” on page 45

2. **Remove the cover and attach the wrist strap.**
   See Chapter 7.

**Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

---

3. **Locate the AVB** (FIGURE 11-33).

4. **If an SBus card is installed above the AVB, remove the SBus card.**
   See “Removing an SBus Card” on page 152.

5. **Grasp the upper corners of the AVB and lift to remove the AVB from the slot** (FIGURE 11-33).

6. **Place the AVB on an antistatic surface.**
FIGURE 11-33 AVB Installed

FIGURE 11-34 Removing the AVB
Installing an AVB

Caution – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

1. Locate the AVB slot.

2. Install the AVB as follows (FIGURE 11-35):
   a. Insert the back edge of the AVB between the tabs on the back panel.
   b. Push the AVB connector into the slot.
   c. Hold the corners of the AVB.
   d. Press down firmly until the AVB connector is firmly seated in the slot.

Caution – Excessive force may bend or damage the pins.

3. Remove the wrist strap and replace the cover.
   See FIGURE 11-33.

4. Turn on system power.
   See Chapter 6.

FIGURE 11-35 Installing the Auxiliary Video Board
System Board

The following sections describe how to remove and install the system board, and verify jumper settings.

Removing the System Board

1. **Remove power from the system.**
   See “Removing Power from the System” on page 45.

2. **Remove the cover and attach the wrist strap.**
   See FIGURE 11-33.

   **Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

3. **Remove the following:**
   - MBus modules (see “MBus Module” on page 143)
   - SBus cards (see “SBus Card” on page 152)
   - SIMMs (see “DSIMMs, VSIMMs, and NVSIMMs” on page 159)
   - AVB (see “Auxiliary Video Board” on page 168)
   - NVRAM/TOD (see “Removing a NVRAM/TOD” on page 180)

   **Note** – The NVRAM/TOD contains system configuration parameters, the Ethernet address, and host ID numbers. Install the removed NVRAM/TOD on the replacement system board to preserve this information.

4. **Disconnect the following from the system board** (FIGURE 11-36):
   - Diskette connector (optional)
   - SCSI connector
   - DC power connector
   - Speaker/LED connector
5. At the chassis rear, loosen the lower two captive screws (FIGURE 11-37).

FIGURE 11-36 Disconnecting Diskette, SCSI, DC Power, and Speaker/LED Connectors

FIGURE 11-37 System Board Captive Screws
6. Slide the system board toward the back of the chassis until it clears the plastic card guide (FIGURE 11-38).

7. Lift the back of the system board slightly. Slide the system board clear of the plastic card guide and out of the chassis.

8. Place the system board on an antistatic surface.

**Note** – If a captive screw is in the upper right corner of the system board back panel, remove the screw by backing it out of the plastic back panel cover. This screw will be needed to secure the top cover to the chassis.

![FIGURE 11-38 Removing the System Board](image)

**Jumper Settings**

Verify the jumper settings of the clock speed jumper (J1401) and the serial port jumpers (J0801 and J0802) on the system board before installation. The jumpers are preset at the factory as shown in FIGURE 11-39.
**Note** – Leave the clock speed jumper set at 40/50 MHz for optimum performance. If the clock speed jumper is changed to the 40-MHz position, the MBus will only operate at 40 MHz and the SBus will only operate at 20 MHz.

---

**FIGURE 11-39** System Board Jumper Settings

The serial port jumpers are preset to RS-423 mode, but are changeable to RS-232 mode for connection to an X.25 network in Europe or Australia. To change the setting of serial port jumpers J0801 and J0802 from the RS-423 mode (position B) to the RS-232 mode (position A), refer to **FIGURE 11-39** and proceed as follows:

1. **Using needlenose pliers, remove the 2-pin shunt from position B.**

2. **Using needlenose pliers, replace the 2-pin shunt to position A.**
Installing the System Board

**Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

1. Position the system board over the back of the chassis so that the SIMM slots are facing the drive siderails and the chassis front.

2. Lift the back edge of the system board slightly, ensuring that the foot on the underside of the system board does not catch the metal edge of the chassis (FIGURE 11-40).

3. Position the system board edges into the plastic card guides; slide the system board toward the chassis front.

4. Tighten the captive screws at the back of the chassis (FIGURE 11-41).
Note – Do not overtighten captive screws.

5. Ensure that the configuration of the replacement system board is identical to the removed system board. Remove any SBus or MBus filler panels from the replacement system board, as necessary.

6. Replace the following:
   - NVRAM/TOD (see “Installing a NVRAM/TOD” on page 181)
   - AVB (see “Auxiliary Video Board” on page 168)
   - SIMMs (see “DSIMMs, VSIMMs, and NVSIMMs” on page 159)
   - SBus cards (see “SBus Card” on page 152)
   - MBus modules (see “MBus Module” on page 143)

![System Board Captive Screws](image)

Note – It is not necessary to remove the OpenBoot PROM from the defective board. The new system board already has the correct OpenBoot PROM installed.

7. Connect the following to the system board (FIGURE 11-42):
   - Diskette connector (optional)
   - SCSI connector
   - DC power connector
   - Speaker/LED connector

8. Remove the wrist strap and replace the cover.
   See Chapter 7.

9. Turn on system power.
   See Chapter 6.
OpenBoot PROM

To remove and install an OpenBoot PROM, proceed as follows.

**Note** – It is not necessary to transfer the OpenBoot PROM from an existing system board to a replacement system board.
Removing an OpenBoot PROM

1. Remove power from the system.
   See “Removing Power from the System” on page 45.

2. Remove the cover and attach the wrist strap.
   See Chapter 7.

   **Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

3. Remove any SBus cards.
   See “Removing an SBus Card” on page 152.

4. Use a DIP/IC extractor tool or a small flat-blade screwdriver to remove the OpenBoot PROM (FIGURE 11-43).

5. Place the OpenBoot PROM on an antistatic surface.

   ![OpenBoot PROM Location](FIGURE 11-43 OpenBoot PROM Location)
Installing an OpenBoot PROM

**Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

1. Hold the OpenBoot PROM by the edges and align the notch on the OpenBoot PROM with the notch on the socket (FIGURE 11-44).

**Caution** – Do not touch the OpenBoot PROM pins.

2. Align the OpenBoot PROM pins with the sockets; insert the pins into the socket.

**Note** – Press down firmly until the pins are seated into the socket.

3. Replace any SBus cards.
   See “Installing an SBus Card” on page 156.

4. Remove the wrist strap and replace the cover.
   See Chapter 7.

5. Turn on system power.
   See Chapter 6.
FIGURE 11-44 OpenBoot PROM Orientation

NVRAM/TOD

To remove and install a NVRAM/TOD, proceed as follows.

Removing a NVRAM/TOD

1. **Remove power from the system.**
   See “Removing Power from the System” on page 45.

2. **Remove the cover and attach the wrist strap.**
   See Chapter 7.

---

**Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.
3. Remove any SBus cards.
   See “Removing an SBus Card” on page 152.

4. Locate the NVRAM/TOD chip on the system board (FIGURE 11-45).

5. Grasp the NVRAM/TOD by the tabs on the NVRAM/TOD carrier and pull up on the carrier to extract the NVRAM/TOD.

6. Place the NVRAM/TOD on an antistatic surface.

Installing a NVRAM/TOD

**Caution** – Use proper ESD grounding techniques when handling components. Wear an antistatic wrist strap and use an ESD-protected mat. Store ESD-sensitive components in antistatic bags before placing it on any surface.

1. Locate the NVRAM/TOD socket on the system board (FIGURE 11-46).
2. Insert the NVRAM/TOD into the carrier. Insert the NVRAM/TOD carrier into the socket, ensuring that the notches on the socket and the carrier are aligned properly (FIGURE 11-46).

Note: NVRAM/TOD carrier not illustrated.

FIGURE 11-46  NVRAM/TOD Orientation

3. Push the NVRAM/TOD carrier into the socket.

   Note – Press down firmly until the pins are seated in the socket.

4. Replace any SBus cards removed earlier.
   See “Installing an SBus Card” on page 156.

5. Detach the wrist strap and replace the cover.
   See Chapter 7.

6. Power on the system.
   See Chapter 6.
This chapter lists the authorized replaceable parts for the SPARCstation 20 system.

**Note** – The listed part numbers are correct as of the service manual publication date but are subject to change without notice. Consult your authorized Sun sales representative or service provider to confirm a part number prior to ordering a replacement part.

## Replacement Parts List

The following table lists the FRUs and associated part numbers.

<table>
<thead>
<tr>
<th>Replaceable Parts</th>
<th>Part Numbers</th>
</tr>
</thead>
<tbody>
<tr>
<td>System board (0 Mbytes, 0 CPU)</td>
<td>501-2924</td>
</tr>
<tr>
<td>SCSI backplane</td>
<td>501-2462</td>
</tr>
<tr>
<td>OpenBoot PROM</td>
<td>525-1377</td>
</tr>
<tr>
<td>NVRAM/TOD chip</td>
<td>525-1378</td>
</tr>
<tr>
<td>NVSIMM, 2-Byte</td>
<td>501-2001</td>
</tr>
<tr>
<td>VSIMM, 4-Byte</td>
<td>501-2481</td>
</tr>
<tr>
<td>VSIMM, 8-Byte</td>
<td>501-2482</td>
</tr>
</tbody>
</table>
### Table 12-1 Replacement Parts List and Part Numbers

<table>
<thead>
<tr>
<th>Replaceable Parts</th>
<th>Part Numbers</th>
</tr>
</thead>
<tbody>
<tr>
<td>DSIMM, 16-Mbyte</td>
<td>501-2479</td>
</tr>
<tr>
<td>DSIMM, 32-Mbyte</td>
<td>501-2622</td>
</tr>
<tr>
<td>DSIMM, 64-Mbyte</td>
<td>501-2480</td>
</tr>
<tr>
<td>Auxiliary video board</td>
<td>501-2488</td>
</tr>
<tr>
<td>SM50 (50-MHz SuperSPARC™)</td>
<td>501-2708</td>
</tr>
<tr>
<td>SM51 (50-MHz SuperSPARC with SuperCache™)</td>
<td>501-2754</td>
</tr>
<tr>
<td>SM52 (Dual 50-MHz SuperSPARC with SuperCache)</td>
<td>501-2780</td>
</tr>
<tr>
<td>SM61 (60-MHz SuperSPARC with SuperCache)</td>
<td>501-2825</td>
</tr>
<tr>
<td>SM71 (75-MHz SuperSPARC II with SuperCache)</td>
<td>501-3001</td>
</tr>
<tr>
<td>HS11 (100-MHz hyperSPARC™)</td>
<td>370-1866</td>
</tr>
<tr>
<td>HS12 (Dual 100-MHz hyperSPARC)</td>
<td>370-1867</td>
</tr>
<tr>
<td>HS21 (125-MHz hyperSPARC)</td>
<td>370-1865</td>
</tr>
<tr>
<td>SM151 (150-MHz hyperSPARC)</td>
<td>370-2162</td>
</tr>
<tr>
<td>Power supply</td>
<td>300-1279</td>
</tr>
<tr>
<td>644-Mbyte SCSI-2 SunCD 2Plus drive</td>
<td>540-2500</td>
</tr>
<tr>
<td>644-Mbyte SCSI-2 SunCD 4 drive</td>
<td>540-2852</td>
</tr>
<tr>
<td>1.44-Mbyte diskette drive (SunCD 2Plus drive-type chassis)</td>
<td>370-1419</td>
</tr>
<tr>
<td>1.44-Mbyte diskette drive (slim floppy) (SunCD 4 CD-ROM drive-type chassis)</td>
<td>540-2854</td>
</tr>
<tr>
<td>535-Mbyte SCSI-2 disk drive w/ mounting kit</td>
<td>540-2443</td>
</tr>
<tr>
<td>1.05-Gbyte SCSI-2 disk drive w/ mounting kit</td>
<td>540-2560</td>
</tr>
<tr>
<td>2.1-Gbyte SCSI-2 disk drive w/ mounting kit</td>
<td>540-2699</td>
</tr>
<tr>
<td>Speaker</td>
<td>370-1579</td>
</tr>
</tbody>
</table>
### TABLE 12-1  Replacement Parts List and Part Numbers

<table>
<thead>
<tr>
<th>Replaceable Parts</th>
<th>Part Numbers</th>
</tr>
</thead>
<tbody>
<tr>
<td>Fan</td>
<td>540-2640</td>
</tr>
<tr>
<td>Power harness (SunCD 2Plus drive-type chassis)</td>
<td>530-2066</td>
</tr>
<tr>
<td>Power harness (SunCD 4 CD-ROM drive-type chassis)</td>
<td>530-2279</td>
</tr>
<tr>
<td>Speaker/LED cable (SunCD 2Plus drive-type chassis)</td>
<td>530-2080</td>
</tr>
<tr>
<td>Speaker/LED cable (SunCD 4 CD-ROM drive-type chassis)</td>
<td>530-2291</td>
</tr>
<tr>
<td>Fan cable</td>
<td>530-2159</td>
</tr>
<tr>
<td>SCSI data cable (SunCD 2Plus drive-type chassis)</td>
<td>530-2053</td>
</tr>
<tr>
<td>SCSI data cable (SunCD 4 CD-ROM drive-type chassis)</td>
<td>530-2278</td>
</tr>
<tr>
<td>Diskette drive cable (SunCD 2Plus drive-type chassis)</td>
<td>530-2067</td>
</tr>
<tr>
<td>Diskette drive cable (SunCD 4 CD-ROM drive-type chassis)</td>
<td>530-2277</td>
</tr>
<tr>
<td>AUI adapter cable</td>
<td>530-2021</td>
</tr>
<tr>
<td>Serial port splitter cable</td>
<td>530-1869</td>
</tr>
<tr>
<td>IBM parallel port adapter cable</td>
<td>530-1861</td>
</tr>
<tr>
<td>Centronics parallel port adapter cable</td>
<td>530-1857</td>
</tr>
</tbody>
</table>

### Illustration of Selected FRUs

FIGURE 12-1 shows a top view of selected FRUs. FIGURE 12-2 shows an exploded view of the SPARCstation 20 system. FIGURE 12-3 shows the major components of the system board. FIGURE 12-4 shows standard external cables and FIGURE 12-5 shows optional external cables.
FIGURE 12-1 Selected FRUs
FIGURE 12-2 SPARCstation 20 System Exploded View
FIGURE 12-3  Major Components of the System Board

Note: NVRAM/TOD carrier not illustrated.
FIGURE 12-4 Standard External Cables

- External power cable
- Twisted-pair Ethernet cable
- Video cable
- Keyboard cable
- Microphone and cable
FIGURE 12-5 Optional External Cables

- IBM parallel printer cable
- Centronics parallel printer cable
- Serial port Y breakout cable
- SCSI cable
- Thick Ethernet AUI adapter cable
Product Specifications

This appendix provides product specifications for the SPARCStation 20 system and is divided into three parts:

- Physical specifications
- Electrical specifications
- Environmental specifications

Physical Specifications

TABLE A-1 lists the physical specifications for the SPARCStation 20 system.

<table>
<thead>
<tr>
<th>Component</th>
<th>Height inches (mm)</th>
<th>Width inches (mm)</th>
<th>Depth inches (mm)</th>
<th>Net Weight lbs (kg)</th>
</tr>
</thead>
<tbody>
<tr>
<td>System unit</td>
<td>3.07 (78)</td>
<td>16.40 (417)</td>
<td>16.10 (409)</td>
<td>27.00 (12.70)</td>
</tr>
<tr>
<td>Keyboard</td>
<td>1.70 (44)</td>
<td>20.10 (510)</td>
<td>7.20 (182)</td>
<td>3.50 (1.60)</td>
</tr>
<tr>
<td>Mouse</td>
<td>2.00 (50)</td>
<td>4.00 (100)</td>
<td>3.00 (80)</td>
<td>0.30 (0.14)</td>
</tr>
<tr>
<td>Color monitor (17-inch)</td>
<td>16.40 (414)</td>
<td>16.00 (406)</td>
<td>17.70 (450)</td>
<td>49.50 (22.50)</td>
</tr>
<tr>
<td>Color monitor (20-inch)</td>
<td>18.54 (471)</td>
<td>18.70 (475)</td>
<td>19.49 (495)</td>
<td>69.50 (31.50)</td>
</tr>
</tbody>
</table>
Electrical Specifications

TABLE A-2 lists the electrical specifications for the system unit. TABLE A-3 lists the electrical specifications for the 17-inch monitor. TABLE A-4 lists the electrical specifications for the 20-inch monitor.

**TABLE A-2**  Electrical Specifications (System Unit)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Nominal frequencies</td>
<td>50 Hz or 60 Hz</td>
</tr>
<tr>
<td>Nominal voltage range</td>
<td>100-120 VAC or 200-240 VAC</td>
</tr>
<tr>
<td>Maximum current</td>
<td>2 A at 100 V, system unit</td>
</tr>
<tr>
<td></td>
<td>1 A at 200 V, system unit</td>
</tr>
<tr>
<td></td>
<td>4 A at 100 V, auxiliary output</td>
</tr>
<tr>
<td></td>
<td>2 A at 200 V, auxiliary output</td>
</tr>
<tr>
<td></td>
<td>6 A at 100 V, total maximum</td>
</tr>
<tr>
<td></td>
<td>3 A at 200 V, total maximum</td>
</tr>
<tr>
<td>Maximum VA rating</td>
<td>200 VA</td>
</tr>
<tr>
<td>Maximum power consumption</td>
<td>350 W</td>
</tr>
<tr>
<td>Heat output (without monitor)</td>
<td>683 BTUs/hr</td>
</tr>
</tbody>
</table>

**TABLE A-3**  Electrical Specifications (17-inch Monitor)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Nominal frequencies</td>
<td>50 Hz or 60 Hz</td>
</tr>
<tr>
<td>Nominal voltage range</td>
<td>100 to 120 VAC or 200 to 240 VAC</td>
</tr>
<tr>
<td>Typical operating current</td>
<td>1.6 A at 100 VAC, 0.8 A at 240 VAC</td>
</tr>
<tr>
<td>Typical operating power</td>
<td>120 W</td>
</tr>
<tr>
<td>Maximum current</td>
<td>2.7 A at 100 to 120 VAC;</td>
</tr>
<tr>
<td></td>
<td>1.5 A at 200 to 240 VAC</td>
</tr>
</tbody>
</table>
### TABLE A-3  Electrical Specifications (17-inch Monitor) *(Continued)*

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Maximum VA rating</td>
<td>290 VA</td>
</tr>
<tr>
<td>Maximum power consumption</td>
<td>200 W</td>
</tr>
<tr>
<td>Heat output</td>
<td>410 BTUs/hr</td>
</tr>
</tbody>
</table>

### TABLE A-4  Electrical Specifications (20-inch Monitor)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Nominal frequencies</td>
<td>50 Hz or 60 Hz (nominal)</td>
</tr>
<tr>
<td>Nominal voltage range</td>
<td>100 to 120 VAC or 200 to 240 VAC</td>
</tr>
<tr>
<td>Typical operating current</td>
<td>1.7 A at 110 VAC, 0.85 A at 220 VAC</td>
</tr>
<tr>
<td>Standby power</td>
<td>5 W</td>
</tr>
<tr>
<td>Typical operating power</td>
<td>125 W</td>
</tr>
<tr>
<td>Maximum current</td>
<td>2.2 A at 100 to 120 VAC, 1.4 A at 200 to 240 VAC</td>
</tr>
<tr>
<td>Maximum VA rating</td>
<td>295 VA</td>
</tr>
<tr>
<td>Maximum power consumption</td>
<td>150 W</td>
</tr>
<tr>
<td>Heat output</td>
<td>427 BTUs/hr</td>
</tr>
</tbody>
</table>
Environmental Specifications

TABLE A-5 lists the operating and non-operating environmental specifications for the SPARCStation 20 system.

### TABLE A-5  Environmental Specifications

<table>
<thead>
<tr>
<th>Operating Environment</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Temperature range</td>
<td>32˚ F to 104˚ F (0˚ C to 40˚ C)</td>
</tr>
<tr>
<td>Humidity</td>
<td>93% RH at 104˚ F (40˚ C)</td>
</tr>
<tr>
<td>Altitude range</td>
<td>0 to 10,000 feet (0 to 3 km)</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Non-Operating Environment</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Temperature range</td>
<td>-40˚ F to 149˚ F (-40˚ C to 65˚ C)</td>
</tr>
<tr>
<td>Humidity</td>
<td>93% RH at 104˚ F (40˚ C)</td>
</tr>
<tr>
<td>Altitude</td>
<td>0 to 40,000 feet (0 to 12 km)</td>
</tr>
</tbody>
</table>
Functional Description

This chapter describes the system architecture for the SPARCstation 20. See Chapter 1 for illustrations of the back panel.

System Overview

The SPARCstation 20 supports a system board, a power supply, up to two hard disk drives, a SunCD 2Plus or a SunCD 4 drive, and a diskette drive. A variety of SBus cards and MBus modules can be installed on the system board.

Memory is installed using DSIMMs. A maximum of eight DSIMMs may be installed. Available DSIMMs capacities include 16-Mbyte, 32-Mbyte, and 64-Mbyte.

Additionally, the SPARCstation 20 system supports up to two VSIMMs or one VSIMM and one NVSIMM. The VSIMM output is provided through the 13W3 connector that is built into the back panel. Optional installation of a second VSIMM is permitted but requires an AVB with a dedicated 13W3 video output. Although the second VSIMM (with AVB combination) provides a second video output for the SPARCstation 20 system, it reduces the number of I/O connectors from four to three.

System Architecture

The SPARCstation 20 system provides the following:

- SPARC architecture (scalable processor architecture)
- Memory management unit (MMU)
- MBus-based system processor architecture
- SBus-based system bus architecture
The following are located on the system board:

- MBus module (two maximum)
- SBus card (four maximum)

**Note** – A maximum of four SBus cards may be installed unless optional VSIMM/AVB is installed, in which case a maximum of three SBus cards may be installed.

- Main Memory
  - DSIMM (eight maximum)
  - VSIMM (two maximum, in conjunction with up to six DSIMMs)
  - NVSIMM (one maximum, in conjunction with a second VSIMM and five DSIMMs)

**Note** – Installing a second VSIMM and NVSIMM reduces the maximum number of DSIMMs to six.

- AVB (one maximum, used with a second VSIMM)

---

**System Board**

FIGURE B-1 shows a block diagram of the SPARCstation 20 system board.
SBus Card

Each system board supports one to four SBus cards. A wide variety of I/O options can be used with SBus cards, such as graphic displays, Ethernet, printers, and serial/parallel controllers.
MBus Module

The MBus module provides a high-speed interface between the processor modules, the physical memory, and the I/O devices. The MBus module operates at a clock rate starting at 40 MHz. Interface features of the MBus module include:

- Synchronous operation
- Automatic switching
- 64-bit multiplexed address and data
- 64-Gbyte physical address space
- Multiple master
- Centralized arbitration, reset, interrupt, and clock distribution
- Overlapped arbitration with parking
- Shared memory multiprocessor signals and transactions
- Write-invalidate cache coherency protocol

In the SPARCstation 20 system, the MBus module can automatically switch from a 40-MHz frequency to a 50-MHz frequency, depending on the type of MBus module installed. Changing jumpers on the system board is not required.

MBus-to-SBus Interface

The MBus-to-SBus interface (MSBI) is located in the MSBI chip. The MSBI contains:

- IOMMU
- Base address register
- IOMMU control register
- Arbiter enable register
- MBus-to-SBus asynchronous fault status and address registers

The IOMMU is used to perform address translations when SBus masters request the SBus.

Memory

Various memory allocations are configured in up to eight SIMM slots. Allocations include a combination of DSIMMs, VSIMMs, and NVSIMMs. TABLE B-1 lists the possible memory allocations using the three SIMM types.
The following sections provide brief descriptions of DSIMMs, VSIMMs, and NVSIMMs, and identifies the maximum available memory capacities based on possible memory allocations.

<table>
<thead>
<tr>
<th>Memory Allocation</th>
<th>DSIMM</th>
<th>VSIMM</th>
<th>NVSIMM</th>
</tr>
</thead>
<tbody>
<tr>
<td>8 (max.)</td>
<td>0</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>7</td>
<td>1</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>7</td>
<td>0</td>
<td>1</td>
<td>(max.)</td>
</tr>
<tr>
<td>6</td>
<td>2 (max.)</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>6</td>
<td>1</td>
<td>1</td>
<td></td>
</tr>
</tbody>
</table>

**TABLE B-2**  DSIMM DRAM Densities

<table>
<thead>
<tr>
<th>RAM</th>
<th>Density</th>
<th>Number of RAMs</th>
<th>Capacity</th>
<th>Type</th>
</tr>
</thead>
<tbody>
<tr>
<td>4-Mbit</td>
<td>1M x 4</td>
<td>36</td>
<td>16 Mbytes</td>
<td>ECC DRAM</td>
</tr>
<tr>
<td>8-Mbit</td>
<td>2M x 4</td>
<td>36</td>
<td>32 Mbytes</td>
<td>ECC DRAM</td>
</tr>
<tr>
<td>16-Mbit</td>
<td>4M x 4</td>
<td>36</td>
<td>64 Mbytes</td>
<td>ECC DRAM</td>
</tr>
</tbody>
</table>

**DSIMM**

The SPARCstation 20 memory system has a 144-bit-wide data path. The 144-bit-wide path is divided into a 128-bit-wide data path and 16 bits of error correcting code (ECC). Through the Scalable Memory Controller (SMC), the 60-nanosecond DSIMMs receive control, address information, and data. The maximum available DSIMM memory capacities, based upon possible memory allocations, are as follows:

- Eight DSIMMs, zero VSIMMs, zero DVSIMMs: 8 x 64 Mbytes or 512 Mbytes
- Seven DSIMMS with one VSIMM or one DVSIMM: 7 x 64 Mbytes or 448 Mbytes
- Six DSIMMS with two VSIMMs or one VSIMM and one DVSIMM: 6 x 64 Mbytes or 384 Mbytes

TABLE B-2 lists the DSIMM DRAM densities supported by the SMC. FIGURE B-2 illustrates the DSIMM memory system.
The SPARCstation 20 system may contain one or more 32-Mbyte DSIMMs. The Solaris 1.1.1 Version B or later software environment for the SPARCstation 20 system supports up to eight 32-Mbyte DSIMMs or any combination of 16-Mbyte, 32-Mbyte, and 64-Mbyte DSIMMs.

The Solaris 2.3 software environment requires kernel patch 101318-34 or later to support certain 32-Mbyte DSIMM configurations. Install patch 101318-34, available from your local Answer Center, if additional DSIMMs are installed to create one of the following memory combinations:

- One 32-Mbyte DSIMM and seven 16-Mbyte or seven 64-Mbyte DSIMMs
- Two 32-Mbyte DSIMMs and five or more 16-Mbyte or 64-Mbyte DSIMMs
- Three 32-Mbyte DSIMMs and three or more 16-Mbyte or 64-Mbyte DSIMMs
- Four 32-Mbyte DSIMMs and one or more 16-Mbyte, 32-Mbyte or 64-Mbyte DSIMMs

**Note** – Install the patch before installing the DSIMMs or the operating system will fail to boot.
VSIMM

Two versions of the SPARCstation 20 VSIMM are available: 4- Mbyte and 8-Mbyte. The maximum available VSIMM memory capacities, based upon possible memory allocations, are as follows:

- One VSIMM with seven DSIMMS: 8 Mbytes
- Two VSIMMs with six DSIMMS: 16 Mbytes

The VSIMM contains a memory display interface (MDI) chip, a video buffer chip (VBC), and a digital-to-analog converter (DAC). There is also pixel clock generation circuitry on the VSIMM. The SPARCstation 20 uses 60-nanosecond VRAMs on the VSIMM. VRAM densities are supported by the SMC and are listed in TABLE B-3. FIGURE B-3 illustrates the VSIMM memory system.

<table>
<thead>
<tr>
<th>VRAM</th>
<th>Density</th>
<th>VRAMs/VSIMM</th>
<th>Capacity</th>
<th>Type</th>
</tr>
</thead>
<tbody>
<tr>
<td>1-Mbit</td>
<td>256K x 4</td>
<td>32</td>
<td>4-Mbyte</td>
<td>128-bit VRAM</td>
</tr>
<tr>
<td>2-Mbit</td>
<td>256K x 8</td>
<td>32</td>
<td>8-Mbyte</td>
<td>256-bit VRAM</td>
</tr>
</tbody>
</table>

FIGURE B-3 VSIMM Memory System
NVSIMM

The NVSIMM provides an interface to the Prestoserve™™ software. The NVSIMM furnishes a battery backed-up memory that is used as a cache for disk drive access. Operating conjointly with the Prestoserve software, the NVRAM accelerates both NFS and UFS disk access. The maximum available NVSIMM memory capacities based upon possible memory allocations is 2 Mbytes.

SBus-to-SCSI, Ethernet, and Parallel Interfaces

The SBus-to-SCSI, Ethernet, and parallel interface (MACIO) ASIC provides the interface between the SBus and the following:

- SCSI
- Thick AUI Ethernet
- Twisted-pair Ethernet (TPE)
- Parallel port

The MACIO ASIC integrates three functions:

- Direct memory access (DMA)
- Local area network control for Ethernet (LANCE)
- Fast SCSI bus interface

FIGURE B-4 illustrates a functional block diagram of the MACIO ASIC.
SBus-to-External Bus Interface

The SBus-to-external bus interface (SEC) ASIC provides the interface between the SBus and the external bus. The external bus connects to the following:

- Serial port chips (2)
- Audio chip
- Diskette drive controller
- 4-Mbit EEPROM
- NVRAM/TOD chip
- LED interface

One of the serial port chips provides serial ports A and B. The second serial port chip provides the keyboard and mouse port. Line drivers interface the two serial port chips to the serial ports. FIGURE B-5 illustrates a functional block diagram of the SEC ASIC.
The clock chip provides the MBus and SBus clocks for the system board and the VSIMMs. There are 14 MBus clocks generated by the clock chip, one for each receiver:

- Four MBus clock receivers for each of two MBus modules (8 total)
- One for the scalable memory controller (SMC)
- One for the MBus-to-SBus interface (MSBI)
- One for each pair of the eight memory SIMMs (4 total)
- One for the SBus-to-external bus interface (SEC)
- One for the SBus to DBRI Interface (DBRI)
- One for the SBus to external data interface (MACIO)
- One for SBus slots 0 and 1
- One for SBus slots 2 and 3
System Board Connector Pinouts

This appendix provides signal descriptions for the SPARCStation 20 system unit back panel connectors. TABLE C-1 through TABLE C-8 lists connector pin assignments and signal descriptions. An illustration of each connector is also provided.

Parallel Connector

TABLE C-1 lists connector pin assignments and signal descriptions for the parallel connector.

![Parallel (Top)](image-url)

FIGURE C-1 Parallel Connector
<table>
<thead>
<tr>
<th>Pin</th>
<th>Function</th>
<th>I/O</th>
<th>Level</th>
<th>Signal Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>DataStrobe*</td>
<td>I/O</td>
<td>24-mA OC</td>
<td>Data valid strobe</td>
</tr>
<tr>
<td>2</td>
<td>Data Bit 0</td>
<td>I/O</td>
<td>24-mA OC</td>
<td>Data Bit 0</td>
</tr>
<tr>
<td>3</td>
<td>Data Bit 1</td>
<td>I/O</td>
<td>24-mA OC</td>
<td>Data Bit 1</td>
</tr>
<tr>
<td>4</td>
<td>Data Bit 2</td>
<td>I/O</td>
<td>24-mA OC</td>
<td>Data Bit 2</td>
</tr>
<tr>
<td>5</td>
<td>Data Bit 3</td>
<td>I/O</td>
<td>24-mA OC</td>
<td>Data Bit 3</td>
</tr>
<tr>
<td>6</td>
<td>Data Bit 4</td>
<td>I/O</td>
<td>24-mA OC</td>
<td>Data Bit 4</td>
</tr>
<tr>
<td>7</td>
<td>Data Bit 5</td>
<td>I/O</td>
<td>24-mA OC</td>
<td>Data Bit 5</td>
</tr>
<tr>
<td>8</td>
<td>Data Bit 6</td>
<td>I/O</td>
<td>24-mA OC</td>
<td>Data Bit 6</td>
</tr>
<tr>
<td>9</td>
<td>Data Bit 7</td>
<td>I/O</td>
<td>24-mA OC</td>
<td>Data Bit 7</td>
</tr>
<tr>
<td>10</td>
<td>Ack*</td>
<td>I</td>
<td>TTL</td>
<td>Data acknowledge</td>
</tr>
<tr>
<td>11</td>
<td>Busy</td>
<td>I</td>
<td>TTL</td>
<td>Busy</td>
</tr>
<tr>
<td>12</td>
<td>PE</td>
<td>I</td>
<td>TTL</td>
<td>Paper error</td>
</tr>
<tr>
<td>13</td>
<td>Select</td>
<td>I</td>
<td>TTL</td>
<td>Select</td>
</tr>
<tr>
<td>14</td>
<td>AutoFeed*</td>
<td>O</td>
<td>24-mA TTL</td>
<td>AutoFeed</td>
</tr>
<tr>
<td>15</td>
<td>Error*</td>
<td>I</td>
<td>TTL</td>
<td>Error</td>
</tr>
<tr>
<td>16</td>
<td>Reset*</td>
<td>O</td>
<td>24-mA TTL</td>
<td>Initialize</td>
</tr>
<tr>
<td>17</td>
<td>SctIn*</td>
<td>O</td>
<td>24-mA TTL</td>
<td>Select in</td>
</tr>
<tr>
<td>18</td>
<td>Shield</td>
<td>None</td>
<td>GND</td>
<td>Shield</td>
</tr>
<tr>
<td>19</td>
<td>Shield</td>
<td>None</td>
<td>GND</td>
<td>Shield</td>
</tr>
<tr>
<td>20</td>
<td>Shield</td>
<td>None</td>
<td>GND</td>
<td>Shield</td>
</tr>
<tr>
<td>21</td>
<td>Shield</td>
<td>None</td>
<td>GND</td>
<td>Shield</td>
</tr>
<tr>
<td>22</td>
<td>Shield</td>
<td>None</td>
<td>GND</td>
<td>Shield</td>
</tr>
</tbody>
</table>

* Active low signal
AUI Connector

The AUI connector is the bottom connector in the stacked pair of 26-pin connectors on the SPARCstation 20 system. TABLE C-2 lists connector pin assignments and signal descriptions for the parallel connector.

![AUI Connector](image)

TABLE C-2  AUI Connector Pin Assignments

<table>
<thead>
<tr>
<th>Pin</th>
<th>Function</th>
<th>I/O</th>
<th>Level</th>
<th>Signal Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>AUI_DO-</td>
<td>O</td>
<td>Analog</td>
<td>Data out negative</td>
</tr>
<tr>
<td>2</td>
<td>AUI_DI+</td>
<td>I</td>
<td>Analog</td>
<td>Data out positive</td>
</tr>
<tr>
<td>3</td>
<td>AUI_CI-</td>
<td>I</td>
<td>Analog</td>
<td>AUI collision negative</td>
</tr>
<tr>
<td>4</td>
<td>+12 VDC</td>
<td>O</td>
<td>DC</td>
<td>Power</td>
</tr>
</tbody>
</table>

* Active low signal

TABLE C-1  Parallel Connector Pin Assignments (Continued)
<table>
<thead>
<tr>
<th>Pin</th>
<th>Function</th>
<th>I/O</th>
<th>Level</th>
<th>Signal Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>5</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
<tr>
<td>6</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
<tr>
<td>7</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
<tr>
<td>8</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
<tr>
<td>9</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
<tr>
<td>10</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
<tr>
<td>11</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
<tr>
<td>12</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
<tr>
<td>13</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
<tr>
<td>14</td>
<td>AUI_DO+</td>
<td>O</td>
<td>Analog</td>
<td>Data out positive</td>
</tr>
<tr>
<td>15</td>
<td>AUI_DI_</td>
<td>I</td>
<td>Analog</td>
<td>Data out negative</td>
</tr>
<tr>
<td>16</td>
<td>AUI_CI+</td>
<td>I</td>
<td>Analog</td>
<td>AUI collision positive</td>
</tr>
<tr>
<td>17</td>
<td>Ground</td>
<td>None</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>18</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
<tr>
<td>19</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
<tr>
<td>20</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
<tr>
<td>21</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
<tr>
<td>22</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
<tr>
<td>23</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
<tr>
<td>24</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
<tr>
<td>25</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
<tr>
<td>26</td>
<td>Ground</td>
<td>None</td>
<td>GND</td>
<td>Ground</td>
</tr>
</tbody>
</table>
# Keyboard and Mouse Connector

TABLE C-3 lists connector pin assignments and signal descriptions for the keyboard and mouse connector.

![Keyboard and Mouse Connector Diagram](image)

---

**TABLE C-3** Keyboard/Mouse Connector Pin Assignments

<table>
<thead>
<tr>
<th>Pin</th>
<th>Function</th>
<th>I/O</th>
<th>Level</th>
<th>Signal Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Ground</td>
<td>O</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>2</td>
<td>Ground</td>
<td>O</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>3</td>
<td>+5 VDC</td>
<td>O</td>
<td>VCC</td>
<td>Power</td>
</tr>
<tr>
<td>4</td>
<td>Mouse In</td>
<td>I</td>
<td>TTL</td>
<td>Input from mouse</td>
</tr>
<tr>
<td>5</td>
<td>KeyBd Out</td>
<td>O</td>
<td>TTL</td>
<td>Keyboard output</td>
</tr>
<tr>
<td>6</td>
<td>KeyBd In</td>
<td>I</td>
<td>TTL</td>
<td>Keyboard input</td>
</tr>
<tr>
<td>7</td>
<td>PowerOn*</td>
<td>I</td>
<td>TTL</td>
<td>Power-on key</td>
</tr>
<tr>
<td>8</td>
<td>+5VDC</td>
<td>O</td>
<td>VCC</td>
<td>Power</td>
</tr>
</tbody>
</table>

* Active low signal
Serial Ports A and B Connector

TABLE C-4 lists connector pin assignments and signal descriptions for the serial port A and serial port B connectors.

![Serial Ports A and B Connector](image)

TABLE C-4  Serial Ports A and B Connector Pin Assignments

<table>
<thead>
<tr>
<th>Pin</th>
<th>Function</th>
<th>I/O</th>
<th>Level</th>
<th>Signal Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
<tr>
<td>2</td>
<td>Xmit Data A</td>
<td>O</td>
<td>RS-232/423</td>
<td>Xmit data A/transmit data port A</td>
</tr>
<tr>
<td>3</td>
<td>Rec Data A</td>
<td>I</td>
<td>RS-232/423</td>
<td>Receive data port A</td>
</tr>
<tr>
<td>4</td>
<td>RTS A</td>
<td>O</td>
<td>RS-232/423</td>
<td>Request to send port A</td>
</tr>
<tr>
<td>5</td>
<td>CTS A</td>
<td>I</td>
<td>RS-232/423</td>
<td>Clear to send port A</td>
</tr>
<tr>
<td>6</td>
<td>DSR A</td>
<td>I</td>
<td>RS-232/423</td>
<td>Data set ready port A</td>
</tr>
<tr>
<td>7</td>
<td>Ground</td>
<td></td>
<td>GND</td>
<td>Signal ground</td>
</tr>
<tr>
<td>8</td>
<td>DCD A</td>
<td>I/O</td>
<td>RS-232/423</td>
<td>Data carrier detect port A</td>
</tr>
<tr>
<td>9</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
<tr>
<td>10</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
<tr>
<td>11</td>
<td>DTR B</td>
<td>I</td>
<td>RS-232/423</td>
<td>Data terminal ready port B</td>
</tr>
<tr>
<td>12</td>
<td>DCD B</td>
<td>I</td>
<td>RS-232/423</td>
<td>Data carrier detect port B</td>
</tr>
<tr>
<td>13</td>
<td>CTS B</td>
<td>I</td>
<td>RS-232/423</td>
<td>Clear to send port B</td>
</tr>
<tr>
<td>14</td>
<td>Xmit Data B</td>
<td>O</td>
<td>RS-232/423</td>
<td>Transmit data port B</td>
</tr>
<tr>
<td>Pin</td>
<td>Function</td>
<td>I/O</td>
<td>Level</td>
<td>Signal Description</td>
</tr>
<tr>
<td>-----</td>
<td>----------</td>
<td>-----</td>
<td>---------</td>
<td>----------------------------------------</td>
</tr>
<tr>
<td>15</td>
<td>TRxC A</td>
<td>I</td>
<td>RS-232/423</td>
<td>Receive/transmit clock port A (pullup)</td>
</tr>
<tr>
<td>16</td>
<td>Rec Data B</td>
<td>I</td>
<td>RS-232/423</td>
<td>Receive data port B</td>
</tr>
<tr>
<td>17</td>
<td>RTxC A</td>
<td>I</td>
<td>RS-232/423</td>
<td>Secondary/transmit clock (receive) port</td>
</tr>
<tr>
<td>18</td>
<td>TRxC B</td>
<td>I</td>
<td>RS-232/423</td>
<td>Receive/transmit clock port B (pullup)</td>
</tr>
<tr>
<td>19</td>
<td>RTS B</td>
<td>O</td>
<td>RS-232/423</td>
<td>Request to send port B</td>
</tr>
<tr>
<td>20</td>
<td>DTR A</td>
<td>O</td>
<td>RS-232/423</td>
<td>Data terminal ready port A</td>
</tr>
<tr>
<td>21</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
<tr>
<td>22</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
<tr>
<td>23</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
<tr>
<td>24</td>
<td>TxC A</td>
<td>O</td>
<td>RS-232/423</td>
<td>Transmit clock port A</td>
</tr>
<tr>
<td>25</td>
<td>TxC B</td>
<td>O</td>
<td>RS2-32/423</td>
<td>Transmit clock port B</td>
</tr>
</tbody>
</table>

Twisted-Pair Ethernet Connector

TABLE C-5 lists connector pin assignments and signal descriptions for the twisted-pair Ethernet connector.

![Twisted-pair Ethernet Connector Diagram](image)
TABLE C-5  Twisted-Pair Ethernet Connector Pin Assignments

<table>
<thead>
<tr>
<th>Pin</th>
<th>Function</th>
<th>I/O</th>
<th>Level</th>
<th>Signal Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>TPO+</td>
<td>O</td>
<td>Analog</td>
<td>Differential twisted-pair transmit data</td>
</tr>
<tr>
<td>2</td>
<td>TPO−</td>
<td>O</td>
<td>Analog</td>
<td>Differential twisted-pair transmit data</td>
</tr>
<tr>
<td>3</td>
<td>TPI+</td>
<td>I</td>
<td>Analog</td>
<td>Differential twisted-pair receive data</td>
</tr>
<tr>
<td>4</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
<tr>
<td>5</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
<tr>
<td>6</td>
<td>TPI−</td>
<td>I</td>
<td>Analog</td>
<td>Differential twisted-pair receive data</td>
</tr>
<tr>
<td>7</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
<tr>
<td>8</td>
<td>N.C.</td>
<td>None</td>
<td>GND</td>
<td>Not connected</td>
</tr>
</tbody>
</table>

SCSI Connector

TABLE C-6 lists the pin assignments and signal descriptions for the SCSI connector.

FIGURE C-6  SCSI Connector

TABLE C-6  SCSI Connector Pin Assignments

<table>
<thead>
<tr>
<th>Pin</th>
<th>Function</th>
<th>I/O</th>
<th>Level</th>
<th>Signal Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Ground</td>
<td>None</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>2</td>
<td>Ground</td>
<td>None</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>3</td>
<td>Ground</td>
<td>None</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>4</td>
<td>Ground</td>
<td>None</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>Pin</td>
<td>Function</td>
<td>I/O</td>
<td>Level</td>
<td>Signal Description</td>
</tr>
<tr>
<td>-----</td>
<td>----------</td>
<td>-----</td>
<td>-------</td>
<td>-------------------</td>
</tr>
<tr>
<td>5</td>
<td>Ground</td>
<td>None</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>6</td>
<td>Ground</td>
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<td>Ground</td>
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<td>7</td>
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<tr>
<td>8</td>
<td>Ground</td>
<td>None</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>9</td>
<td>Ground</td>
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<td>Ground</td>
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<td>10</td>
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<td>Ground</td>
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<td>11</td>
<td>Ground</td>
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<td>GND</td>
<td>Ground</td>
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<tr>
<td>12</td>
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<td>13</td>
<td>Ground</td>
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<td>14</td>
<td>Ground</td>
<td>None</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>15</td>
<td>Ground</td>
<td>None</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>16</td>
<td>Ground</td>
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<td>Ground</td>
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<tr>
<td>18</td>
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<td>19</td>
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<td>Ground</td>
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<tr>
<td>20</td>
<td>Ground</td>
<td>None</td>
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<td>Ground</td>
</tr>
<tr>
<td>21</td>
<td>Ground</td>
<td>None</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>22</td>
<td>Ground</td>
<td>None</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>23</td>
<td>Ground</td>
<td>None</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>24</td>
<td>Ground</td>
<td>None</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>25</td>
<td>Ground</td>
<td>None</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>26</td>
<td>sd0*</td>
<td>I/O</td>
<td>TTL</td>
<td>SCSI data bit 0</td>
</tr>
<tr>
<td>27</td>
<td>sd1*</td>
<td>I/O</td>
<td>TTL</td>
<td>SCSI data bit 1</td>
</tr>
<tr>
<td>Pin</td>
<td>Function</td>
<td>I/O</td>
<td>Level</td>
<td>Signal Description</td>
</tr>
<tr>
<td>-----</td>
<td>----------</td>
<td>-----</td>
<td>-------</td>
<td>--------------------</td>
</tr>
<tr>
<td>28</td>
<td>sd2*</td>
<td>I/O</td>
<td>TTL</td>
<td>SCSI data bit 2</td>
</tr>
<tr>
<td>29</td>
<td>sd3*</td>
<td>I/O</td>
<td>TTL</td>
<td>SCSI data bit 3</td>
</tr>
<tr>
<td>30</td>
<td>sd4*</td>
<td>I/O</td>
<td>TTL</td>
<td>SCSI data bit 4</td>
</tr>
<tr>
<td>31</td>
<td>sd5*</td>
<td>I/O</td>
<td>TTL</td>
<td>SCSI data bit 5</td>
</tr>
<tr>
<td>32</td>
<td>sd6*</td>
<td>I/O</td>
<td>TTL</td>
<td>SCSI data bit 6</td>
</tr>
<tr>
<td>33</td>
<td>sd7*</td>
<td>I/O</td>
<td>TTL</td>
<td>SCSI data bit 7</td>
</tr>
<tr>
<td>34</td>
<td>sdp*</td>
<td>I/O</td>
<td>TTL</td>
<td>SCSI data parity</td>
</tr>
<tr>
<td>35</td>
<td>Ground</td>
<td>None</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>36</td>
<td>Ground</td>
<td>None</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>37</td>
<td>Ground</td>
<td>None</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>38</td>
<td>Termpower</td>
<td>I/O</td>
<td>TTL</td>
<td>Termination voltage</td>
</tr>
<tr>
<td>39</td>
<td>Ground</td>
<td>None</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>40</td>
<td>Ground</td>
<td>None</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>41</td>
<td>atn*</td>
<td>O</td>
<td>TTL</td>
<td>Attention</td>
</tr>
<tr>
<td>42</td>
<td>Ground</td>
<td>None</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>43</td>
<td>bsy*</td>
<td>O</td>
<td>TTL</td>
<td>Busy</td>
</tr>
<tr>
<td>44</td>
<td>ack*</td>
<td>O</td>
<td>TTL</td>
<td>Acknowledge</td>
</tr>
<tr>
<td>45</td>
<td>rst*</td>
<td>O</td>
<td>TTL</td>
<td>Reset</td>
</tr>
<tr>
<td>46</td>
<td>msg*</td>
<td>O</td>
<td>TTL</td>
<td>Message</td>
</tr>
<tr>
<td>47</td>
<td>sel*</td>
<td>O</td>
<td>TTL</td>
<td>Select</td>
</tr>
<tr>
<td>48</td>
<td>cd*</td>
<td>O</td>
<td>TTL</td>
<td>Control/data</td>
</tr>
</tbody>
</table>
TABLE C-7 lists the pin assignments and signal descriptions for the 13W3 connector.

**Note** – Connector 13W3 is not enabled unless a VSIMM is installed into SIMM slot J0304.

### 13W3 Connector

**TABLE C-7** 13W3 Connector Pin Assignments

<table>
<thead>
<tr>
<th>Pin</th>
<th>Function</th>
<th>I/O</th>
<th>Level</th>
<th>Signal Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>A1</td>
<td>Red</td>
<td>O</td>
<td>Analog</td>
<td>Video</td>
</tr>
<tr>
<td>A2</td>
<td>Green</td>
<td>O</td>
<td>Analog</td>
<td>Video</td>
</tr>
<tr>
<td>A3</td>
<td>Blue</td>
<td>O</td>
<td>Analog</td>
<td>Video</td>
</tr>
<tr>
<td>1</td>
<td>Serial Read</td>
<td>I</td>
<td>TTL</td>
<td>Serial read</td>
</tr>
<tr>
<td>2</td>
<td>Vert Sync</td>
<td>O</td>
<td>TTL</td>
<td>Vertical sync</td>
</tr>
<tr>
<td>3</td>
<td>Sense &lt;0&gt;</td>
<td>I</td>
<td>TTL</td>
<td>Sense</td>
</tr>
<tr>
<td>4</td>
<td>Ground</td>
<td>O</td>
<td>GND</td>
<td>Ground</td>
</tr>
</tbody>
</table>
Audio Ports

The audio ports include: headphone connector, microphone connector, audio line-in connector, and audio line-out connector. TABLE C-8 lists the pin assignments and signal descriptions for the audio ports.

<table>
<thead>
<tr>
<th>Pin</th>
<th>Function</th>
<th>I/O</th>
<th>Level</th>
<th>Signal Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>5</td>
<td>Comp Sync</td>
<td>O</td>
<td>GND</td>
<td>Composite sync</td>
</tr>
<tr>
<td>6</td>
<td>Hort Sync</td>
<td>O</td>
<td>TTL</td>
<td>Horizontal sync</td>
</tr>
<tr>
<td>7</td>
<td>Serial Write</td>
<td>O</td>
<td>TTL</td>
<td>Serial write</td>
</tr>
<tr>
<td>8</td>
<td>Sense &lt;1&gt;</td>
<td>I</td>
<td>TTL</td>
<td>Sense</td>
</tr>
<tr>
<td>9</td>
<td>Sense &lt;2&gt;</td>
<td>I</td>
<td>TTL</td>
<td>Sense</td>
</tr>
<tr>
<td>10</td>
<td>Ground</td>
<td>None</td>
<td>GND</td>
<td>Ground</td>
</tr>
</tbody>
</table>

TABLE C-8 Audio Port Signals

<table>
<thead>
<tr>
<th>Headphone</th>
<th>Microphone (old)</th>
<th>Microphone (new)</th>
<th>Line-Out</th>
<th>Line-In</th>
</tr>
</thead>
<tbody>
<tr>
<td>Tip</td>
<td>Left Channel</td>
<td>Left Channel</td>
<td>Left Channel</td>
<td>Left Channel</td>
</tr>
<tr>
<td>Ring (Center)</td>
<td>Right Channel</td>
<td>Microphone power</td>
<td>Right Channel</td>
<td>Right Channel</td>
</tr>
<tr>
<td>Shield</td>
<td>Return</td>
<td>Ground</td>
<td>Return</td>
<td>Return</td>
</tr>
</tbody>
</table>
Headphone Connector

The headphone connector is for connecting a set of stereophonic headphones to the system for private listening of audio output.

**FIGURE C-9** Microphone Icon

The microphone connector allows connection to the Sun Microphone II microphone.

**FIGURE C-10** Audio Line-Out Icon

The audio line-out connector allows connection to a stereophonic amplifier and external loudspeakers.

**FIGURE C-11** Audio Line-In Icon

The audio line-in connector allows connection to external sound sources: compact disc player or cassette tape player.

**FIGURE C-12** Audio Line-in Icon
SCSI Addresses

This appendix describes the SCSI addresses for the two internal hard disk drives. Also described are the SCSI address conventions for optional SCSI peripherals. For complete SCSI address information regarding optional SCSI peripherals, see the installation manual for the particular SCSI peripheral.

**Note** – Assign each SCSI device on the SCSI chain a unique SCSI address.

## Disk Drives

TABLE D-1 lists SCSI device addresses for the Solaris 1.1.1, Version B, operating environment. TABLE D-2 lists SCSI device addresses for the Solaris 2.3 (and later versions) operating environment.

**TABLE D-1**  
SCSI Addresses, Solaris 1.1.1 Version B (SunOS 4.1.3_U1 Version B) Operating Environment

<table>
<thead>
<tr>
<th>SCSI Device</th>
<th>SCSI Address</th>
</tr>
</thead>
<tbody>
<tr>
<td>Internal disk: lower position (first internal disk drive)</td>
<td>3</td>
</tr>
<tr>
<td>Internal disk: upper position (second internal disk drive)</td>
<td>1</td>
</tr>
<tr>
<td>External disk drives</td>
<td>0, 2, 1*</td>
</tr>
</tbody>
</table>
Note – Do not change the physical position of the internal drive. Doing so may cause an operating system initialization failure.

The internal disk drives are assigned an SCSI address number based on the drive bay disk drive position. The lower drive bay disk drive position is automatically assigned SCSI address 3. The upper drive bay disk drive position is assigned SCSI address 1. These addresses cannot be changed. With the Solaris 2.3 operating environment (and later operating environment releases), any SCSI device (disk, tape, CD-ROM, etc.) may be assigned any SCSI address from 0 to 6.

Note – SCSI address 7 is reserved for the SCSI processor of the computer system or the SCSI host adapter card.
Selected On-Board Diagnostics

This appendix describes the OpenBoot PROM on-board diagnostics. To execute the on-board diagnostics, the system must be at the `ok` prompt.

**Caution** – To execute the OpenBoot PROM on-board diagnostics from the `ok` prompt, the system must be shut down in an orderly manner. When the operating system or any other stand-alone application is booted, do not use the keyboard `Stop(L1)` and a keys to halt the system. Abruptly aborting the operating system may cause damage to data files.

`test device alias, test device pathname`

The `test` command, combined with a device alias or device path name, initializes the self-test program for that device.
To execute the self-test program for a device, type the `test` command followed by the `device alias` or `device pathname`. If a device has no self-test program, the following is displayed: No selftest method for `device name`. CODE EXAMPLE E-1 identifies the `test` output message.

**CODE EXAMPLE E-1  test Command Output Message**

```
ok test floppy
Testing floppy disk system. A formatted disk should be in the drive.
Test succeeded.
```

test-all

The `test-all` command tests all devices in the system that have a self-test program. Disks, tapes, and CDROMs are not tested by `test-all`. Tests are executed in the device tree (the device tree may be viewed with the `show-devs` command).

To execute the `test-all` program, type the `test-all` command.

**CODE EXAMPLE E-2  test-all Command Output Message**

```
ok test-all
```
**watch-clock**

*watch-clock* reads a register in the NVRAM/TOD chip and displays the result as a seconds counter. During normal operation, the seconds counter repeatedly increments from 0 to 59 until interrupted by pressing any key on the keyboard. CODE EXAMPLE E-3 identifies the *watch-clock* command output message.

**CODE EXAMPLE E-3  watch-clock Command Output Message**

```
ok  watch-clock
Watching the ’seconds’ register of the real time clock chip.
It should be ’ticking’ once a second.
Type any key to stop
41
```
watch-net, watch-aui, watch-tpe, watch-net-all

The watch-net, watch-aui, watch-tpe, and watch-net-all commands monitor broadcast Ethernet packets on the Ethernet cable(s) connected to the system. Good packets received by the system are displayed by a period (.). Erroneous packets received by the system are indicated with an X and the error description. CODE EXAMPLE E-4 identifies the watch-net command output message.

CODE EXAMPLE E-4  watch-net  Command Output Message

```
ok watch-net
   Internal loopback test -- succeeded.
   External loopback test -- Auto-selecting Ethernet cable I/F
Trying AUI
Received packet on AUI
Select cable - AUI
   succeeded.
Looking for Ethernet packets.
  '.' is a good packet.  "X" is a bad packet.
  Type any key to stop.
...............................................................
...............................................................
...................Framing error CRC error X.................
```

The SPARCstation 20 system has two types of on-board Ethernet interfaces: 10Base-T (also called twisted-pair Ethernet or TPE) and 10Base5 (also called thick Ethernet or AUI). Only one on-board interface may be connected at a time. The system automatically selects which interface is connected and active.

The AUI port is first tested by transmitting data packets and checking for No Carrier or response packets. If the AUI test fails, the TPE port is then tested.

This automatic selection occurs by typing watch-net. CODE EXAMPLE E-5 identifies the watch-net command output message. CODE EXAMPLE E-5 identifies the watch-tpe command output message. CODE EXAMPLE E-5 identifies the watch-net-all command output message.
Note – Selecting a specific Ethernet interface to monitor may be controlled through these user-specific commands: watch-tpe monitors the TPE connection; watch-aui monitors the thick Ethernet connection; watch-net-all monitors Ethernet packets on all installed Ethernet interfaces, one at a time.

**CODE EXAMPLE E-5**  
*watch-tpe Command Output Message*

```
ok watch-tpe
  Internal loopback test -- succeeded.
  External loopback test -- succeeded.
Looking for Ethernet packets.
  '.' is a good packet. 'X' is a bad packet.
Type any key to stop.
..............................................................
```

The `watch-net-all` command monitors Ethernet packets on all Ethernet interfaces installed in the system, one at a time. For example, if an SBus Ethernet card is installed in one of the system SBus slots, `watch-net-all` monitors the Ethernet transmissions detected by that card.
**Note** – When watch-net-all is interrupted by pressing any key, it initiates testing of the next system Ethernet interface.

**CODE EXAMPLE E-6  watch-net-all Command Output Message**

```
ok watch-net-all
/iommu@f,e0000000/sbus@f,e0001000/lebuffer@1,40000/le@1,60000
   Internal loopback test -- succeeded.
   External loopback test -- succeeded.
Looking for Ethernet packets.
'.' is a good packet.'X' is a bad packet.
Type any key to stop.
...............................................................
...............(keyboard key pressed here)

/iommu@f,e0000000/sbus@f,e0001000/ledma@f,400010/le@f,c00000
   Internal loopback test -- succeeded.
   External loopback test -- succeeded.
Looking for Ethernet packets.
'.' is a good packet.'X' is a bad packet.
Type any key to stop.
...............................................................
...............................................................
```

**probe-scsi, probe-scsi-all**

The `probe-scsi` command transmits an inquiry command to internal and external SCSI devices connected to the SPARCstation 20 on-board SCSI interface. If a SCSI device is connected and powered up, the target address, unit number, device type, and manufacturer name is displayed.

The `probe-scsi-all` command transmits an inquiry command to all SCSI devices connected to the built-in SCSI port and to all the SCSI host adapters installed in the system. The `probe-scsi-all` command returns the following information:

- SCSI host adapter address in the system device tree
- SCSI device identification data (SCSI address or target ID, unit number, and SCSI device information)
CODE EXAMPLE E-7 identifies the probe-scsi command output message.
CODE EXAMPLE E-8 identifies the probe-scsi-all command output message:

**CODE EXAMPLE E-7  probe-scsi Command Output Message**

```plaintext
ok  probe-scsi
Target 3
   Unit 0 Disk SEAGATE ST1480 SUN04245828 Copyright (c) 1991
Seagate All rights reserved.
```

**CODE EXAMPLE E-8  probe-scsi-all Command Output Message**

```plaintext
ok  probe-scsi-all
/iommu@f,e0000000/sbus@f,e0001000/dma@1,81000/esp@1,80000
Target 2
   Unit 0 Disk SEAGATE ST41600N SUN1.3G00286965 Copyright (c)1991
Seagate All rights reserved.

/iommu@f,e0000000/sbus@f,e0001000/espdma@f,400000/esp@f,800000
Target 3
   Unit 0 Disk SEAGATE ST1480 SUN04245828 Copyright (c)1991 Seagate
All rights reserved.
```

test-memory

The test-memory command tests the main memory if the system diag-switch? parameter is set to true. If the system diag-switch? parameter is set to false, the test-memory command verifies the memory according to a user-specified number of megabytes to be examined.

When the test-memory command is active, the quantity of memory being tested is displayed in megabytes and sequentially decrements to zero during the test. If the diag-switch? parameter is set to false, the test-memory command uses the selftest-#megs parameter in NVRAM to determine the quantity of memory to test. The default setting for the selftest-#megs parameter is 1, thus testing one megabyte of memory. To change the amount of memory to test, use the selftest-#megs parameter and enter the example command where XX is any whole number that does not exceed the actual size of memory in megabytes.
Example:

```
ok setenv selftest-#megs XX
```

---

**power-off**

The `power-off` command turns off the system power. A Type-5 keyboard must be attached for this command to work.

---

**module-info**

The `module-info` command is used to display the installed MBus module type and MBus module and SBus card clock speeds. If multiple MBus modules are installed, the `module-info` command identifies each module individually. CODE EXAMPLE E-9 identifies the `module-info` command output message for a uniprocessor. CODE EXAMPLE E-10 identifies the `module-info` command output message for multiprocessor.

**CODE EXAMPLE E-9  Uniprocessor module-info Command Output Message**

```
ok module-info
MBus: 40.00 MHz
SBus: 20.00 MHz
CPU#0: 50.00 MHz SuperSPARC/SuperCache
```

**CODE EXAMPLE E-10  Multiprocessor module-info Command Output Message**

```
<#0> ok module-info
MBus: 50.00 MHz
SBus: 25.00 MHz
CPU#0: 50.00 MHz SuperSPARC
CPU#2: 50.00 MHz SuperSPARC
```
<table>
<thead>
<tr>
<th>Term</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>address</td>
<td>Refers to a location within a computer system memory. The word location is a synonym. Reference is usually made to an address for the purpose of retrieving or storing information.</td>
</tr>
<tr>
<td>AUI port</td>
<td>Attachment unit interface port. (The port on the SPARCstation 20 system unit where the AUI adapter cable is connected.</td>
</tr>
<tr>
<td>AVB</td>
<td>Auxiliary video board. Supports graphic capabilities when used with a second VSIMM option.</td>
</tr>
<tr>
<td>back up</td>
<td>To make a copy of files on media, such as a disk or tape.</td>
</tr>
<tr>
<td>board</td>
<td>See printed circuit board.</td>
</tr>
<tr>
<td>Boot</td>
<td>To load the system software into memory and initiate system operation.</td>
</tr>
<tr>
<td>boot PROM</td>
<td>Refers to the system board OpenBoot™™ PROM chip. This chip contains boot code and a Forth Monitor program. The OpenBoot PROM is connected to the I/O data bus.</td>
</tr>
<tr>
<td>Boot PROM diagnostics</td>
<td>The diagnostic firmware in the boot PROM. These diagnostics include the Power-On Self-Test (POST) and on-board diagnostics.</td>
</tr>
<tr>
<td>buffer</td>
<td>The auxiliary data-storage device that holds data temporarily, and which may also perform other functions in conjunction with various I/O machines.</td>
</tr>
<tr>
<td>cables</td>
<td>Wires or bundles of wires configured with connectors at each end and used to connect two or more hardware devices.</td>
</tr>
<tr>
<td>cache</td>
<td>A mechanism for accelerating a microprocessor performance. A cache is a small, fast memory between the microprocessor and main memory.</td>
</tr>
<tr>
<td>card</td>
<td>See printed circuit board.</td>
</tr>
<tr>
<td>CPU</td>
<td>Central processing unit.</td>
</tr>
<tr>
<td>DAC</td>
<td>Digital-to-analog converter.</td>
</tr>
</tbody>
</table>
default  A preset value that is assumed to be correct unless changed by the user.
disk     A round platter, or set of platters, coated with magnetic medium and
         organized into concentric tracks for storing data.
disk drive The mechanism that rotates a disk.
diskette  A removable disk of magnetic medium for storing software and information.
diskette drive A device that reads and writes diskettes. If installed, the diskette drive access
         is located on the right side of the SPARCstation 20 system unit.
DMA      Direct memory access.
DRAM     Dynamic random access memory.
DSIMM    DRAM single in-line memory module. A small printed circuit card that
         contains DRAM chips.
Ecache   External cache is a cache that is located external to the microprocessor. See
         cache.
ECC      Error correcting code.
Ethernet A type of network hardware that allows communications between systems
         connected together by transceiver taps, transceiver cables, and a coaxial cable.
Forth    A high-level programming language. A major feature of Forth is that user-
         defined operators can be used as primitives.
Forth Toolkit An interactive command interpreter based upon the Forth programming
         language. The Toolkit provides access to an extensive set of functions for
         performing fault isolation and debugging.
frame buffer board A printed circuit board installed in an SBus slot of the system unit that controls
         a video display.
IC       Integrated circuit.
ID       Identification.
I/O      input/output. A device that inputs data to a computer CPU and receives data
         from a computer CPU.
keyboard Input device for entering data.
keyboard port The port on the SPARCstation 20 system unit where the keyboard cable is
         connected.
LANCE    Local area network control for Ethernet.
LED      Light emitting diode.
location See address.
MACIO  SBus-to-SCSI, Ethernet, and parallel interface. An ASIC that integrates three functions: DMA, LANCE, and fast SCSI bus interface. Provides the interface between the SBus and the following: SCSI, thick AUI Ethernet, TPE, and parallel port.

MBus module  A printed circuit board installed in an MBus slot of the system unit that contains the CPU circuitry and components.

Mbyte  Megabyte.

MDI  Memory display interface.

MMU  Memory management unit.

Monitor 1)  Indicated by the > prompt. From the > prompt, you can boot the system, continue the execution of a halted program, or enter the Forth Toolkit. If disaster happened to your operating system, the Monitor automatically starts.

Monitor 2)  A video display unit that is part of the SPARCstation 20 system. It is not the same as a terminal.

MSBI  MBus-to-SBus interface.

NVRAM  Non-volatile RAM.

NVSIMM  Non-volatile single in-line memory module.

on-board diagnostics  Tests that allow you to test the network controller, diskette drive system, memory, cache, system clock, and watch the network for valid Ethernet data packets.

Pok  Power ok. A signal from the power supply that is activated after DC voltages are nominal.

POST  Power-on self-test. Executes when you turn on the system power switch or reboot the system and press and hold the keyboard Stop (L1) and D keys. Initiated from the OpenBoot PROM, POST is a series of rudimentary tests designed to verify that the major components on the system board and the MBus modules installed within the system are operating properly.

printed circuit board  Any board with electronic wiring etched on it.

PROM  Programmable read-only memory.

RAM  Random access memory.

RH  Relative humidity.

ROM  Read-only memory.

SBus card  A printed circuit board installed in an SBus slot of the system unit.

SCC  Serial communications controller.
SCSI  Small computer system interface.
SEC  SBus-to-external bus interface. An ASIC ASIC that provides the interface between the SBus and the external bus.
SIA  Serial Interface Adapter.
SMC  Scalable memory controller.

**SunDiag System**

**Exerciser**  Diagnostic software that operates under SunOS system software; displays real-time use of system resources and peripheral equipment.

**SunVTS**  Sun verification test suite. Diagnostic software that operates under SunOS software.

**system monitor**  See *Monitor 1)* or *2)*.

**terminal**  A device that consists of a video display and keyboard. Used to type and display information. A terminal may be connected to either of the RS-423/RS-232 serial interface ports on the backpanel of the SPARCstation 20 system.

**TOD**  Time of day.

**TPE**  Twisted-pair Ethernet.

**UART**  Universal asynchronous receiver/transmitter.

**VBC**  Video buffer chip.

**VSIMM**  Video single in-line memory module. A printed circuit board containing static random access memory (SRAM) chips. A primary system VSIMM uses the backpanel 13W3 graphics connector. Availability of a secondary system VSIMM through conjoint operation with optional AVB.

**wrist strap**  A device that provides grounding for static electricity between your body and the system unit chassis. Electric current and voltage do not pass through the wrist strap.
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