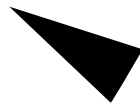


# SunOS Reference Manual

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*SunSoft*  
A Sun Microsystems, Inc. Business

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# *Preface*

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## *OVERVIEW*

A man page is provided for both the naive user, and sophisticated user who is familiar with the SunOS operating system and is in need of on-line information. A man page is intended to answer concisely the question “What does it do?” The man pages in general comprise a reference manual. They are not intended to be a tutorial.

The following contains a brief description of each section in the man pages and the information it references:

- Section 1 describes, in alphabetical order, commands available with the operating system.
- Section 1M describes, in alphabetical order, commands that are used chiefly for system maintenance and administration purposes.
- Section 2 describes all of the system calls. Most of these calls have one or more error returns. An error condition is indicated by an otherwise impossible returned value.
- Section 3 describes functions found in various libraries, other than those functions that directly invoke UNIX system primitives, which are described in Section 2 of this volume.

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- Section 4 outlines the formats of various files. The C structure declarations for the file formats are given where applicable.
  - Section 5 contains miscellaneous documentation such as character set tables, etc.
  - Section 6 contains available games and demos.
  - Section 7 describes various special files that refer to specific hardware peripherals, and device drivers. STREAMS software drivers, modules and the STREAMS-generic set of system calls are also described.
  - Section 9 provides reference information needed to write device drivers in the kernel operating systems environment. It describes two device driver interface specifications: the Device Driver Interface (DDI) and the Driver–Kernel Interface (DKI).
  - Section 9E describes the DDI/DKI, DDI-only, and DKI-only entry-point routines a developer may include in a device driver.
  - Section 9F describes the kernel functions available for use by device drivers.
  - Section 9S describes the data structures used by drivers to share information between the driver and the kernel.

Below is a generic format for man pages. The man pages of each manual section generally follow this order, but include only needed headings. For example, if there are no bugs to report, there is no BUGS section. See the intro pages for more information and detail about each section, and **man(1)** for more information about man pages in general.

## *NAME*

This section gives the names of the commands or functions documented, followed by a brief description of what they do.

## *SYNOPSIS*

This section shows the syntax of commands or functions. When a command or file does not exist in the standard path, its full pathname is shown. Literal characters (commands and options) are in **bold** font and variables (arguments, parameters and substitution characters) are in *italic* font. Options and

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arguments are alphabetized, with single letter arguments first, and options with arguments next, unless a different argument order is required.

The following special characters are used in this section:

- [ ] The option or argument enclosed in these brackets is optional. If the brackets are omitted, the argument *must* be specified.
- ... Ellipses. Several values may be provided for the previous argument, or the previous argument can be specified multiple times, for example, '*filename ...*'.
- | Separator. Only one of the arguments separated by this character can be specified at time.
- { } Braces. The options and/or arguments enclosed within braces are interdependent, such that everything enclosed must be treated as a unit.

## *PROTOCOL*

This section occurs only in subsection 3R to indicate the protocol description file. The protocol specification pathname is always listed in **bold** font.

## *DESCRIPTION*

This section defines the functionality and behavior of the service. Thus it describes concisely what the command does. It does not discuss **OPTIONS** or cite **EXAMPLES**. Interactive commands, subcommands, requests, macros, functions and such, are described under **USAGE**.

## *IOCTL*

This section appears on pages in Section 7 only. Only the device class which supplies appropriate parameters to the **ioctl(2)** system call is called **ioctl** and generates its own heading. **ioctl** calls for a specific device are listed alphabetically (on the man page for that specific device). **ioctl** calls are used for a particular class of devices all of which have an **io** ending, such as **mtio(7)**.

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## *OPTIONS*

This lists the command options with a concise summary of what each option does. The options are listed literally and in the order they appear in the SYNOPSIS section. Possible arguments to options are discussed under the option, and where appropriate, default values are supplied.

## *OPERANDS*

This section lists the command operands and describes how they affect the actions of the command.

## *OUTPUT*

This section describes the output - standard output, standard error, or output files - generated by the command.

## *RETURN VALUES*

If the man page documents functions that return values, this section lists these values and describes the conditions under which they are returned. If a function can return only constant values, such as 0 or -1, these values are listed in tagged paragraphs. Otherwise, a single paragraph describes the return values of each function. Functions declared as **void** do not return values, so they are not discussed in RETURN VALUES.

## *ERRORS*

On failure, most functions place an error code in the global variable **errno** indicating why they failed. This section lists alphabetically all error codes a function can generate and describes the conditions that cause each error. When more than one condition can cause the same error, each condition is described in a separate paragraph under the error code.

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## USAGE

This section is provided as a *guidance* on use. This section lists special rules, features and commands that require in-depth explanations. The subsections listed below are used to explain built-in functionality:

- Commands**
- Modifiers**
- Variables**
- Expressions**
- Input Grammar**

## EXAMPLES

This section provides examples of usage or of how to use a command or function. Wherever possible a complete example including command line entry and machine response is shown. Whenever an example is given, the prompt is shown as

**example%**

or if the user must be super-user,

**example#**

Examples are followed by explanations, variable substitution rules, or returned values. Most examples illustrate concepts from the SYNOPSIS, DESCRIPTION, OPTIONS and USAGE sections.

## ENVIRONMENT

This section lists any environment variables that the command or function affects, followed by a brief description of the effect.

## EXIT STATUS

This section lists the values the command returns to the calling program or shell and the conditions that cause these values to be returned. Usually, zero is returned for successful completion and values other than zero for various error conditions.

## FILES

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This section lists all filenames referred to by the man page, files of interest, and files created or required by commands. Each is followed by a descriptive summary or explanation.

## *ATTRIBUTES*

This section lists characteristics of commands, utilities, and device drivers by defining the attribute type and its corresponding value. (See **attributes(5)** for more information.)

## *SEE ALSO*

This section lists references to other man pages, in-house documentation and outside publications.

## *DIAGNOSTICS*

This section lists diagnostic messages with a brief explanation of the condition causing the error. Messages appear in **bold** font with the exception of variables, which are in *italic* font.

## *WARNINGS*

This section lists warnings about special conditions which could seriously affect your working conditions — this is not a list of diagnostics.

## *NOTES*

This section lists additional information that does not belong anywhere else on the page. It takes the form of an *aside* to the user, covering points of special interest. Critical information is never covered here.

## *BUGS*

This section describes known bugs and wherever possible suggests workarounds.



**NAME** Intro, intro – introduction to device driver entry points

**DESCRIPTION** Section 9E describes the entry-point routines a developer may include in a device driver. These are called entry-point because they provide the calling and return syntax from the kernel into the driver. Entry-points are called, for instance, in response to system calls, when the driver is loaded, or in response to STREAMS events.

Kernel functions usable by the driver are described in section 9F.

In this section, reference pages contain the following headings:

- **NAME** describes the routine's purpose.
- **SYNOPSIS** summarizes the routine's calling and return syntax.
- **INTERFACE LEVEL** describes any architecture dependencies. It also indicates whether the use of the entry point is required, optional, or discouraged.
- **ARGUMENTS** describes each of the routine's arguments.
- **DESCRIPTION** provides general information about the routine.
- **RETURN VALUES** describes each of the routine's return values.
- **SEE ALSO** gives sources for further information.

**Overview of Driver  
Entry-Point Routines  
and Naming  
Conventions**

By convention, a prefix string is added to the driver routine names. For a driver with the prefix *prefix*, the driver code may contain routines named *prefixopen*, *prefixclose*, *prefixread*, *prefixwrite*, and so forth. All global variables associated with the driver should also use the same prefix.

All routines and data should be declared as **static**.

Every driver MUST include `<sys/ddi.h>` and `<sys/sunddi.h>`, in that order, and after all other include files.

The following table summarizes the STREAMS driver entry points described in this section.

<b>Routine</b>	<b>Type</b>
<b>put</b>	DDI/DKI
<b>srv</b>	DDI/DKI

The following table summarizes the driver entry points described in this section.

<b>Routine</b>	<b>Type</b>
<b>_fini</b>	Solaris DDI
<b>_info</b>	Solaris DDI
<b>_init</b>	Solaris DDI
<b>aread</b>	Solaris DDI
<b>attach</b>	Solaris DDI
<b>awrite</b>	Solaris DDI
<b>chpoll</b>	DDI/DKI
<b>close</b>	DDI/DKI

<b>detach</b>	Solaris DDI
<b>devmap</b>	Solaris DDI
<b>devmap_access</b>	Solaris DDI
<b>devmap_contextmgt</b>	Solaris DDI
<b>devmap_dup</b>	Solaris DDI
<b>devmap_map</b>	Solaris DDI
<b>devmap_unmap</b>	Solaris DDI
<b>dump</b>	Solaris DDI
<b>getinfo</b>	Solaris DDI
<b>identify</b>	Solaris DDI
<b>ioctl</b>	DDI/DKI
<b>ks_update</b>	Solaris DDI
<b>mapdev_access</b>	Solaris DDI
<b>mapdev_dup</b>	Solaris DDI
<b>mapdev_free</b>	Solaris DDI
<b>mmap</b>	DKI only
<b>open</b>	DDI/DKI
<b>power</b>	Solaris DDI
<b>print</b>	DDI/DKI
<b>probe</b>	Solaris DDI
<b>prop_op</b>	Solaris DDI
<b>read</b>	DDI/DKI
<b>segmap</b>	DKI only
<b>strategy</b>	DDI/DKI
<b>tran_abort</b>	Solaris DDI
<b>tran_destroy_pkt</b>	Solaris DDI
<b>tran_dmafree</b>	Solaris DDI
<b>tran_getcap</b>	Solaris DDI
<b>tran_init_pkt</b>	Solaris DDI
<b>tran_reset</b>	Solaris DDI
<b>tran_reset_notify</b>	Solaris DDI
<b>tran_setcap</b>	Solaris DDI
<b>tran_start</b>	Solaris DDI
<b>tran_sync_pkt</b>	Solaris DDI
<b>tran_tgt_free</b>	Solaris DDI
<b>tran_tgt_init</b>	Solaris DDI
<b>tran_tgt_probe</b>	Solaris DDI
<b>write</b>	DDI/DKI

The following table lists the error codes returned by a driver routine when it encounters an error. The error values are listed in alphabetic order and are defined in `<sys/errno.h>`. In the driver **open**(9E), **close**(9E), **ioctl**(9E), **read**(9E), and **write**(9E) routines, errors are passed back to the user by returning the value. In the driver **strategy**(9E) routine, errors are passed back to the user by setting the **b\_error** member of the **buf**(9S) structure to the error code. For STREAMS **ioctl** routines, errors should be sent upstream in an **M\_IOCNAK** message. For STREAMS **read** and **write** routines, errors should be sent upstream in an

**M\_ERROR** message. The driver **print** routine should not return an error code because the function that it calls, **cmn\_err(9F)**, is declared as **void** (no error is returned).

Error Value	Error Description	Use in these Driver Routines (9E)
<b>EAGAIN</b>	Kernel resources, such as the <b>buf</b> structure or cache memory, are not available at this time (device may be busy, or the system resource is not available).	<b>open, ioctl, read, write, strategy</b>
<b>EFAULT</b>	An invalid address has been passed as an argument; memory addressing error.	<b>open, close, ioctl, read, write, strategy</b>
<b>EINTR</b>	Sleep interrupted by signal.	<b>open, close, ioctl, read, write, strategy</b>
<b>EINVAL</b>	An invalid argument was passed to the routine.	<b>open, ioctl, read, write, strategy</b>
<b>EIO</b>	A device error occurred; an error condition was detected in a device status register (the I/O request was valid, but an error occurred on the device).	<b>open, close, ioctl, read, write, strategy</b>
<b>ENXIO</b>	An attempt was made to access a device or subdevice that does not exist (one that is not configured); an attempt was made to perform an invalid I/O operation; an incorrect minor number was specified.	<b>open, close, ioctl, read, write, strategy</b>
<b>EPERM</b>	A process attempting an operation did not have required permission.	<b>open, ioctl, read, write, close</b>
<b>EROFS</b>	An attempt was made to open for writing a read-only device.	<b>open</b>

The table below cross references error values to the driver routines from which the error values can be returned.

<b>open</b>	<b>close</b>	<b>ioctl</b>	<b>read, write, and strategy</b>
<b>EAGAIN</b>	<b>EFAULT</b>	<b>EAGAIN</b>	<b>EAGAIN</b>
<b>EFAULT</b>	<b>EINTR</b>	<b>EFAULT</b>	<b>EFAULT</b>
<b>EINTR</b>	<b>EIO</b>	<b>EINTR</b>	<b>EINTR</b>
<b>EINVAL</b>	<b>ENXIO</b>	<b>EINVAL</b>	<b>EINVAL</b>
<b>EIO</b>		<b>EIO</b>	<b>EIO</b>
<b>ENXIO</b>		<b>ENXIO</b>	<b>ENXIO</b>
<b>EPERM</b>		<b>EPERM</b>	
<b>EROFS</b>			

Name	Description
<b>aread</b> (9E)	asynchronous read from a device
<b>attach</b> (9E)	attach a device to the system, or resume it
<b>awrite</b> (9E)	asynchronous write to a device
<b>chpoll</b> (9E)	poll entry point for a non-STREAMS character driver
<b>close</b> (9E)	relinquish access to a device
<b>csx_event_handler</b> (9E)	PC Card driver event handler
<b>detach</b> (9E)	detach a device
<b>devmap</b> (9E)	validate and translate virtual mapping for memory mapped device
<b>devmap_access</b> (9E)	device mapping access entry point
<b>devmap_contextmgt</b> (9E)	driver callback function for context management
<b>devmap_dup</b> (9E)	device mapping duplication entry point
<b>devmap_map</b> (9E)	device mapping create entry point
<b>devmap_unmap</b> (9E)	device mapping unmap entry point
<b>dump</b> (9E)	dump memory to device during system failure
<b>_fini</b> (9E)	loadable module configuration entry points
<b>getinfo</b> (9E)	get device driver information
<b>identify</b> (9E)	determine if a driver is associated with a device
<b>_info</b> (9E)	See <b>_fini</b> (9E)
<b>_init</b> (9E)	See <b>_fini</b> (9E)
<b>ioctl</b> (9E)	control a character device
<b>ks_update</b> (9E)	dynamically update kstats
<b>mapdev_access</b> (9E)	device mapping access entry point
<b>mapdev_dup</b> (9E)	device mapping duplication entry point
<b>mapdev_free</b> (9E)	device mapping free entry point
<b>mmap</b> (9E)	check virtual mapping for memory mapped device
<b>open</b> (9E)	gain access to a device
<b>pm</b> (9E)	power management properties
<b>power</b> (9E)	power a device attached to the system
<b>print</b> (9E)	display a driver message on system console
<b>probe</b> (9E)	determine if a non-self-identifying device is present
<b>prop_op</b> (9E)	report driver property information
<b>put</b> (9E)	receive messages from the preceding queue
<b>read</b> (9E)	read data from a device

<b>segmap(9E)</b>	map device memory into user space
<b>srv(9E)</b>	service queued messages
<b>strategy(9E)</b>	perform block I/O
<b>tran_abort(9E)</b>	abort a SCSI command
<b>tran_destroy_pkt(9E)</b>	See <b>tran_init_pkt(9E)</b>
<b>tran_dmafree(9E)</b>	SCSI HBA DMA deallocation entry point
<b>tran_getcap(9E)</b>	get/set SCSI transport capability
<b>tran_init_pkt(9E)</b>	SCSI HBA packet preparation and deallocation
<b>tran_reset(9E)</b>	reset a SCSI bus or target
<b>tran_reset_notify(9E)</b>	request to notify SCSI target of bus reset
<b>tran_setcap(9E)</b>	See <b>tran_getcap(9E)</b>
<b>tran_start(9E)</b>	request to transport a SCSI command
<b>tran_sync_pkt(9E)</b>	SCSI HBA memory synchronization entry point
<b>tran_tgt_free(9E)</b>	request to free HBA resources allocated on behalf of a target
<b>tran_tgt_init(9E)</b>	request to initialize HBA resources on behalf of a particular target
<b>tran_tgt_probe(9E)</b>	request to probe SCSI bus for a particular target
<b>write(9E)</b>	write data to a device

<b>NAME</b>	aread – asynchronous read from a device
<b>SYNOPSIS</b>	<pre>#include &lt;sys/uio.h&gt; #include &lt;sys/aio_req.h&gt; #include &lt;sys/cred.h&gt; #include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt;  int prefixaread(dev_t dev, struct aio_req *aio_reqp, cred_t *cred_p);</pre>
<b>INTERFACE LEVEL</b>	Solaris DDI specific (Solaris DDI). This entry point is <i>optional</i> . Drivers that do not support an <b>aread()</b> entry point should use <b>nodev(9F)</b> .
<b>ARGUMENTS</b>	<p><i>dev</i> Device number.</p> <p><i>aio_reqp</i> Pointer to the <b>aio_req(9S)</b> structure that describes where the data is to be stored.</p> <p><i>cred_p</i> Pointer to the credential structure.</p>
<b>DESCRIPTION</b>	<p>The driver's <b>aread()</b> routine is called to perform an asynchronous read. <b>getminor(9F)</b> can be used to access the minor number component of the <i>dev</i> argument. <b>aread()</b> may use the credential structure pointed to by <i>cred_p</i> to check for superuser access by calling <b>drv_priv(9F)</b>. The <b>aread()</b> routine may also examine the <b>uio(9S)</b> structure through the <b>aio_req</b> structure pointer, <i>aio_reqp</i>. <b>aread()</b> must call <b>aphysio(9F)</b> with the <b>aio_req</b> pointer and a pointer to the driver's <b>strategy(9E)</b> routine.</p> <p>No fields of the <b>uio(9S)</b> structure pointed to by <b>aio_req</b>, other than <b>uio_offset</b> or <b>uio_loffset</b>, may be modified for non-seekable devices.</p>
<b>RETURN VALUES</b>	The <b>aread()</b> routine should return <b>0</b> for success, or the appropriate error number.
<b>CONTEXT</b>	This function is called from user context only.
<b>EXAMPLES</b>	<p>The following is an example of an <b>aread()</b> routine:</p> <pre>static int xxaread(dev_t dev, struct aio_req *aio, cred_t *cred_p) {     int instance;     struct xxstate *xsp;     instance = getminor(dev);     xsp = ddi_get_soft_state(statep, instance);</pre>

```
/*Verify soft state structure has been allocated */  
if (xsp == NULL)  
    return (ENXIO);  
    return (aphysio(xxstrategy, anocancel, dev, B_READ, xxminphys, aio));  
}
```

**SEE ALSO** [read\(2\)](#), [aioread\(3\)](#), [awrite\(9E\)](#), [read\(9E\)](#), [strategy\(9E\)](#), [write\(9E\)](#), [anocancel\(9F\)](#), [aphysio\(9F\)](#), [ddi\\_get\\_soft\\_state\(9F\)](#), [drv\\_priv\(9F\)](#), [getminor\(9F\)](#), [minphys\(9F\)](#), [nodev\(9F\)](#), [aio\\_req\(9S\)](#), [cb\\_ops\(9S\)](#), [uio\(9S\)](#)

*Writing Device Drivers*

**BUGS** There is no way other than calling [aphysio\(9F\)](#) to accomplish an asynchronous read.

<b>NAME</b>	attach – attach a device to the system, or resume it
<b>SYNOPSIS</b>	<pre>#include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt; int prefixattach(dev_info_t *dip, ddi_attach_cmd_t cmd);</pre>
<b>INTERFACE LEVEL ARGUMENTS</b>	<p>Solaris DDI specific (Solaris DDI).</p> <p><i>dip</i>     A pointer to the device's <b>dev_info</b> structure.</p> <p><i>cmd</i>     Attach type. Possible values are <b>DDI_ATTACH</b>, <b>DDI_PM_RESUME</b>, and <b>DDI_RESUME</b>. Other values are reserved. The driver must return <b>DDI_FAILURE</b> if reserved values are passed to it.</p>
<b>DESCRIPTION</b>	<p>The <b>attach()</b> function is the device-specific initialization entry point. This entry point is <i>required</i> and must be written. The <b>DDI_ATTACH</b> command must be provided in the <b>attach</b> entry point. The <b>DDI_PM_RESUME</b> command is optional, but must be supported if the driver will support power management. See <b>pm(7D)</b>. The <b>DDI_RESUME</b> command is optional, but must be supported if the driver will support the processes of suspend and resume. See <b>cpr(7)</b>. When <b>attach()</b> is called with <i>cmd</i> set to <b>DDI_ATTACH</b>, all normal kernel services (such as <b>kmem_alloc(9F)</b>) are available for use by the driver. Device interrupts are not blocked when attaching a device to the system.</p> <p>The <b>attach()</b> function will be called once for each instance of the device on the system with <i>cmd</i> set to <b>DDI_ATTACH</b>. Until <b>attach()</b> succeeds, the only driver entry points which may be called are <b>open(9E)</b> and <b>getinfo(9E)</b>. See the "Autoconfiguration" chapter in <i>Writing Device Drivers</i>. The instance number may be obtained using <b>ddi_get_instance(9F)</b>.</p>
<b>DDI_PM_RESUME</b>	<p>The <b>attach()</b> function may be called with <i>cmd</i> set to <b>DDI_PM_RESUME</b> after <b>detach(9E)</b> has been successfully called with <i>cmd</i> set to <b>DDI_PM_SUSPEND</b>. When called with <i>cmd</i> set to <b>DDI_PM_RESUME</b>, <b>attach()</b> must restore the hardware state of a device (power may have been removed from the device), allow pending requests to continue, and service new requests.</p> <p>The driver must not make any assumptions about the state of the hardware, but must restore it to the state it had when the <b>detach(9E)</b> entry point was called with <b>DDI_PM_SUSPEND</b>.</p>
<b>DDI_RESUME</b>	<p>The <b>attach()</b> function may be called with <i>cmd</i> set to <b>DDI_RESUME</b> after <b>detach(9E)</b> has been successfully called with <i>cmd</i> set to <b>DDI_SUSPEND</b>.</p> <p>If the device is still suspended by <b>DDI_PM_SUSPEND</b>, the only effect of <b>DDI_RESUME</b> is to allow the driver to call <b>ddi_dev_is_needed(9F)</b> for any new or pending requests, as a subsequent call to <b>attach()</b> will be made with <i>cmd</i> set to <b>DDI_PM_RESUME</b> to restore the hardware state.</p>



When called with *cmd* set to **DDI\_RESUME**, **attach()** must restore the hardware state of a device (power may have been removed from the device), allow pending requests to continue, and service new requests. In this case, the driver must not make any assumptions about the state of the hardware, but must restore it to the state it had when the **detach(9E)** entry point was called with **DDI\_SUSPEND**.

**RETURN VALUES**

The **attach()** function returns:

**DDI\_SUCCESS** Successful completion.

**DDI\_FAILURE** The operation failed.

**SEE ALSO**

**cpr(7)**, **pm(7D)**, **detach(9E)**, **getinfo(9E)**, **identify(9E)**, **open(9E)**, **pm(9E)**, **probe(9E)**, **ddi\_add\_intr(9F)**, **ddi\_create\_minor\_node(9F)**, **ddi\_get\_instance(9F)**, **ddi\_map\_regs(9F)**, **kmem\_alloc(9F)**

*Writing Device Drivers*

<b>NAME</b>	awrite – asynchronous write to a device
<b>SYNOPSIS</b>	<pre>#include &lt;sys/uio.h&gt; #include &lt;sys/aio_req.h&gt; #include &lt;sys/cred.h&gt; #include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt;  int prefixawrite(dev_t dev, struct aio_req *aio_req, cred_t *cred_p);</pre>
<b>INTERFACE LEVEL</b>	Solaris DDI specific (Solaris DDI). This entry point is <i>optional</i> . Drivers that do not support an <b>awrite ()</b> entry point should use <b>nodev(9F)</b> .
<b>ARGUMENTS</b>	<p><i>dev</i>            Device number.</p> <p><i>aio_req</i>        Pointer to the <b>aio_req(9S)</b> structure that describes where the data is stored.</p> <p><i>cred_p</i>         Pointer to the credential structure.</p>
<b>DESCRIPTION</b>	<p>The driver's <b>awrite()</b> routine is called to perform an asynchronous write. <b>getminor(9F)</b> can be used to access the minor number component of the <i>dev</i> argument. <b>awrite()</b> may use the credential structure pointed to by <i>cred_p</i> to check for superuser access by calling <b>drv_priv(9F)</b>. The <b>awrite()</b> routine may also examine the <b>uio(9S)</b> structure through the <b>aio_req</b> structure pointer, <i>aio_reqp</i>. <b>awrite()</b> must call <b>aphysio(9F)</b> with the <b>aio_req</b> pointer and a pointer to the driver's <b>strategy(9E)</b> routine.</p> <p>No fields of the <b>uio(9S)</b> structure pointed to by <b>aio_req</b>, other than <b>uio_offset</b> or <b>uio_loffset</b>, may be modified for non-seekable devices.</p>
<b>RETURN VALUES</b>	The <b>awrite()</b> routine should return <b>0</b> for success, or the appropriate error number.
<b>CONTEXT</b>	This function is called from user context only.
<b>EXAMPLES</b>	<p>The following is an example of an <b>awrite()</b> routine:</p> <pre>static int xxawrite(dev_t dev, struct aio_req *aio, cred_t *cred_p) {     int instance;     struct xxstate *xsp;     instance = getminor(dev);     xsp = ddi_get_soft_state(statep, instance);</pre>

```
/*Verify soft state structure has been allocated */
if (xsp == NULL)
    return (ENXIO);
return (aphysio(xxstrategy, anocancel, dev, B_WRITE, xxminphys, aio));
}
```

**SEE ALSO** write(2), aiowrite(3), aread(9E), read(9E), strategy(9E), write(9E), anocancel(9F), aphysio(9F), ddi\_get\_soft\_state(9F), drv\_priv(9F), getminor(9F), minphys(9F), nodev(9F), aio\_req(9S), cb\_ops(9S), uio(9S)

*Writing Device Drivers*

**BUGS** There is no way other than calling **aphysio(9F)** to accomplish an asynchronous write.

<b>NAME</b>	chpoll – poll entry point for a non-STREAMS character driver																		
<b>SYNOPSIS</b>	<pre>#include &lt;sys/types.h&gt; #include &lt;sys/poll.h&gt; #include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt;  int prefixchpoll(dev_t dev, short events, int anyyet, short *reventsp,                 struct pollhead **phpp);</pre>																		
<b>INTERFACE LEVEL</b>	This entry point is <i>optional</i> . Architecture independent level 1 (DDI/DKI).																		
<b>ARGUMENTS</b>	<p><i>dev</i>        The device number for the device to be polled.</p> <p><i>events</i>     The events that may occur. Valid events are:</p> <table border="0"> <tr> <td style="padding-left: 2em;"><b>POLLIN</b></td> <td>Data other than high priority data may be read without blocking.</td> </tr> <tr> <td style="padding-left: 2em;"><b>POLLOUT</b></td> <td>Normal data may be written without blocking.</td> </tr> <tr> <td style="padding-left: 2em;"><b>POLLPRI</b></td> <td>High priority data may be received without blocking.</td> </tr> <tr> <td style="padding-left: 2em;"><b>POLLHUP</b></td> <td>A device hangup has occurred.</td> </tr> <tr> <td style="padding-left: 2em;"><b>POLLERR</b></td> <td>An error has occurred on the device.</td> </tr> <tr> <td style="padding-left: 2em;"><b>POLLRDNORM</b></td> <td>Normal data (priority band = 0) may be read without blocking.</td> </tr> <tr> <td style="padding-left: 2em;"><b>POLLRDBAND</b></td> <td>Data from a non-zero priority band may be read without blocking</td> </tr> <tr> <td style="padding-left: 2em;"><b>POLLWRNORM</b></td> <td>The same as <b>POLLOUT</b>.</td> </tr> <tr> <td style="padding-left: 2em;"><b>POLLWRBAND</b></td> <td>Priority data (priority band &gt; 0) may be written.</td> </tr> </table> <p><i>anyyet</i>     A flag that is non-zero if any other file descriptors in the <b>pollfd</b> array have events pending. The <b>poll(2)</b> system call takes a pointer to an array of <b>pollfd</b> structures as one of its arguments. See the <b>poll(2)</b> reference page for more details.</p> <p><i>reventsp</i>    A pointer to a bitmask of the returned events satisfied.</p> <p><i>phpp</i>        A pointer to a pointer to a <b>pollhead</b> structure.</p>	<b>POLLIN</b>	Data other than high priority data may be read without blocking.	<b>POLLOUT</b>	Normal data may be written without blocking.	<b>POLLPRI</b>	High priority data may be received without blocking.	<b>POLLHUP</b>	A device hangup has occurred.	<b>POLLERR</b>	An error has occurred on the device.	<b>POLLRDNORM</b>	Normal data (priority band = 0) may be read without blocking.	<b>POLLRDBAND</b>	Data from a non-zero priority band may be read without blocking	<b>POLLWRNORM</b>	The same as <b>POLLOUT</b> .	<b>POLLWRBAND</b>	Priority data (priority band > 0) may be written.
<b>POLLIN</b>	Data other than high priority data may be read without blocking.																		
<b>POLLOUT</b>	Normal data may be written without blocking.																		
<b>POLLPRI</b>	High priority data may be received without blocking.																		
<b>POLLHUP</b>	A device hangup has occurred.																		
<b>POLLERR</b>	An error has occurred on the device.																		
<b>POLLRDNORM</b>	Normal data (priority band = 0) may be read without blocking.																		
<b>POLLRDBAND</b>	Data from a non-zero priority band may be read without blocking																		
<b>POLLWRNORM</b>	The same as <b>POLLOUT</b> .																		
<b>POLLWRBAND</b>	Priority data (priority band > 0) may be written.																		
<b>DESCRIPTION</b>	The <b>chpoll()</b> entry point routine is used by non-STREAMS character device drivers that wish to support polling. The driver must implement the polling discipline itself. The following rules must be followed when implementing the polling discipline:																		

1. Implement the following algorithm when the **chpoll()** entry point is called:  

```
if (events_are_satisfied_now) {
    *reventsp = mask_of_satisfied_events;
} else {
    *reventsp = 0;
    if (!anyyet)
        *phpp = &my_local_pollhead_structure;
}
return (0);
```
2. Allocate an instance of the **pollhead** structure. This instance may be tied to the per-minor data structure defined by the driver. The **pollhead** structure should be treated as a “black box” by the driver. None of its fields should be referenced. However, the size of this structure is guaranteed to remain the same across releases.
3. Call the **pollwakeup()** function whenever an event of type **events** listed above occur. This function should only be called with one event at a time.

**RETURN VALUES**

**chpoll()** should return **0** for success, or the appropriate error number.

**SEE ALSO**

**poll(2)**, **nochpoll(9F)**, **pollwakeup(9F)**

*Writing Device Drivers*

**NOTES**

Driver defined locks should not be held across calls to this function.

<b>NAME</b>	close – relinquish access to a device
<b>SYNOPSIS</b> Block and Character	<pre>#include &lt;sys/types.h&gt; #include &lt;sys/file.h&gt; #include &lt;sys/errno.h&gt; #include &lt;sys/open.h&gt; #include &lt;sys/cred.h&gt; #include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt;  int prefixclose(dev_t dev, int flag, int otyp, cred_t *cred_p);</pre>
<b>STREAMS</b>	<pre>#include &lt;sys/types.h&gt; #include &lt;sys/stream.h&gt; #include &lt;sys/file.h&gt; #include &lt;sys/errno.h&gt; #include &lt;sys/open.h&gt; #include &lt;sys/cred.h&gt; #include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt;  int prefixclose(queue_t *q, int flag, cred_t *cred_p);</pre>
<b>INTERFACE LEVEL</b>	Architecture independent level 1 (DDI/DKI). This entry point is <b>required</b> for block devices.
<b>ARGUMENTS</b> Block and Character	<p><i>dev</i> Device number.</p> <p><i>flag</i> File status flag, as set by the <b>open(2)</b> or modified by the <b>fcntl(2)</b> system calls. The flag is for information only—the file should always be closed completely. Possible values are: <b>FEXCL</b>, <b>FNDELAY</b>, <b>FREAD</b>, <b>FKLYR</b>, and <b>FWRITE</b>. Refer to <b>open(9E)</b> for more information.</p> <p><i>otyp</i> Parameter supplied so that the driver can determine how many times a device was opened and for what reasons. The flags assume the <b>open()</b> routine may be called many times, but the <b>close()</b> routine should only be called on the last <b>close</b> of a device.</p> <p><b>OTYP_BLK</b> close was through block interface for the device</p> <p><b>OTYP_CHR</b> close was through the raw/character interface for the device</p> <p><b>OTYP_LYR</b> close a layered process (a higher-level driver called the <b>close()</b> routine of the device)</p> <p><i>*cred_p</i> Pointer to the user credential structure.</p>

## STREAMS

- \*q* Pointer to **queue**(9S) structure used to reference the read side of the driver. (A queue is the central node of a collection of structures and routines pointed to by a queue.)
- flag* File status flag.
- \*cred\_p* Pointer to the user credential structure.

## DESCRIPTION

For STREAMS drivers, the **close()** routine is called by the kernel through the **cb\_ops**(9S) table entry for the device. (Modules use the **fmodsw** table.) A non-null value in the **d\_str** field of the **cb\_ops** entry points to a **streamtab** structure, which points to a **qinit**(9S) containing a pointer to the **close()** routine. Non-STREAMS **close()** routines are called directly from the **cb\_ops** table.

**close()** ends the connection between the user process and the device, and prepares the device (hardware and software) so that it is ready to be opened again.

A device may be opened simultaneously by multiple processes and the **open()** driver routine is called for each open, but the kernel will only call the **close()** routine when the last process using the device issues a **close(2)** or **umount(2)** system call or exits. (An exception is a close occurring with the *otyp* argument set to **OTYP\_LYR**, for which a close (also having *otyp* = **OTYP\_LYR**) occurs for each open.)

In general, a **close()** routine should always check the validity of the minor number component of the *dev* parameter. The routine should also check permissions as necessary, by using the user credential structure (if pertinent), and the appropriateness of the *flag* and *otyp* parameter values.

**close()** could perform any of the following general functions:

- disable interrupts
- hang up phone lines
- rewind a tape
- deallocate buffers from a private buffering scheme
- unlock an unsharable device (that was locked in the **open()** routine)
- flush buffers
- notify a device of the close
- deallocate any resources allocated on open

The **close()** routines of STREAMS drivers and modules are called when a stream is dismantled or a module popped. The steps for dismantling a stream are performed in the following order. First, any multiplexor links present are unlinked and the lower streams are closed. Next, the following steps are performed for each module or driver on the stream, starting at the head and working toward the tail:

1. The write queue is given a chance to drain.
2. The **close()** routine is called.
3. The module or driver is removed from the stream.

**RETURN VALUES**

**close()** should return 0 for success, or the appropriate error number. Return errors rarely occur, but if a failure is detected, the driver should decide whether the severity of the problem warrants either displaying a message on the console or, in worst cases, triggering a system panic. Generally, a failure in a **close()** routine occurs because a problem occurred in the associated device.

**SEE ALSO**

**close(2)**, **fcntl(2)**, **open(2)**, **umount(2)**, **detach(9E)**, **open(9E)**, **cb\_ops(9S)**, **qinit(9S)**, **queue(9S)**

*Writing Device Drivers*

*STREAMS Programming Guide*



<b>NAME</b>	csx_event_handler – PC Card driver event handler
<b>SYNOPSIS</b>	<pre>#include &lt;sys/pccard.h&gt; int32_t prefix event_handler(event_t event, int32_t priority,     event_callback_args_t *args);</pre>
<b>INTERFACE LEVEL</b>	Solaris architecture specific (Solaris DDI)
<b>ARGUMENTS</b>	<p><i>event</i>            The event.</p> <p><i>priority</i>        The priority of the event.</p> <p><i>args</i>             A pointer to the <b>event_callback_t</b> structure.</p>
<b>DESCRIPTION</b>	<p>Each instance of a PC Card driver must register an event handler to manage events associated with its PC Card. The driver event handler is registered using the <b>event_handler</b> field of the <b>client_req_t</b> structure passed to <b>csx_RegisterClient(9F)</b>. The driver may also supply a parameter to be passed to its event handler function using the <b>event_callback_args.client_data</b> field. Typically, this argument is the driver instance's soft state pointer. The driver also registers which events it is interested in receiving through the <b>EventMask</b> field of the <b>client_req_t</b> structure.</p> <p>Each event is delivered to the driver with a priority, <i>priority</i>. High priority events with <b>CS_EVENT_PRI_HIGH</b> set in <i>priority</i> are delivered above lock level, and the driver must use its high-level event mutex initialized with the <b>iblk_cookie</b> returned by <b>csx_RegisterClient(9F)</b> to protect such events. Low priority events with <b>CS_EVENT_PRI_LOW</b> set in <i>priority</i> are delivered below lock level, and the driver must use its low-level event mutex initialized with a NULL interrupt cookie to protect these events.</p> <p><b>csx_RegisterClient(9F)</b> registers the driver's event handler, but no events begin to be delivered to the driver until after a successful call to <b>csx_RequestSocketMask(9F)</b>.</p> <p>In all cases, Card Services delivers an event to each driver instance associated with a function on a multiple function PC Card.</p>
<b>Event Indications</b>	<p>The events and their indications are listed below; they are always delivered as low priority unless otherwise noted:</p> <p><b>CS_EVENT_REGISTRATION_COMPLETE</b> A registration request processed in the background has been completed.</p> <p><b>CS_EVENT_CARD_INSERTION</b> A PC Card has been inserted in a socket.</p> <p><b>CS_EVENT_CARD_READY</b> A PC Card's READY line has transitioned from the busy to ready state.</p>

**CS\_EVENT\_CARD\_REMOVAL**

A PC Card has been removed from a socket.

This event is delivered twice; first as a high priority event, followed by delivery as a low priority event.

As a high priority event, the event handler should only note that the PC Card is no longer present to prevent accesses to the hardware from occurring.

As a low priority event, the event handler should release the configuration and free all I/O, window and IRQ resources for use by other PC Cards.

**CS\_EVENT\_BATTERY\_LOW**

The battery on a PC Card is weak and is in need of replacement.

**CS\_EVENT\_BATTERY\_DEAD**

The battery on a PC Card is no longer providing operational voltage.

**CS\_EVENT\_PM\_RESUME**

Card Services has received a resume notification from the system's power management software.

**CS\_EVENT\_PM\_SUSPEND**

Card Services has received a suspend notification from the system's power management software.

**CS\_EVENT\_CARD\_LOCK**

A mechanical latch has been manipulated preventing the removal of the PC Card from the socket.

**CS\_EVENT\_CARD\_UNLOCK**

A mechanical latch has been manipulated allowing the removal of the PC Card from the socket.

**CS\_EVENT\_EJECTION\_REQUEST**

A request that the PC Card be ejected from a socket using a motor-driven mechanism.

**CS\_EVENT\_EJECTION\_COMPLETE**

A motor has completed ejecting a PC Card from a socket.

**CS\_EVENT\_ERASE\_COMPLETE**

A queued erase request that is processed in the background has been completed.

**CS\_EVENT\_INSERTION\_REQUEST**

A request that a PC Card be inserted into a socket using a motor-driven mechanism.

**CS\_EVENT\_INSERTION\_COMPLETE**

A motor has completed inserting a PC Card in a socket.

**CS\_EVENT\_CARD\_RESET**

A hardware reset has occurred.

**CS\_EVENT\_RESET\_REQUEST**

A request for a physical reset by a client.

**CS\_EVENT\_RESET\_COMPLETE**

A reset request that is processed in the background has been completed.

**CS\_EVENT\_RESET\_PHYSICAL**

A reset is about to occur.

**CS\_EVENT\_CLIENT\_INFO**

A request that the client return its client information data. If

**GET\_CLIENT\_INFO\_SUBSVC(args->client\_info.Attributes)** is equal to **CS\_CLIENT\_INFO\_SUBSVC\_CS**, the driver should fill in the other fields in the **client\_info** structure as described below, and return **CS\_SUCCESS**. Otherwise, it should return **CS\_UNSUPPORTED\_EVENT**.

**args->client\_data.Attributes**

Must be **OR**'ed with **CS\_CLIENT\_INFO\_VALID**.

**args->client\_data.Revision**

Must be set to a driver-private version number.

**args->client\_data.CSLevel**

Must be set to **CS\_VERSION**.

**args->client\_data.RevDate**

Must be set to the revision date of the PC Card driver, using **CS\_CLIENT\_INFO\_MAKE\_DATE(day, month, year)**. *day* must be the day of the month, *month* must be the month of the year, and *year* must be the year, offset from a base of 1980. For example, this field could be set to a revision date of July 4 1997 with **CS\_CLIENT\_INFO\_MAKE\_DATE(4, 7, 17)**.

**args->client\_data.ClientName**

A string describing the PC Card driver should be copied into this space.

**args->client\_data.VendorName**

A string supplying the name of the PC Card driver vendor should be copied into this space.

**args->client\_data.DriverName**

A string supplying the name of the PC Card driver will be copied into this space by Card Services after the PC Card driver has successfully processed this event; the driver does not need to initialize this field.

**CS\_EVENT\_WRITE\_PROTECT**

The write protect status of the PC Card in the indicated socket has changed. The current write protect state of the PC Card is in the **args->info** field:

**CS\_EVENT\_WRITE\_PROTECT\_WPOFF**

Card is not write protected.

**CS\_EVENT\_WRITE\_PROTECT\_WPON**

Card is write protected.

**STRUCTURE MEMBERS**

The structure members of `event_callback_args_t` are:

```
void          *info;          /* event-specific information */
void          *client_data;   /* driver-private data */
client_info_t client_info;    /* client information */
```

The structure members of `client_info_t` are:

```
uint32_t      Attributes;     /* attributes */
uint32_t      Revision;       /* version number */
uint32_t      CSLevel;        /* Card Services version */
uint32_t      RevDate;        /* revision date */
char          ClientName[CS_CLIENT_INFO_MAX_NAME_LEN];
/* PC Card driver description */
char          VendorName[CS_CLIENT_INFO_MAX_NAME_LEN];
/* PC Card driver vendor name */
char          DriverName[MODMAXNAMELEN];
/* PC Card driver name */
```

**RETURN VALUES**

```
CS_SUCCESS      The event was handled successfully.
CS_UNSUPPORTED_EVENT Driver does not support this event.
CS_FAILURE      Error occurred while handling this event.
```

**CONTEXT**

This function is called from high-level interrupt context in the case of high priority events, and from kernel context in the case of low priority events.

**EXAMPLES**

```
static int
xx_event(event_t event, int priority, event_callback_args_t *args)
{
    int          rval;
    struct xxx   *xxx = args->client_data;
    client_info_t *info = &args->client_info;

    switch (event) {
    case CS_EVENT_REGISTRATION_COMPLETE:
        ASSERT(priority & CS_EVENT_PRI_LOW);
        mutex_enter(&xxx->event_mutex);
        xxx->card_state |= XX_REGISTRATION_COMPLETE;
        mutex_exit(&xxx->event_mutex);
        rval = CS_SUCCESS;
        break;

    case CS_EVENT_CARD_READY:
        ASSERT(priority & CS_EVENT_PRI_LOW);
        mutex_enter(&xxx->event_mutex);
        rval = xx_card_ready(xxx);
        mutex_exit(&xxx->event_mutex);
        break;
    }
```

```

case CS_EVENT_CARD_INSERTION:
    ASSERT(priority & CS_EVENT_PRI_LOW);
    mutex_enter(&xxx->event_mutex);
    rval = xx_card_insertion(xxx);
    mutex_exit(&xxx->event_mutex);
    break;

case CS_EVENT_CARD_REMOVAL:
    if (priority & CS_EVENT_PRI_HIGH) {
        mutex_enter(&xxx->hi_event_mutex);
        xxx->card_state &= ~XX_CARD_PRESENT;
        mutex_exit(&xxx->hi_event_mutex);
    } else {
        mutex_enter(&xxx->event_mutex);
        rval = xx_card_removal(xxx);
        mutex_exit(&xxx->event_mutex);
    }
    break;

case CS_EVENT_CLIENT_INFO:
    ASSERT(priority & CS_EVENT_PRI_LOW);
    if (GET_CLIENT_INFO_SUBSVC_CS(info->Attributes) ==
        CS_CLIENT_INFO_SUBSVC_CS) {
        info->Attributes |= CS_CLIENT_INFO_VALID;
        info->Revision = 4;
        info->CSLevel = CS_VERSION;
        info->RevDate = CS_CLIENT_INFO_MAKE_DATE(4, 7, 17);
        (void)strncpy(info->ClientName,
            "WhizBang Ultra Zowie PC card driver",
            CS_CLIENT_INFO_MAX_NAME_LEN);
        (void)strncpy(info->VendorName,
            "ACME PC card drivers, Inc.",
            CS_CLIENT_INFO_MAX_NAME_LEN);
        rval = CS_SUCCESS;
    } else {
        rval = CS_UNSUPPORTED_EVENT;
    }
    break;

case CS_EVENT_WRITE_PROTECT:
    ASSERT(priority & CS_EVENT_PRI_LOW);
    mutex_enter(&xxx->event_mutex);
    if (args->info == CS_EVENT_WRITE_PROTECT_WPOFF) {
        xxx->card_state &= ~XX_WRITE_PROTECTED;
    }

```

```
    } else {
        xxx->card_state |= XX_WRITE_PROTECTED;
    }
    mutex_exit(&xxx->event_mutex);
    rval = CS_SUCCESS;
    break;

default:
    rval = CS_UNSUPPORTED_EVENT;
    break;
}

return (rval);
}
```

**SEE ALSO**

**csx\_Event2Text(9F), csx\_RegisterClient(9F), csx\_RequestSocketMask(9F)**  
*PC Card 95 Standard, PCMCIA/JEIDA*

<b>NAME</b>	detach – detach a device
<b>SYNOPSIS</b>	<pre>#include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt; int prefixdetach(dev_info_t *dip, ddi_detach_cmd_t cmd);</pre>
<b>INTERFACE LEVEL</b>	Solaris DDI specific (Solaris DDI). This entry point is required. If it is <b>nodev</b> , the device will not support suspend/resume or power management (see <b>cpr(7)</b> , <b>pm(7)</b> , and <b>pm(9E)</b> ).
<b>ARGUMENTS</b>	<p><i>dip</i>     A pointer to the device's <b>dev_info</b> structure.</p> <p><i>cmd</i>     Type of detach; the driver should return <b>DDI_FAILURE</b> if any value other than <b>DDI_DETACH</b>, <b>DDI_PM_SUSPEND</b>, or <b>DDI_SUSPEND</b> is passed to it.</p>
<b>DESCRIPTION</b>	The <b>detach()</b> function is the complement of the <b>attach(9E)</b> routine.
<b>DDI_DETACH</b>	<p>If <i>cmd</i> is set to <b>DDI_DETACH</b>, <b>detach()</b> is used to remove the state associated with a given instance of a device node prior to the removal of that instance from the system.</p> <p>The <b>detach()</b> function will be called once for each instance of the device for which there has been a successful <b>attach()</b> once there are no longer any opens on the device. The <b>detach()</b> function should clean up any per instance data initialized in <b>attach(9E)</b> and call <b>kmem_free(9F)</b> to free any heap allocations. For information on how to unregister interrupt handlers see <b>ddi_add_intr(9F)</b>. This should also include putting the underlying device into a quiescent state so that it will not generate interrupts.</p> <p>Drivers that set up <b>timeout(9F)</b> routines should ensure that they are cancelled before returning <b>DDI_SUCCESS</b> from <b>detach()</b>.</p> <p>If <b>detach()</b> determines a particular instance of the device cannot be removed when requested because of some exceptional condition, <b>detach()</b> must return <b>DDI_FAILURE</b>, which prevents the particular device instance from being detached. This will also prevent the driver from being unloaded.</p> <p>The system guarantees that the function will only be called for a particular <b>dev_info</b> node after (and not concurrently with) a successful <b>attach(9E)</b> of that device. The system also guarantees that <b>detach()</b> will only be called when there are no outstanding <b>open(9E)</b> calls on the device.</p>
<b>DDI_PM_SUSPEND</b>	<p>If <i>cmd</i> is set to <b>DDI_PM_SUSPEND</b> <b>detach()</b> is used to suspend all activity of a device before power is (possibly) removed from the device. In this case, <b>detach()</b> may be called with outstanding <b>open(9E)</b> requests. It must save the hardware state of the device to memory and block incoming or existing requests until <b>attach(9E)</b> is called with a command value of <b>DDI_PM_RESUME</b>. When the driver receives a request, it should call <b>ddi_dev_is_needed(9F)</b> to request to the framework that the device be resumed.</p>

<b>DDI_SUSPEND</b>	<p>The <b>DDI_PM_SUSPEND</b> <i>cmd</i> is issued when the device is being suspended prior to setting component 0 of the device to power level 0 (see <b>pm(7)</b> and <b>power(9E)</b>). A return of <b>DDI_FAILURE</b> will result in component 0 of the device not being set to power level 0.</p> <p>If <i>cmd</i> is set to <b>DDI_SUSPEND</b>, <b>detach()</b> is used to suspend all activity of a device before power is (possibly) removed from the device. In this case, <b>detach()</b> may be called with outstanding <b>open(9E)</b> requests. It must save the hardware state of the device to memory and block incoming or existing requests until <b>attach()</b> is called with <b>DDI_RESUME</b>.</p> <p>The <b>DDI_SUSPEND</b> <i>cmd</i> is issued when the entire system is being suspended and power removed from it or when the system must be made quiescent. It will be issued only to devices which have a <b>reg</b> property or which export a <b>pm-hardware-state</b> property with the value <i>needs-suspend-resume</i>.</p> <p>If the device is used to store file systems, then after <b>DDI_SUSPEND</b> is issued, the device should still honor <b>dump(9E)</b> requests (calling <b>ddi_dev_is_needed(9F)</b> if the device has also been suspended with <b>DDI_PM_SUSPEND</b>), as this entry point may be used by <b>cpr(7)</b> to save the system state. It must do this, however, without disturbing the saved hardware state of the device.</p> <p>Before returning successfully from a call to <b>detach()</b> with a command of <b>DDI_SUSPEND</b>, the driver must cancel any outstanding timeouts and make any driver threads quiescent. If <b>DDI_FAILURE</b> is returned for the <b>DDI_SUSPEND</b> <i>cmd</i>, either the operation to suspend the system or to make it quiescent will be aborted.</p>
<b>RETURN VALUES</b>	<p><b>DDI_SUCCESS</b> For <b>DDI_DETACH</b>, the state associated with the given device was successfully removed. For <b>DDI_SUSPEND</b> and <b>DDI_PM_SUSPEND</b>, the driver was successfully suspended.</p> <p><b>DDI_FAILURE</b> The operation failed or the request was not understood. The associated state is unchanged.</p>
<b>CONTEXT</b>	This function is called from user context only.
<b>SEE ALSO</b>	<p><b>cpr(7)</b>, <b>pm(7)</b>, <b>attach(9E)</b>, <b>dump(9E)</b>, <b>open(9E)</b>, <b>pm(9E)</b>, <b>power(9E)</b>, <b>ddi_add_intr(9F)</b>, <b>ddi_dev_is_needed(9F)</b>, <b>ddi_map_regs(9F)</b>, <b>kmem_free(9F)</b>, <b>timeout(9F)</b></p> <p><i>Writing Device Drivers</i></p>



<b>NAME</b>	devmap – validate and translate virtual mapping for memory mapped device
<b>SYNOPSIS</b>	<pre>#include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt;  int prefixdevmap(dev_t dev, devmap_cookie_t dhp, offset_t off, size_t len,                 size_t *maplen, uint_t model);</pre>
<b>INTERFACE LEVEL</b>	Solaris DDI specific (Solaris DDI).
<b>ARGUMENTS</b>	<p><i>dev</i> Device whose memory is to be mapped.</p> <p><i>dhp</i> An opaque mapping handle that the system uses to describe the mapping.</p> <p><i>off</i> User offset within the logical device memory at which the mapping begins.</p> <p><i>len</i> Length (in bytes) of the mapping to be mapped.</p> <p><i>maplen</i> Pointer to length (in bytes) of mapping that has been validated. <i>maplen</i> is less than or equal to <i>len</i>.</p> <p><i>model</i> The data model type of the current thread.</p>
<b>DESCRIPTION</b>	<p><b>devmap()</b> is a required entry point for character drivers supporting memory-mapped devices if the drivers use the devmap framework to set up the mapping. A memory mapped device has memory that can be mapped into a process's address space. The <b>mmap(2)</b> system call, when applied to a character special file, allows this device memory to be mapped into user space for direct access by the user applications.</p> <p>As a result of a <b>mmap(2)</b> system call, the system calls the <b>devmap()</b> entry point during the mapping setup when <b>D_DEVMAP</b> is set in the <b>cb_flag</b> field of the <b>cb_ops(9S)</b> structure, and any of the following conditions apply:</p> <ul style="list-style-type: none"> <li>• <b>ddi_devmap_segmap(9F)</b> is used as the <b>segmap(9E)</b> entry point.</li> <li>• <b>segmap(9E)</b> entry point is set to NULL.</li> <li>• <b>mmap(9E)</b> entry point is set to NULL.</li> <li>• <b>ddi_devmap_segmap(9F)</b> is used in the <b>segmap(9E)</b> entry point.</li> </ul> <p>Otherwise <b>EINVAL</b> will be returned to <b>mmap(2)</b>.</p> <p>Device drivers should use <b>devmap()</b> to validate the user mappings to the device, to translate the logical offset, <i>off</i>, to the corresponding physical offset within the device address space, and to pass the mapping information to the system for setting up the mapping.</p> <p><i>dhp</i> is a device mapping handle that the system uses to describe a mapping to a memory that is either contiguous in physical address space or in kernel virtual address space. The system may create multiple mapping handles in one <b>mmap(2)</b> system call (for example, if the mapping contains multiple physically discontinuous memory regions).</p>

*model* returns the C Language Type Model which the current thread expects. It is set to **DDI\_MODEL\_ILP32** if the current thread expects 32-bit (*ILP32*) semantics, or **DDI\_MODEL\_LP64** if the current thread expects 64-bit (*LP64*) semantics. *model* is used in combination with **ddi\_model\_convert\_from(9F)** to determine whether there is a data model mismatch between the current thread and the device driver. The device driver might have to adjust the shape of data structures before exporting them to a user thread which supports a different data model.

**devmap()** should return **EINVAL** if the logical offset, *off*, is out of the range of memory exported by the device to user space. If *off + len* exceeds the range of the contiguous memory, **devmap()** should return the length from *off* to the end of the contiguous memory region. The system will repeatedly call **devmap()** until the original mapping length is satisfied. The driver sets *\*maplen* to the validated length which must be either less than or equal to *len*.

The **devmap()** entry point must initialize the mapping parameters before passing them to the system through either **devmap\_devmem\_setup(9F)** (if the memory being mapped is device memory) or **devmap\_umem\_setup(9F)** (if the memory being mapped is kernel memory). The **devmap()** entry point initializes the mapping parameters by mapping the control callback structure (see **devmap\_callback\_ctl(9S)**), the device access attributes, mapping length, maximum protection possible for the mapping, and optional mapping flags. See **devmap\_devmem\_setup(9F)** and **devmap\_umem\_setup(9F)** for further information on initializing the mapping parameters.

The system will copy the driver's **devmap\_callback\_ctl(9S)** data into its private memory so the drivers do not need to keep the data structure after the return from either **devmap\_devmem\_setup(9F)** or **devmap\_umem\_setup(9F)**.

For device mappings, the system establishes the mapping to the physical address that corresponds to *off* by passing the register number and the offset within the register address space to **devmap\_devmem\_setup(9F)**.

For kernel memory mapping, the system selects a user virtual address that is aligned with the kernel address being mapped for cache coherence.

## RETURN VALUES

**0** Successful completion.  
Non-zero An error occurred.

## EXAMPLES

The following is an example of the implementation for the **devmap()** entry point. For mapping device memory, **devmap()** calls **devmap\_devmem\_setup(9F)** with the register number, *rnumber*, and the offset within the register, *roff*. For mapping kernel memory, the driver must first allocate the kernel memory using **ddi\_umem\_alloc(9F)**. For example, **ddi\_umem\_alloc(9F)** can be called in the **attach(9E)** routine. The resulting kernel memory cookie is stored in the driver soft state structure (see **ddi\_soft\_state(9F)**), which is accessible from the **devmap()** entry point. **devmap()** passes the cookie obtained from **ddi\_umem\_alloc(9F)** and the offset within the allocated kernel memory to **devmap\_umem\_setup(9F)**. The corresponding **ddi\_umem\_free(9F)** can be made in the **detach(9E)** routine to free up the kernel memory.

```

...

#define MAPPING_SIZE 0x2000    /* size of the mapping */
#define MAPPING_START 0x7000000 /* logical offset at beginning
                                of the mapping */

static
struct devmap_callback_ctl xxmap_ops = {
    DEVMAP_OPS_REV, /* devmap_ops version number */
    xxmap_map,      /* devmap_ops map routine */
    xxmap_access,   /* devmap_ops access routine */
    xxmap_dup,      /* devmap_ops dup routine */
    xxmap_unmap,    /* devmap_ops unmap routine */
};

static int
xxdevmap(dev_t dev, devmap_cookie_t dhp, offset_t off, size_t len,
         size_t *maplen, uint_t model)
{
    int instance;
    struct xxstate *xsp;
    struct ddi_device_acc_attr *endian_attr;
    struct devmap_callback_ctl *callbackops = NULL;
    ddi_umem_cookie_t cookie;
    dev_info_t *dip;
    offset_t roff;
    offset_t koff;
    u_int rnumber;
    u_int maxprot;
    u_int flags = 0;
    size_t length;
    int err;

    /* get device soft state */
    instance = getminor(dev);
    xsp = ddi_get_soft_state(statep, instance);
    if (xsp == NULL)
        return (-1);

    dip = xsp->dip;
    /* check for a valid offset */
    if (off is invalid)
        return (-1);

    /* check if len is within the range of contiguous memory */

```

```

if ( (off + len) is contiguous.)
    length = len;
else
    length = MAPPING_START + MAPPING_SIZE - off;

/* device access attributes */
endian_attr = xsp->endian_attr;

if ( off is referring to a device memory. ) {
    /* assign register related parameters */
    rnumber = XXX; /* index to register set at off */
    roff = XXX; /* offset of rnumber at local bus */
    callbackops = &xxmap_ops; /* do all callbacks for this mapping */
    maxprot = PROT_ALL; /* allowing all access */
    if ((err = devmap_devmem_setup(dhp, dip, callbackops, rnumber, roff,
        length, maxprot, flags, endian_attr)) < 0)
        return (err);
} else if ( off is referring to a kernel memory. ) {
    cookie = xsp->cookie; /* cookie is obtained from
        ddi_umem_alloc(9F) */
    koff = XXX; /* offset within the kernel memory. */
    callbackops = NULL; /* don't do callback for this mapping */
    maxprot = PROT_ALL; /* allowing all access */
    if ((err = devmap_umem_setup(dhp, dip, callbackops, cookie, koff,
        length, maxprot, flags, endian_attr)) < 0)
        return (err);
}

*maplen = length;
return (0);
}

```

**SEE ALSO**

**mmap(2), attach(9E), detach(9E), mmap(9E), segmap(9E), ddi\_devmap\_segmap(9F), ddi\_model\_convert\_from(9F), ddi\_soft\_state(9F), ddi\_umem\_alloc(9F), ddi\_umem\_free(9F), devmap\_devmem\_setup(9F), devmap\_setup(9F), devmap\_umem\_setup(9F), cb\_ops(9S), devmap\_callback\_ctl(9S)**

*Writing Device Drivers*

<b>NAME</b>	devmap_access – device mapping access entry point																								
<b>SYNOPSIS</b>	<pre>#include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt;  int prefix devmap_access(devmap_cookie_t dhp, void *pvtp, offset_t off, size_t len,     u_int type, u_int rw)</pre>																								
<b>INTERFACE LEVEL</b>	Solaris DDI specific (Solaris DDI).																								
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<b>DEVMAP_EXEC</b>	Execution access attempted.																								
<b>DESCRIPTION</b>	<p>The <b>devmap_access()</b> entry point is an optional routine. It notifies drivers whenever an access is made to a mapping described by <i>dhp</i> that has not been validated or does not have sufficient protection for the access. The system expects <b>devmap_access()</b> to call either <b>devmap_do_ctxmgt(9F)</b> or <b>devmap_default_access(9F)</b> to load the memory address translations before it returns. For mappings that support context switching, device drivers should call <b>devmap_do_ctxmgt(9F)</b>. For mappings that do not support context switching, the drivers should call <b>devmap_default_access(9F)</b>.</p> <p>In <b>devmap_access</b>, drivers perform memory access related operations such as, context switching, checking the availability of the memory object, and locking and unlocking the memory object being accessed. The <b>devmap_access()</b> entry point is set to NULL if no operations need to be performed.</p> <p><i>pvtp</i> is a pointer to the driver's private mapping data that was allocated and initialized in the <b>devmap_map(9E)</b> entry point.</p> <p><i>off</i> and <i>len</i> define the range to be affected by the operations in <b>devmap_access()</b>. <i>type</i> defines the type of operation that device drivers should perform on the memory object. If <i>type</i> is either <b>DEVMAP_LOCK</b> or <b>DEVMAP_UNLOCK</b>, the length passed to either <b>devmap_do_ctxmgt(9F)</b> or <b>devmap_default_access(9F)</b> must be same as <i>len</i>. <i>rw</i> specifies</p>																								

the direction of access on the memory object.

A non-zero return value from **devmap\_access()** may result in a SIGSEGV or SIGBUS signal being delivered to the process.

#### RETURN VALUES

**devmap\_access** returns the following values:

- 0** Successful completion.
- Non-zero An error occurred. The return value from **devmap\_do\_ctxmgt(9F)** or **devmap\_default\_access(9F)** should be returned.

#### EXAMPLES

The following is an example of the **devmap\_access()** entry point. If the mapping supports context switching, **devmap\_access()** calls **devmap\_do\_ctxmgt(9F)**. Otherwise, **devmap\_access()** calls **devmap\_default\_access(9F)**.

```

...

#define OFF_DO_CTXMGT 0x40000000
#define OFF_NORMAL 0x40100000
#define CTXMGT_SIZE 0x100000
#define NORMAL_SIZE 0x100000

/*
 * Driver devmap_contextmgt(9E) callback function.
 */
static int
xx_context_mgt(devmap_cookie_t dhp, void *pvtp, offset_t offset,
    size_t length, u_int type, u_int rw)
{
    .....
    /*
     * see devmap_contextmgt(9E) for an example
     */
}

/*
 * Driver devmap_access(9E) entry point
 */
static int
xxdevmap_access(devmap_cookie_t dhp, void *pvtp, offset_t off,
    size_t len, u_int type, u_int rw)
{
    offset_t diff;
    int err;

    /*
     * check if off is within the range that supports

```

```

* context management.
*/
if ((diff = off - OFF_DO_CTXMG) >= 0 && diff < CTXMGT_SIZE) {
    /*
    * calculates the length for context switching
    */
    if ((len + off) > (OFF_DO_CTXMGT + CTXMGT_SIZE))
        return (-1);

    /*
    * perform context switching
    */
    err = devmap_do_ctxmgt(dhp, pvtp, off, len, type,
        rw, xx_context_mgt);
    /*
    * check if off is within the range that does normal
    * memory mapping.
    */
} else if ((diff = off - OFF_NORMAL) >= 0 && diff < NORMAL_SIZE) {
    if ((len + off) > (OFF_NORMAL + NORMAL_SIZE))
        return (-1);
    err = devmap_default_access(dhp, pvtp, off, len, type, rw);
} else
    return (-1);

return (err);
}

```

**SEE ALSO**

**devmap\_map(9E)**, **devmap\_default\_access(9F)**, **devmap\_do\_ctxmgt(9F)**,  
**devmap\_callback\_ctl(9S)**

*Writing Device Drivers*

<b>NAME</b>	devmap_contextmgt – driver callback function for context management
<b>SYNOPSIS</b>	<pre>#include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt;  int devmap_contextmgt(devmap_cookie_t dhp, void *pvtp, offset_t off,     size_t len, u_int type, u_int rw);</pre>
<b>INTERFACE LEVEL</b>	Solaris DDI specific (Solaris DDI).
<b>ARGUMENTS</b>	<p><i>dhp</i> An opaque mapping handle that the system uses to describe the mapping.</p> <p><i>pvtp</i> Driver private mapping data.</p> <p><i>off</i> User offset within the logical device memory at which the access begins.</p> <p><i>len</i> Length (in bytes) of the memory being accessed.</p> <p><i>type</i> Type of access operation. Possible values are:</p> <ul style="list-style-type: none"> <li><b>DEVMAP_ACCESS</b> Memory access.</li> <li><b>DEVMAP_LOCK</b> Lock the memory being accessed.</li> <li><b>DEVMAP_UNLOCK</b> Unlock the memory being accessed.</li> </ul> <p><i>rw</i> Direction of access. Possible values are:</p> <ul style="list-style-type: none"> <li><b>DEVMAP_READ</b> Read access attempted.</li> <li><b>DEVMAP_WRITE</b> Write access attempted.</li> </ul>
<b>DESCRIPTION</b>	<p><b>devmap_contextmgt()</b> is a driver-supplied function that performs device context switching on a mapping. Device drivers pass <b>devmap_contextmgt()</b> as an argument to <b>devmap_do_ctxmgt(9F)</b> in the <b>devmap_access(9E)</b> entry point. The system will call <b>devmap_contextmgt()</b> when memory is accessed. The system expects <b>devmap_contextmgt()</b> to load the memory address translations of the mapping by calling <b>devmap_load(9F)</b> before returning.</p> <p><i>dhp</i> uniquely identifies the mapping and is used as an argument to <b>devmap_load(9F)</b> to validate the mapping. <i>off</i> and <i>len</i> define the range to be affected by the operations in <b>devmap_contextmgt()</b>.</p> <p>The driver must check if there is already a mapping established at <i>off</i> that needs to be unloaded. If a mapping exists at <i>off</i>, <b>devmap_contextmgt()</b> must call <b>devmap_unload(9F)</b> on the current mapping. <b>devmap_unload(9F)</b> must be followed by <b>devmap_load()</b> on the mapping that generated this call to <b>devmap_contextmgt()</b>. <b>devmap_unload(9F)</b> unloads the current mapping so that a call to <b>devmap_access(9E)</b>, which causes the system to call <b>devmap_contextmgt()</b>, will be generated the next time the mapping is accessed.</p> <p><i>pvtp</i> is a pointer to the driver's private mapping data that was allocated and initialized in the <b>devmap_map(9E)</b> entry point. <i>type</i> defines the type of operation that device drivers should perform on the memory object. If <i>type</i> is either <b>DEVMAP_LOCK</b> or</p>



**DEVMAP\_UNLOCK**, the length passed to either **devmap\_unload(9F)** or **devmap\_load(9F)** must be same as *len*. *rw* specifies the access direction on the memory object.

A non-zero return value from **devmap\_contextmgt()** will be returned to **devmap\_access(9E)** and will cause the corresponding operation to fail. The failure may result in a **SIGSEGV** or **SIGBUS** signal being delivered to the process.

**RETURN VALUES**

**0** Successful completion.

Non-zero An error occurred.

**EXAMPLES**

The following shows an example of managing a device context.

```

struct xxcontext cur_ctx;

static int
xxdevmap_contextmgt(devmap_cookie_t dhp, void *pvtp, offset_t off,
size_t len, u_int type, u_int rw)
{
    devmap_cookie_t cur_dhp;
    struct xxpvtdata *p;
    struct xxpvtdata *pvp = (struct xxpvtdata *)pvtp;
    struct xx_softc *softc = pvp->softc;
    int err;

    mutex_enter(&softc->mutex);

    /*
     * invalidate the translations of current context before
     * switching context.
    */
    if (cur_ctx != NULL && cur_ctx != pvp->ctx) {
        p = cur_ctx->pvt;
        cur_dhp = p->dhp;
        if ((err = devmap_unload(cur_dhp, off, len)) != 0)
            return (err);
    }

    /* Switch device context - device dependent*/
    ...

    /* Make handle the new current mapping */
    cur_ctx = pvp->ctx;

    /*
     * Load the address translations of the calling context.
    */

```

```
err = devmap_load(pvp->dhp, off, len, type, rw);  
mutex_exit(&softc->mutex);  
return (err);  
}
```

**SEE ALSO****devmap\_access(9E), devmap\_do\_ctxmgt(9F) devmap\_load(9F), devmap\_unload(9F)***Writing Device Drivers*

<b>NAME</b>	devmap_dup – device mapping duplication entry point								
<b>SYNOPSIS</b>	<pre>#include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt; int prefix devmap_dup(devmap_cookie_t dhp, void *pvtp, devmap_cookie_t new_dhp, void **new_pvtp);</pre>								
<b>INTERFACE LEVEL</b>	Solaris DDI specific (Solaris DDI).								
<b>ARGUMENTS</b>	<table border="0"> <tr> <td style="padding-right: 20px;"><i>dhp</i></td> <td>An opaque mapping handle that the system uses to describe the mapping currently being duplicated.</td> </tr> <tr> <td><i>pvtp</i></td> <td>Driver private mapping data for the mapping currently being duplicated.</td> </tr> <tr> <td><i>new_dhp</i></td> <td>An opaque data structure that the system uses to describe the duplicated device mapping.</td> </tr> <tr> <td><i>new_pvtp</i></td> <td>A pointer to be filled in by device drivers with the driver private mapping data for the duplicated device mapping.</td> </tr> </table>	<i>dhp</i>	An opaque mapping handle that the system uses to describe the mapping currently being duplicated.	<i>pvtp</i>	Driver private mapping data for the mapping currently being duplicated.	<i>new_dhp</i>	An opaque data structure that the system uses to describe the duplicated device mapping.	<i>new_pvtp</i>	A pointer to be filled in by device drivers with the driver private mapping data for the duplicated device mapping.
<i>dhp</i>	An opaque mapping handle that the system uses to describe the mapping currently being duplicated.								
<i>pvtp</i>	Driver private mapping data for the mapping currently being duplicated.								
<i>new_dhp</i>	An opaque data structure that the system uses to describe the duplicated device mapping.								
<i>new_pvtp</i>	A pointer to be filled in by device drivers with the driver private mapping data for the duplicated device mapping.								
<b>DESCRIPTION</b>	<p>The system calls <b>devmap_dup()</b> when a device mapping is duplicated such as during the <b>fork(2)</b> system call. The system expects <b>devmap_dup()</b> to generate new driver private data for the new mapping, and to set <i>new_pvtp</i> to point to it. <i>new_dhp</i> is the handle of the new mapped object.</p> <p>A non-zero return value from <b>devmap_dup()</b> will cause a corresponding operation, such as <b>fork()</b> to fail.</p>								
<b>RETURN VALUES</b>	<p><b>devmap_dup(9E)</b> returns the following values:</p> <table border="0"> <tr> <td style="padding-right: 20px;"><b>0</b></td> <td>Successful completion.</td> </tr> <tr> <td>Non-zero</td> <td>An error occurred.</td> </tr> </table>	<b>0</b>	Successful completion.	Non-zero	An error occurred.				
<b>0</b>	Successful completion.								
Non-zero	An error occurred.								
<b>EXAMPLES</b>	<pre>static int xxdevmap_dup(devmap_cookie_t dhp, void *pvtp, devmap_cookie_t new_dhp, void **new_pvtp) {     struct xpvtdata *prvtdata;     struct xpvtdata *p = (struct xpvtdata *)pvtp;     struct xx_softc *softc = p-&gt;softc;      mutex_enter(&amp;softc-&gt;mutex);     /* Allocate a new private data structure */     prvtdata = kmem_alloc(sizeof (struct xpvtdata), KM_SLEEP);     /* Return the new data */     prvtdata-&gt;off = p-&gt;off;     prvtdata-&gt;len = p-&gt;len;</pre>								

```
    prvtdata->ctx = p->ctx;
    prvtdata->dhp = new_dhp;
    prvtdata->softc = p->softc;
    *new_pvtp = prvtdata;
    mutex_exit(&softc->mutex);
    return (0);
}
```

**SEE ALSO** [fork\(2\)](#), [devmap\\_callback\\_ctl\(9S\)](#)

*Writing Device Drivers*

<b>NAME</b>	devmap_map – device mapping create entry point
<b>SYNOPSIS</b>	<pre>#include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt;  int prefix devmap_map(devmap_cookie_t dhp, dev_t dev, u_int flags, offset_t off,     size_t len, void **pvtp)</pre>
<b>INTERFACE LEVEL</b>	Solaris DDI specific (Solaris DDI).
<b>ARGUMENTS</b>	<p><i>dhp</i>            An opaque mapping handle that the system uses to describe the mapping currently being created.</p> <p><i>dev</i>            The device whose memory is to be mapped.</p> <p><i>flags</i>          Flags indicating type of mapping. Possible values are:                          <b>MAP_PRIVATE</b> Changes are private.                          <b>MAP_SHARED</b> Changes should be shared.</p> <p><i>off</i>            User offset within the logical device memory at which the mapping begins.</p> <p><i>len</i>            Length (in bytes) of the memory to be mapped.</p> <p><i>pvtp</i>          A pointer to be filled in by device drivers with the driver private mapping data.</p>
<b>DESCRIPTION</b>	<p>The <b>devmap_map()</b> entry point is an optional routine that allows drivers to perform additional processing or to allocate private resources during the mapping setup time. For example, in order for device drivers to support context switching, the drivers allocate private mapping data and associate the private data with the mapping parameters in the <b>devmap_map()</b> entry point.</p> <p>The system calls <b>devmap_map()</b> after the user mapping to device physical memory has been established. (For example, after the <b>devmap(9E)</b> entry point is called.)</p> <p><b>devmap_map()</b> receives a pointer to the driver private data for this mapping in <i>pvtp</i>. The system expects the driver to allocate its private data and set <i>*pvtp</i> to the allocated data. The driver must store <i>off</i> and <i>len</i>, which define the range of the mapping, in its private data. Later, when the system calls <b>devmap_unmap(9E)</b>, the driver will use the <i>off</i> and <i>len</i> stored in <i>pvtp</i> to check if the entire mapping, or just a part of it, is being unmapped. If only a part of the mapping is being unmapped, the driver must allocate a new private data for the remaining mapping before freeing the old private data. The driver will receive <i>*pvtp</i> in subsequent event notification callbacks.</p> <p>If the driver support context switching, it should store the mapping handle, <i>dhp</i>, in its private data <i>*pvtp</i>, for later use in <b>devmap_unload(9F)</b>.</p> <p>For a driver that supports context switching, <i>flags</i> indicates whether or not the driver should allocate a private context for the mapping. For example, a driver may allocate a memory region to store the device context if <i>flags</i> is set to <b>MAP_PRIVATE</b>.</p>

**RETURN VALUES**

**devmap\_map(9E)** returns the following values:

**0**                    Successful completion.  
 Non-zero            An error occurred.

**EXAMPLES**

The following shows an example implementation for **devmap\_map()**.

```
static int
xxdevmap_map(devmap_cookie_t dhp, dev_t dev, u_int flags, offset_t off,
             size_t len, void **pvtp)
{
    struct xx_resources *pvt;
    struct xx_context *this_context;
    struct xx_softc *softc;

    softc = ddi_get_softc(statep, getminor(dev));

    this_context = get_context(softc, off, len);

    /* allocate resources for the mapping - Device dependent */
    pvt = kmem_zalloc(sizeof (struct xx_resources), KM_SLEEP);

    pvt->off = off;
    pvt->len = len;
    pvt->dhp = dhp;
    pvt->ctx = this_context;
    *pvtp = pvt;
}
```

**SEE ALSO**

**devmap\_unmap(9E)**, **devmap\_unload(9F)**, **devmap\_callback\_ctl(9S)**  
*Writing Device Drivers*

<b>NAME</b>	devmap_unmap – device mapping unmap entry point																
<b>SYNOPSIS</b>	<pre>#include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt; void prefix devmap_unmap(devmap_cookie_t dhp, void *pvtp, offset_t off,     size_t len, devmap_cookie_t new_dhp1, void **new_pvtp1,     devmap_cookie_t new_dhp2, void **new_pvtp2)</pre>																
<b>INTERFACE LEVEL</b>	Solaris DDI specific (Solaris DDI).																
<b>ARGUMENTS</b>	<table border="0"> <tr> <td style="padding-right: 10px;"><i>dhp</i></td> <td>An opaque mapping handle that the system uses to describe the mapping.</td> </tr> <tr> <td><i>pvtp</i></td> <td>Driver private mapping data.</td> </tr> <tr> <td><i>off</i></td> <td>User offset within the logical device memory at which the unmapping begins.</td> </tr> <tr> <td><i>len</i></td> <td>Length (in bytes) of the memory being unmapped.</td> </tr> <tr> <td><i>new_dhp1</i></td> <td>The opaque mapping handle that the system uses to describe the new region that ends at (<i>off - 1</i>). <i>new_dhp1</i> may be NULL.</td> </tr> <tr> <td><i>new_pvtp1</i></td> <td>A pointer to be filled in by the driver with the driver private mapping data for the new region that ends at (<i>off - 1</i>); ignored if <i>new_dhp1</i> is NULL.</td> </tr> <tr> <td><i>new_dhp2</i></td> <td>The opaque mapping handle that the system uses to describe the new region that begins at (<i>off + len</i>); <i>new_dhp2</i> may be NULL.</td> </tr> <tr> <td><i>new_pvtp2</i></td> <td>A pointer to be filled in by the driver with the driver private mapping data for the new region that begins at (<i>off + len</i>); ignored if <i>new_dhp2</i> is NULL.</td> </tr> </table>	<i>dhp</i>	An opaque mapping handle that the system uses to describe the mapping.	<i>pvtp</i>	Driver private mapping data.	<i>off</i>	User offset within the logical device memory at which the unmapping begins.	<i>len</i>	Length (in bytes) of the memory being unmapped.	<i>new_dhp1</i>	The opaque mapping handle that the system uses to describe the new region that ends at ( <i>off - 1</i> ). <i>new_dhp1</i> may be NULL.	<i>new_pvtp1</i>	A pointer to be filled in by the driver with the driver private mapping data for the new region that ends at ( <i>off - 1</i> ); ignored if <i>new_dhp1</i> is NULL.	<i>new_dhp2</i>	The opaque mapping handle that the system uses to describe the new region that begins at ( <i>off + len</i> ); <i>new_dhp2</i> may be NULL.	<i>new_pvtp2</i>	A pointer to be filled in by the driver with the driver private mapping data for the new region that begins at ( <i>off + len</i> ); ignored if <i>new_dhp2</i> is NULL.
<i>dhp</i>	An opaque mapping handle that the system uses to describe the mapping.																
<i>pvtp</i>	Driver private mapping data.																
<i>off</i>	User offset within the logical device memory at which the unmapping begins.																
<i>len</i>	Length (in bytes) of the memory being unmapped.																
<i>new_dhp1</i>	The opaque mapping handle that the system uses to describe the new region that ends at ( <i>off - 1</i> ). <i>new_dhp1</i> may be NULL.																
<i>new_pvtp1</i>	A pointer to be filled in by the driver with the driver private mapping data for the new region that ends at ( <i>off - 1</i> ); ignored if <i>new_dhp1</i> is NULL.																
<i>new_dhp2</i>	The opaque mapping handle that the system uses to describe the new region that begins at ( <i>off + len</i> ); <i>new_dhp2</i> may be NULL.																
<i>new_pvtp2</i>	A pointer to be filled in by the driver with the driver private mapping data for the new region that begins at ( <i>off + len</i> ); ignored if <i>new_dhp2</i> is NULL.																
<b>DESCRIPTION</b>	<p><b>devmap_unmap()</b> is called when the system removes the mapping in the range [ <i>off</i>, <i>off + len</i> ] such as in the <b>munmap(2)</b> or <b>exit(2)</b> system calls. Device drivers use <b>devmap_unmap()</b> to free up the resources allocated in <b>devmap_map(9E)</b>.</p> <p><i>dhp</i> is the mapping handle that uniquely identifies the mapping. The driver stores the mapping attributes in the driver's private data, <i>pvtp</i>, when the mapping is created. See <b>devmap_map(9E)</b> for details.</p> <p><i>off</i> and <i>len</i> define the range to be affected by <b>devmap_unmap()</b>. This range is within the boundary of the mapping described by <i>dhp</i>.</p> <p>If the range [ <i>off</i>, <i>off + len</i> ] covers the entire mapping, the system passes NULL to <i>new_dhp1</i>, <i>new_pvtp1</i>, <i>new_dhp2</i>, and <i>new_pvtp2</i>. The system expects device drivers to free all resources allocated for this mapping.</p> <p>If <i>off</i> is at the beginning of the mapping and <i>len</i> does not cover the entire mapping, the system sets NULL to <i>new_dhp1</i> and to <i>new_pvtp1</i>. The system expects the drivers to allocate new driver private data for the region that starts at <i>off + len</i> and to set <i>*new_pvtp2</i> to</p>																

point to it. *new\_dhp2* is the mapping handle of the newly mapped object.

If *off* is not at the beginning of the mapping, but *off + len* is at the end of the mapping the system passes NULL to *new\_dhp2* and *new\_pvtp2*. The system then expects the drivers to allocate new driver private data for the region that begins at the beginning of the mapping (for example, stored in *pvtp*) and to set *\*new\_pvtp1* to point to it. *new\_dhp1* is the mapping handle of the newly mapped object.

The drivers should free up the driver private data, *pvtp*, previously allocated in **devmap\_map(9E)** before returning to the system.

#### EXAMPLES

```
static void
xxdevmap_unmap(devmap_cookie_t dhp, void *pvtp, offset_t off,
               size_t len, devmap_cookie_t new_dhp1, void **new_pvtp1,
               devmap_cookie_t new_dhp2, void **new_pvtp2)
{
    struct xpvtdata *ptmp;
    struct xpvtdata *p = (struct xpvtdata *)pvtp;
    struct xx_softc *softc = p->softc;

    mutex_enter(&softc->mutex);
    /*
     * If new_dhp1 is not NULL, create a new driver private data
     * for the region from the beginning of old mapping to off.
     */
    if (new_dhp1 != NULL) {
        ptmp = kmem_zalloc(sizeof (struct xpvtdata), KM_SLEEP);
        ptmp->dhp = new_dhp1;
        ptmp->off = pvtp->off;
        ptmp->len = off - pvtp->off;
        *new_pvtp1 = ptmp;
    }

    /*
     * If new_dhp2 is not NULL, create a new driver private data
     * for the region from off+len to the end of the old mapping.
     */
    if (new_dhp2 != NULL) {
        ptmp = kmem_zalloc(sizeof (struct xpvtdata), KM_SLEEP);
        ptmp->off = off + len;
        ptmp->len = pvtp->len - (off + len - pvtp->off);
        ptmp->dhp = new_dhp2;
        *new_pvtp2 = ptmp;
    }

    /* Destroy the driver private data - Device dependent */
}
```



```
...
    kmem_free(pvtp, sizeof (struct xpvtdata));
    mutex_exit(&softc->mutex);
}
```

**SEE ALSO** [exit\(2\)](#), [munmap\(2\)](#), [devmap\\_map\(9E\)](#), [devmap\\_callback\\_ctl\(9S\)](#)  
*Writing Device Drivers*

<b>NAME</b>	dump – dump memory to device during system failure
<b>SYNOPSIS</b>	<pre>#include &lt;sys/types.h&gt; #include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt;  int prefixdump(dev_t dev, caddr_t addr, daddr_t blkno, int nblk);</pre>
<b>INTERFACE LEVEL</b>	Solaris specific (Solaris DDI). This entry point is <b>required</b> . For drivers that do not implement dump routines, <b>nodev</b> should be used.
<b>ARGUMENTS</b>	<p><i>dev</i> Device number.</p> <p><i>addr</i> Address for the beginning of the area to be dumped.</p> <p><i>blkno</i> Block offset to dump memory to.</p> <p><i>nblk</i> Number of blocks to dump.</p>
<b>DESCRIPTION</b>	<p><b>dump()</b> is used to dump a portion of virtual address space directly to a device in the case of system failure. The memory area to be dumped is specified by <i>addr</i> (base address) and <i>nblk</i> (length). It is dumped to the device specified by <i>dev</i> starting at offset <i>blkno</i>. Upon completion <b>dump()</b> returns the status of the transfer.</p> <p><b>dump()</b> is called at interrupt priority.</p>
<b>RETURN VALUES</b>	<b>dump()</b> should return 0 on success, or the appropriate error number.
<b>SEE ALSO</b>	<i>Writing Device Drivers</i>

<b>NAME</b>	<code>_fini</code> , <code>_info</code> , <code>_init</code> – loadable module configuration entry points
<b>SYNOPSIS</b>	<pre>#include &lt;sys/modctl.h&gt; int _fini(void); int _info(struct modinfo *modinfo); int _init(void);</pre>
<b>INTERFACE LEVEL</b>	Solaris DDI specific (Solaris DDI). These entry points are required. You <b>must</b> write them.
<b>ARGUMENTS</b>	
<code>_info()</code>	<code>modinfo</code> A pointer to an opaque <b>modinfo</b> structure.
<b>DESCRIPTION</b>	<p><code>_init()</code> initializes a loadable module. It is called before any other routine in a loadable module. <code>_init()</code> returns the value returned by <code>mod_install(9F)</code>. The module may optionally perform some other work before the <code>mod_install(9F)</code> call is performed. If the module has done some setup before the <code>mod_install(9F)</code> function is called, then it should be prepared to undo that setup if <code>mod_install(9F)</code> returns an error.</p> <p><code>_info()</code> returns information about a loadable module. <code>_info()</code> returns the value returned by <code>mod_info(9F)</code>.</p> <p><code>_fini()</code> prepares a loadable module for unloading. It is called when the system wants to unload a module. If the module determines that it can be unloaded, then <code>_fini()</code> returns the value returned by <code>mod_remove(9F)</code>. Upon successful return from <code>_fini()</code> no other routine in the module will be called before <code>_init()</code> is called.</p>
<b>RETURN VALUES</b>	<p><code>_init()</code> should return the appropriate error number if there is an error, else it should return the return value from <code>mod_install(9F)</code>.</p> <p><code>_info()</code> should return the return value from <code>mod_info(9F)</code></p> <p><code>_fini()</code> should return the return value from <code>mod_remove(9F)</code>.</p>
<b>EXAMPLES</b>	<p>The following example demonstrates how to initialize and free a <b>mutex(9F)</b>.</p> <pre>#include &lt;sys/modctl.h&gt; #include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt;  static struct dev_ops drv_ops; /*  * Module linkage information for the kernel.  */ static struct modldrv modldrv = {     &amp;mod_driverops, /* Type of module. This one is a driver */     "Sample Driver",     &amp;drv_ops /* driver ops */</pre>

```
};

static struct modlinkage modlinkage = {
    MODREV_1,
    &modldrv,
    NULL
};

/*
 * Global driver mutex
 */
static kmutex_t xx_global_mutex;

int
_init(void)
{
    int i;

    /*
     * Initialize global mutex before mod_install'ing driver.
     * If mod_install() fails, must clean up mutex initialization
     */
    mutex_init(&xx_global_mutex, "XXX Global Mutex",
        MUTEX_DRIVER, (void *)NULL);

    if ((i = mod_install(&modlinkage)) != 0) {
        mutex_destroy(&xx_global_mutex);
    }

    return (i);
}

int
_info(struct modinfo *modinfop)
{
    return (mod_info(&modlinkage, modinfop));
}

int
_fini(void)
{
    int i;
```

```
/*
 * If mod_remove() is successful, we destroy our global mutex
 */
if ((i = mod_remove(&modlinkage)) == 0) {
    mutex_destroy(&xx_global_mutex);
}
return (i);
}
```

**SEE ALSO** [add\\_drv\(1M\)](#), [mod\\_info\(9F\)](#), [mod\\_install\(9F\)](#), [mod\\_remove\(9F\)](#), [mutex\(9F\)](#), [modldrv\(9S\)](#), [modlinkage\(9S\)](#), [modlstrmod\(9S\)](#)

*Writing Device Drivers*

**WARNINGS** Do not change the structures referred to by the **modlinkage** structure after the call to **mod\_install()**, as the system may copy or change them.

**NOTES** Even though the identifiers **\_fini()**, **\_info()**, and **\_init()** appear to be declared as globals, their scope is restricted by the kernel to the module that they are defined in.

**BUGS** On some implementations **\_info()** may be called before **\_init()**.

<b>NAME</b>	getinfo – get device driver information
<b>SYNOPSIS</b>	<pre>#include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt; int prefixgetinfo(dev_info_t *dip, ddi_info_cmd_t cmd, void *arg, void **resultp);</pre>
<b>INTERFACE LEVEL</b>	Solaris DDI specific (Solaris DDI). This entry point is <b>required</b> for drivers which export <b>cb_ops(9S)</b> entry points.
<b>ARGUMENTS</b>	<p><i>dip</i> Do not use.</p> <p><i>cmd</i> Command argument – valid command values are <b>DDI_INFO_DEVT2DEVINFO</b> and <b>DDI_INFO_DEVT2INSTANCE</b>.</p> <p><i>arg</i> Command specific argument.</p> <p><i>resultp</i> Pointer to where the requested information is stored.</p>
<b>DESCRIPTION</b>	<p>When <i>cmd</i> is set to <b>DDI_INFO_DEVT2DEVINFO</b>, <b>getinfo()</b> should return the <b>dev_info_t</b> pointer associated with the <b>dev_t arg</b>. The <b>dev_info_t</b> pointer should be returned in the field pointed to by <i>resultp</i>.</p> <p>When <i>cmd</i> is set to <b>DDI_INFO_DEVT2INSTANCE</b>, <b>getinfo()</b> should return the instance number associated with the <b>dev_t arg</b>. The instance number should be returned in the field pointed to by <i>resultp</i>.</p> <p>Drivers which do not export <b>cb_ops(9S)</b> entry points are not required to provide a <b>getinfo()</b> entry point, and may use <b>nodev(9F)</b> in the <b>devo_getinfo</b> field of the <b>dev_ops(9S)</b> structure. A SCSI HBA driver is an example of a driver which is not required to provide <b>cb_ops(9S)</b> entry points.</p>
<b>RETURN VALUES</b>	<p><b>getinfo()</b> should return:</p> <p><b>DDI_SUCCESS</b> on success.</p> <p><b>DDI_FAILURE</b> on failure.</p>
<b>EXAMPLES</b>	<pre>/*ARGSUSED*/ static int rd_getinfo(dev_info_t *dip, ddi_info_cmd_t infocmd, void *arg, void **result) {     /* Note that in this simple example      * the minor number is the instance      * number.      */      devstate_t *sp;     int error = DDI_FAILURE;</pre>

```
switch (infocmd) {
case DDI_INFO_DEVT2DEVINFO:
    if ((sp = ddi_get_soft_state(statep,
        getminor((dev_t) arg))) != NULL) {
        *resultp = sp->devi;
        error = DDI_SUCCESS;
    } else
        *result = NULL;
    break;

case DDI_INFO_DEVT2INSTANCE:
    *resultp = (void *) getminor((dev_t) arg);
    error = DDI_SUCCESS;
    break;
}

return (error);
}
```

**SEE ALSO** [nodev\(9F\)](#), [cb\\_ops\(9S\)](#), [dev\\_ops\(9S\)](#)

*Writing Device Drivers*

<b>NAME</b>	identify – determine if a driver is associated with a device
<b>SYNOPSIS</b>	<pre>#include &lt;sys/conf.h&gt; #include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt; int prefixidentify(dev_info_t *dip);</pre>
<b>INTERFACE LEVEL</b>	Solaris DDI specific (Solaris DDI). This entry point is obsolete and is no longer required. This entry point may not be supported in future releases. <b>nulldev(9F)</b> should be specified in the <b>dev_ops(9S)</b> structure.
<b>ARGUMENTS</b>	<i>dip</i> A pointer to a <b>dev_info</b> structure.
<b>DESCRIPTION</b>	<b>identify()</b> was used to determine whether a driver drives the device pointed to by <i>dip</i> . <b>identify()</b> is currently supported to provide backward compatibility with older drivers and should not be implemented. See the <b>INTERFACE LEVEL</b> section.
<b>RETURN VALUES</b>	The return value from <b>identify()</b> is ignored.
<b>SEE ALSO</b>	<b>nulldev(9F)</b> , <b>dev_ops(9S)</b>
<b>WARNINGS</b>	<p>This routine may be called multiple times. It may also be called at any time. The driver should not infer anything from the the sequence or the number of times this entry point has been called.</p> <p>This entry point may not be supported in future releases.</p>



<b>NAME</b>	ioctl – control a character device
<b>SYNOPSIS</b>	<pre>#include &lt;sys/cred.h&gt; #include &lt;sys/file.h&gt; #include &lt;sys/types.h&gt; #include &lt;sys/errno.h&gt; #include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt;  int prefixioctl(dev_t dev, int cmd, intptr_t arg, int mode, cred_t *cred_p, int *rval_p);</pre>
<b>INTERFACE LEVEL ARGUMENTS</b>	<p>Architecture independent level 1 (DDI/DKI). This entry point is <b>optional</b>.</p> <p><i>dev</i> Device number.</p> <p><i>cmd</i> Command argument the driver <b>ioctl</b> routine interprets as the operation to be performed.</p> <p><i>arg</i> Passes parameters between a user program and the driver. When used with terminals, the argument is the address of a user program structure containing driver or hardware settings. Alternatively, the argument may be a value that has meaning only to the driver. The interpretation of the argument is driver dependent and usually depends on the command type; the kernel does not interpret the argument.</p> <p><i>mode</i> A bit field that contains:</p> <ul style="list-style-type: none"> <li>• Information set when the device was opened. The driver may use it to determine if the device was opened for reading or writing. The driver can make this determination by checking the <b>FREAD</b> or <b>FWRITE</b> flags. See the <i>flag</i> argument description of the <b>open()</b> routine for further values.</li> <li>• Information on whether the caller is a 32-bit or 64-bit thread.</li> <li>• In some circumstances address space information about the <i>arg</i> argument. See below.</li> </ul> <p><i>cred_p</i> Pointer to the user credential structure.</p> <p><i>rval_p</i> Pointer to return value for calling process. The driver may elect to set the value which is valid only if the <b>ioctl()</b> succeeds.</p>
<b>DESCRIPTION</b>	<p><b>ioctl()</b> provides character-access drivers with an alternate entry point that can be used for almost any operation other than a simple transfer of characters in and out of buffers. Most often, <b>ioctl()</b> is used to control device hardware parameters and establish the protocol used by the driver in processing data.</p> <p>The kernel determines that this is a character device, and looks up the entry point routines in <b>cb_ops</b> (9S). The kernel then packages the user request and arguments as integers and passes them to the driver's <b>ioctl()</b> routine. The kernel itself does no processing of the passed command, so it is up to the user program and the driver to agree on what the arguments mean.</p>

I/O control commands are used to implement the terminal settings passed from **ttymon(1M)** and **stty(1)**, to format disk devices, to implement a trace driver for debugging, and to clean up character queues. Since the kernel does not interpret the command type that defines the operation, a driver is free to define its own commands.

Drivers that use an **ioctl()** routine typically have a command to “read” the current **ioctl()** settings, and at least one other that sets new settings. Drivers can use the *mode* argument to determine if the device unit was opened for reading or writing, if necessary, by checking the **FREAD** or **FWRITE** setting.

If the third argument, *arg*, is a pointer to a user buffer, the driver can call the **copyin(9F)** and **copyout(9F)** functions to transfer data between kernel and user space.

Other kernel subsystems may need to call into the drivers **ioctl** routine. Drivers that intend to allow their **ioctl()** routine to be used in this way should publish the **ddi-kernel-ioctl** property on the associated devinfo node(s).

When the **ddi-kernel-ioctl** property is present, the *mode* argument is used to pass address space information about *arg* through to the driver. If the driver expects *arg* to contain a buffer address, and the **FKIOCTL** flag is set in *mode*, then the driver should assume that it is being handed a kernel buffer address. Otherwise, *arg* may be the address of a buffer from a user program. The driver can use **ddi\_copyin(9F)** and **ddi\_copyout(9F)** perform the correct type of copy operation for either kernel or user address spaces. See the example on **ddi\_copyout(9F)**.

Drivers have to interact with 32-bit and 64-bit applications. If a device driver shares data structures with the application (for example, through exported kernel memory) and the driver gets recompiled for a 64-bit kernel but the application remains 32-bit, binary layout of any data structures will be incompatible if they contain longs or pointers. The driver needs to know whether there is a model mismatch between the current thread and the kernel and take necessary action. The *mode* argument has additional bits set to determine the C Language Type Model which the current thread expects. *mode* has **FILP32** set if the current thread expects 32-bit (*ILP32*) semantics, or **FLP64** if the current thread expects 64-bit (*LP64*) semantics. *mode* is used in combination with **ddi\_model\_convert\_from(9F)** and the **FMODELS** mask to determine whether there is a data model mismatch between the current thread and the device driver (see the example below). The device driver might have to adjust the shape of data structures before exporting them to a user thread which supports a different data model.

To implement I/O control commands for a driver the following two steps are required:

1. Define the I/O control command names and the associated value in the driver's header and comment the commands.
2. Code the **ioctl** routine in the driver that defines the functionality for each I/O control command name that is in the header.

The **ioctl** routine is coded with instructions on the proper action to take for each command. It is commonly a **switch** statement, with each **case** definition corresponding to an **ioctl** name to identify the action that should be taken. However, the command passed to the driver by the user process is an integer value associated with the command name in the header.

**RETURN VALUES**

**ioctl()** should return **0** on success, or the appropriate error number. The driver may also set the value returned to the calling process through *rval\_p*.

**EXAMPLES**

The following is an example of the **ioctl()** entry point and how to support 32-bit and 64-bit applications with the same device driver.

```

struct passargs32 {
    int len;
    caddr32_t addr;
};

struct passargs {
    int len;
    caddr_t addr;
};

xxioctl(dev_t dev, int cmd, intptr_t arg, int mode,
cred_t *credp, int *rvalp) {
    struct passargs pa;

    #ifdef _MULTI_DATAMODEL
        switch (ddi_model_convert_from(mode & FMODELS)) {
            case DDI_MODEL_ILP32:
                {
                    struct passargs32 pa32;

                    ddi_copyin(arg, &pa32, sizeof (struct passargs32), mode);
                    pa.len = pa32.len;
                    pa.address = pa32.address;
                    break;
                }
            case DDI_MODEL_NONE:
                ddi_copyin(arg, &pa, sizeof (struct passargs), mode);
                break;
            }
    #else /* _MULTI_DATAMODEL */
        ddi_copyin(arg, &pa, sizeof (struct passargs), mode);
    #endif /* _MULTI_DATAMODEL */

    do_ioctl(&pa);
    ....
}

```

**SEE ALSO**

**stty(1)**, **ttymon(1M)**, **dkio(7I)**, **fbio(7I)**, **termio(7I)**, **open(9E)**, **put(9E)**, **srv(9E)**, **copyin(9F)**, **copyout(9F)**, **ddi\_copyin(9F)**, **ddi\_copyout(9F)**, **ddi\_model\_convert\_from(9F)**, **cb\_ops(9S)**

*Writing Device Drivers***WARNINGS**

Non-STREAMS driver **ioctl()** routines must make sure that user data is copied into or out of the kernel address space explicitly using **copyin(9F)**, **copyout(9F)**, **ddi\_copyin(9F)**, or **ddi\_copyout(9F)**, as appropriate.

It is a severe error to simply dereference pointers to the user address space, even when in user context.

Failure to use the appropriate copying routines can result in panics under load on some platforms, and reproducible panics on others.

**NOTES**

STREAMS drivers do not have **ioctl** routines. The stream head converts I/O control commands to **M\_IOCTL** messages, which are handled by the driver's **put(9E)** or **srv(9E)** routine.

<b>NAME</b>	ks_update – dynamically update kstats
<b>SYNOPSIS</b>	<pre>#include &lt;sys/types.h&gt; #include &lt;sys/kstat.h&gt; #include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt;  int prefix_ks_update(kstat_t *ksp, int rw);</pre>
<b>INTERFACE LEVEL ARGUMENTS</b>	<p>Solaris DDI specific (Solaris DDI)</p> <p><i>ksp</i>        Pointer to a <b>kstat</b>(9S) structure.</p> <p><i>rw</i>         Read/Write flag. Possible values are</p> <p>          <b>KSTAT_READ</b>    Update kstat structure statistics from the driver.</p> <p>          <b>KSTAT_WRITE</b>   Update driver statistics from the kstat structure.</p>
<b>DESCRIPTION</b>	<p>The kstat mechanism allows for an optional <b>ks_update()</b> function to update kstat data. This is useful for drivers where the underlying device keeps cheap hardware statistics, but extraction is expensive. Instead of constantly keeping the kstat data section up to date, the driver can supply a <b>ks_update()</b> function which updates the kstat's data section on demand. To take advantage of this feature, set the <b>ks_update</b> field before calling <b>kstat_install</b>(9F).</p> <p>The <b>ks_update()</b> function must have the following structure:</p> <pre>static int xx_kstat_update(kstat_t *ksp, int rw) {     if (rw == KSTAT_WRITE) {         /* update the native stats from ksp-&gt;ks_data */         /* return EACCES if you don't support this */     } else {         /* update ksp-&gt;ks_data from the native stats */     }     return (0); }</pre> <p>In general, the <b>ks_update()</b> routine may need to refer to provider-private data; for example, it may need a pointer to the provider's raw statistics. The <b>ks_private</b> field is available for this purpose. Its use is entirely at the provider's discretion.</p> <p>No kstat locking should be done inside the <b>ks_update()</b> routine. The caller will already be holding the kstat's <b>ks_lock</b> (to ensure consistent data) and will prevent the kstat from being removed.</p>

**RETURN VALUES****ks\_update()** should return

**0** for success  
**EACCES** if **KSTAT\_WRITE** is not allowed  
**EIO** for any other error.

**SEE ALSO****kstat\_create(9F)**, **kstat\_install(9F)**, **kstat(9S)***Writing Device Drivers*

<b>NAME</b>	mapdev_access – device mapping access entry point
<b>SYNOPSIS</b>	<pre>#include &lt;sys/sunddi.h&gt; int prefixmapdev_access(ddi_mapdev_handle_t handle, void *devprivate, off_t offset);</pre>
<b>INTERFACE LEVEL</b>	Solaris DDI specific (Solaris DDI).
<b>ARGUMENTS</b>	<p><i>handle</i>            An opaque pointer to a device mapping.</p> <p><i>devprivate</i>       Driver private mapping data from <b>ddi_mapdev(9F)</b>.</p> <p><i>offset</i>            The offset within device memory at which the access occurred.</p>
<b>DESCRIPTION</b>	<p>Future releases of Solaris will provide this function for binary and source compatibility. However, for increased functionality, use <b>devmap_access(9F)</b> or <b>devmap_contextmgt(9F)</b> instead. See <b>devmap_access(9F)</b> or <b>devmap_contextmgt(9F)</b> for details.</p> <p><b>mapdev_access()</b> is called when an access is made to a mapping that has either been newly created with <b>ddi_mapdev(9F)</b> or that has been enabled with a call to <b>ddi_mapdev_intercept(9F)</b>.</p> <p><b>mapdev_access()</b> is passed the <i>handle</i> of the mapped object on which an access has occurred. This handle uniquely identifies the mapping and is used as an argument to <b>ddi_mapdev_intercept(9F)</b> or <b>ddi_mapdev_nointercept(9F)</b> to control whether or not future accesses to the mapping will cause <b>mapdev_access()</b> to be called. In general, <b>mapdev_access()</b> should call <b>ddi_mapdev_intercept()</b> on the mapping that is currently in use and then call <b>ddi_mapdev_nointercept()</b> on the mapping that generated this call to <b>mapdev_access()</b>. This will ensure that a call to <b>mapdev_access()</b> will be generated for the current mapping next time it is accessed.</p> <p><b>mapdev_access()</b> must at least call <b>ddi_mapdev_nointercept()</b> with <i>offset</i> passed in in order for the access to succeed. A request to allow accesses affects the entire page containing the <i>offset</i>.</p> <p>Accesses to portions of mappings that have been disabled by a call to <b>ddi_mapdev_nointercept()</b> will not generate a call to <b>mapdev_access()</b>. A subsequent call to <b>ddi_mapdev_intercept()</b> will enable <b>mapdev_access()</b> to be called again.</p> <p>A non-zero return value from <b>mapdev_access()</b> will cause the corresponding operation to fail. The failure may result in a SIGSEGV or SIGBUS signal being delivered to the process.</p>
<b>RETURN VALUES</b>	<b>mapdev_access()</b> should return <b>0</b> on success, <b>-1</b> if there was a hardware error, or the return value from <b>ddi_mapdev_intercept()</b> or <b>ddi_mapdev_nointercept()</b> .
<b>CONTEXT</b>	This function is called from user context only.

**EXAMPLES**

The following shows an example of managing a device context that is one page in length.

```

ddi_mapdev_handle_t cur_hdl;
static int
xxmapdev_access(ddi_mapdev_handle_t handle, void *devprivate,
off_t offset)
{
    int    err;

    /* enable calls to mapdev_access for the current mapping */
    if (cur_hdl != NULL) {
        if ((err = ddi_mapdev_intercept(cur_hdl, off, 0)) != 0)
            return (err);
    }

    /* Switch device context - device dependent*/
    ...

    /* Make handle the new current mapping */
    cur_hdl = handle;

    /*
     * Disable callbacks and complete the access for the
     * mapping that generated this callback.
     */

    return (ddi_mapdev_nointercept(handle, off, 0));
}

```

**SEE ALSO**

**mmap(2)**, **mapdev\_dup(9E)**, **mapdev\_free(9E)**, **segmap(9E)**, **ddi\_mapdev(9F)**,  
**ddi\_mapdev\_intercept(9F)**, **ddi\_mapdev\_nointercept(9F)**, **ddi\_mapdev\_ctl(9S)**

*Writing Device Drivers*



<b>NAME</b>	mapdev_dup – device mapping duplication entry point
<b>SYNOPSIS</b>	<pre>#include &lt;sys/sunddi.h&gt; int prefixmapdev_dup(ddi_mapdev_handle_t handle, void *devprivate,     ddi_mapdev_handle_t new_handle, void **new_devprivatep);</pre>
<b>INTERFACE LEVEL</b>	Solaris DDI specific (Solaris DDI).
<b>ARGUMENTS</b>	<p><i>handle</i>            The handle of the mapping that is being duplicated.</p> <p><i>devprivate</i>       Driver private mapping data from the mapping that is being duplicated.</p> <p><i>new_handle</i>        An opaque pointer to the duplicated device mapping.</p> <p><i>new_devprivatep</i> A pointer to be filled in by the driver with the driver private mapping data for the duplicated device mapping.</p>
<b>DESCRIPTION</b>	<p>Future releases of Solaris will provide this function for binary and source compatibility. However, for increased functionality, use <b>devmap_dup(9F)</b> instead. See <b>devmap_dup(9F)</b> for details.</p> <p><b>mapdev_dup()</b> is called when a device mapping is duplicated such as through <b>fork(2)</b>. <b>mapdev_dup()</b> is expected to generate new driver private data for the new mapping, and set <i>new_devprivatep</i> to point to it. <i>new_handle</i> is the handle of the new mapped object.</p> <p>A non-zero return value from <b>mapdev_dup()</b> will cause the corresponding operation, such as <b>fork()</b> to fail.</p>
<b>RETURN VALUES</b>	<b>mapdev_dup()</b> returns <b>0</b> for success or the appropriate error number on failure.
<b>CONTEXT</b>	This function is called from user context only.
<b>EXAMPLES</b>	<pre>static int xxmapdev_dup(ddi_mapdev_handle_t handle, void *devprivate,     ddi_mapdev_handle_t new_handle, void **new_devprivate) {     struct xxpvtdata    *pvtdata;     /* Allocate a new private data structure */     pvtdata = kmem_alloc(sizeof (struct xxpvtdata), KM_SLEEP);     /* Copy the old data to the new - device dependent*/     ...     /* Return the new data */     *new_pvtdata = pvtdata;     return (0); }</pre>

**SEE ALSO**

**fork(2), mmap(2), mapdev\_access(9E), mapdev\_free(9E), segmap(9E), ddi\_mapdev(9F),  
ddi\_mapdev\_intercept(9F), ddi\_mapdev\_nointercept(9F), ddi\_mapdev\_ctl(9S)**  
*Writing Device Drivers*

<b>NAME</b>	mapdev_free – device mapping free entry point
<b>SYNOPSIS</b>	<b>#include</b> <sys/sunddi.h> <b>void</b> <i>prefix</i> mapdev_free(ddd_mapdev_handle_t handle, void *devprivate);
<b>INTERFACE LEVEL</b>	Solaris DDI specific (Solaris DDI).
<b>ARGUMENTS</b>	<i>handle</i> An opaque pointer to a device mapping. <i>devprivate</i> Driver private mapping data from <b>ddd_mapdev(9F)</b> .
<b>DESCRIPTION</b>	Future releases of Solaris will provide this function for binary and source compatibility. However, for increased functionality, use <b>devmap_unmap(9F)</b> instead. See <b>devmap_unmap(9F)</b> for details. <b>mapdev_free()</b> is called when a mapping created by <b>ddd_mapdev(9F)</b> is being destroyed. <b>mapdev_free()</b> receives the <i>handle</i> of the mapping being destroyed and a pointer to the driver private data for this mapping in <i>devprivate</i> . The <b>mapdev_free()</b> routine is expected to free any resources that were allocated by the driver for this mapping.
<b>CONTEXT</b>	This function is called from user context only.
<b>EXAMPLES</b>	<pre>static void xxmapdev_free(ddd_mapdev_handle_t hdl, void *pvtdata) {     /* Destroy the driver private data - Device dependent */     ...     kmem_free(pvtdata, sizeof (struct xpvtdata)); }</pre>
<b>SEE ALSO</b>	<b>exit(2)</b> , <b>mmap(2)</b> , <b>munmap(2)</b> , <b>mapdev_access(9E)</b> , <b>mapdev_dup(9E)</b> , <b>segmap(9E)</b> , <b>ddd_mapdev(9F)</b> , <b>ddd_mapdev_intercept(9F)</b> , <b>ddd_mapdev_nointercept(9F)</b> , <b>ddd_mapdev_ctl(9S)</b> <i>Writing Device Drivers</i>

<b>NAME</b>	mmap – check virtual mapping for memory mapped device
<b>SYNOPSIS</b>	<pre>#include &lt;sys/types.h&gt; #include &lt;sys/cred.h&gt; #include &lt;sys/mman.h&gt; #include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt;  int prefixmmap(dev_t dev, off_t off, int prot);</pre>
<b>INTERFACE LEVEL</b>	Architecture independent level 1 (DDI/DKI).
<b>ARGUMENTS</b>	<p><i>dev</i> Device whose memory is to be mapped.</p> <p><i>off</i> Offset within device memory at which mapping begins.</p> <p><i>prot</i> A bit field that specifies the protections this page of memory will receive. Possible settings are:</p> <ul style="list-style-type: none"> <li><b>PROT_READ</b> Read access will be granted.</li> <li><b>PROT_WRITE</b> Write access will be granted.</li> <li><b>PROT_EXEC</b> Execute access will be granted.</li> <li><b>PROT_USER</b> User-level access will be granted.</li> <li><b>PROT_ALL</b> All access will be granted.</li> </ul>
<b>DESCRIPTION</b>	<p>Future releases of Solaris will provide this function for binary and source compatibility. However, for increased functionality, use <b>devmap(9E)</b> instead. See <b>devmap(9E)</b> for details.</p> <p>The <b>mmap(9E)</b> entry point is a required entry point for character drivers supporting memory-mapped devices. A memory mapped device has memory that can be mapped into a process's address space. The <b>mmap(2)</b> system call, when applied to a character special file, allows this device memory to be mapped into user space for direct access by the user application.</p> <p>The <b>mmap(9E)</b> entry point is called as a result of an <b>mmap(2)</b> system call, and also as a result of a page fault. <b>mmap(9E)</b> is called to translate the offset <i>off</i> in device memory to the corresponding physical page frame number.</p> <p>The <b>mmap(9E)</b> entry point checks if the offset <i>off</i> is within the range of pages exported by the device. For example, a device that has 512 bytes of memory that can be mapped into user space should not support offsets greater than 512. If the offset does not exist, then -1 is returned. If the offset does exist, <b>mmap(9E)</b> returns the value returned by <b>hat_getkpfnum(9F)</b> for the physical page in device memory containing the offset <i>off</i>.</p> <p><b>hat_getkpfnum(9F)</b> accepts a kernel virtual address as an argument. A kernel virtual address can be obtained by calling <b>ddi_regs_map_setup(9F)</b> in the driver's <b>attach(9E)</b> routine. The corresponding <b>ddi_regs_map_free(9F)</b> call can be made in the driver's <b>detach(9E)</b> routine. Refer to the <b>EXAMPLES</b> section below for more information.</p>

**mmap(9E)** should only be supported for memory-mapped devices. See the **segmap(9E)** and **ddi\_mapdev(9F)** reference pages for further information on memory-mapped device drivers.

If a device driver shares data structures with the application (for example, through exported kernel memory) and the driver gets recompiled for a 64-bit kernel but the application remains 32-bit, binary layout of any data structures will be incompatible if they contain longs or pointers. The driver needs to know whether there is a model mismatch between the current thread and the kernel and take necessary action.

**ddi\_mmap\_get\_model(9F)** can be used to get the C Language Type Model which the current thread expects. In combination with **ddi\_model\_convert\_from(9F)** the driver can determine whether there is a data model mismatch between the current thread and the device driver. The device driver might have to adjust the shape of data structures before exporting them to a user thread which supports a different data model. (see **ddi\_mmap\_get\_model(9F)** for an example)

## RETURN VALUES

If the protection and offset are valid for the device, the driver should return the value returned by **hat\_getkpfnum(9F)**, for the page at offset *off* in the device's memory. If not, **-1** should be returned.

## EXAMPLES

The following is an example of the **mmap()** entry point. If offset *off* is valid, **hat\_getkpfnum(9F)** is called to obtain the page frame number corresponding to this offset in the device's memory. In this example, **xsp→regp→csr** is a kernel virtual address which maps to device memory. **ddi\_regs\_map\_setup(9F)** can be used to obtain this address. For example, **ddi\_regs\_map\_setup(9F)** can be called in the driver's **attach(9E)** routine. The resulting kernel virtual address is stored in the **xxstate** structure (see **ddi\_soft\_state(9F)**), which is accessible from the driver's **mmap()** entry point. The corresponding **ddi\_regs\_map\_free(9F)** call can be made in the driver's **detach(9E)** routine.

```

    struct reg {
        uint8_t csr;
        uint8_t data;
    };

    struct xxstate {
        ...
        struct reg    *regp
        ...
    };

    struct xxstate *xsp;
    ...

    static int
    xxmmap(dev_t dev, off_t off, int prot)
    {

```

```

    int         instance;
    struct xxstate *xsp;

    /* No write access */
    if (prot & PROT_WRITE)
        return (-1);

    instance = getminor(dev);
    xsp = ddi_get_soft_state(statep, instance);
    if (xsp == NULL)
        return (-1);

    /* check for a valid offset */
    if ( off is invalid )
        return (-1);
    return (hat_getkpfnum (xsp->regp->csr + off));
}

```

**SEE ALSO**

**mmap(2)**, **attach(9E)**, **detach(9E)**, **devmap(9E)**, **segmap(9E)**, **ddi\_btop(9F)**, **ddi\_get\_soft\_state(9F)**, **ddi\_mmap\_get\_model(9F)**, **ddi\_model\_convert\_from(9F)**, **ddi\_regs\_map\_free(9F)**, **ddi\_regs\_map\_setup(9F)**, **ddi\_soft\_state(9F)**, **devmap\_setup(9F)**, **getminor(9F)**, **hat\_getkpfnum(9F)**

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**NOTES**

For some devices, mapping device memory in the driver's **attach(9E)** routine and unmaping device memory in the driver's **detach(9E)** routine is a sizeable drain on system resources. This is especially true for devices with a large amount of physical address space.

One alternative is to create a mapping for only the first page of device memory in **attach(9E)**. If the device memory is contiguous, a kernel page frame number may be obtained by calling **hat\_getkpfnum(9F)** with the kernel virtual address of the first page of device memory and adding the desired page offset to the result. The page offset may be obtained by converting the byte offset *off* to pages (see **ddi\_btop(9F)**).

Another alternative is to call **ddi\_regs\_map\_setup(9F)** and **ddi\_regs\_map\_free(9F)** in **mmap**. These function calls would bracket the call to **hat\_getkpfnum(9F)**.

However, note that the above alternatives may not work in all cases. The existence of intermediate nexus devices with memory management unit translation resources which are not locked down may cause unexpected and undefined behavior.

<b>NAME</b>	open – gain access to a device
<b>SYNOPSIS</b> Block and Character	<pre>#include &lt;sys/types.h&gt; #include &lt;sys/file.h&gt; #include &lt;sys/errno.h&gt; #include &lt;sys/open.h&gt; #include &lt;sys/cred.h&gt; #include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt;  int prefixopen(dev_t *devp, int flag, int otyp, cred_t *cred_p);</pre>
<b>STREAMS</b>	<pre>#include &lt;sys/file.h&gt; #include &lt;sys/stream.h&gt; #include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt;  int prefixopen(queue_t *q, dev_t *devp, int oflag, int sflag, cred_t *cred_p);</pre>
<b>INTERFACE LEVEL</b>	Architecture independent level 1 (DDI/DKI). This entry point is <b>required</b> , but it can be <b>nulldev(9F)</b> .
<b>ARGUMENTS</b> Block and Character	<p><i>devp</i> Pointer to a device number.</p> <p><i>flag</i> A bit field passed from the user program <b>open(2)</b> system call that instructs the driver on how to open the file. Valid settings are:</p> <p><b>FEXCL</b> Open the device with exclusive access; fail all other attempts to open the device.</p> <p><b>FNDELAY</b> Open the device and return immediately (do not block the open even if something is wrong).</p> <p><b>FREAD</b> Open the device with read-only permission (if ORed with <b>FWRITE</b>, then allow both read and write access)</p> <p><b>FWRITE</b> Open a device with write-only permission (if ORed with <b>FREAD</b>, then allow both read and write access)</p> <p><i>otyp</i> Parameter supplied so that the driver can determine how many times a device was opened and for what reasons.</p> <p>For <b>OTYP_BLK</b> and <b>OTYP_CHR</b>, the <b>open()</b> routine may be called many times, but the <b>close(9E)</b> routine is called only when the last reference to a device is removed. If the device is accessed through file descriptors, this is by a call to <b>close(2)</b> or <b>exit(2)</b>. If the device is accessed through memory mapping, this is by a call to <b>munmap(2)</b> or <b>exit(2)</b>.</p> <p>For <b>OTYP_LYR</b>, there is exactly one <b>close(9E)</b> for each <b>open()</b> called. This permits software drivers to exist above hardware drivers and removes any ambiguity from the hardware driver regarding how a device is used.</p>

## STREAMS

<b>OTYP_BLK</b>	Open occurred through block interface for the device
<b>OTYP_CHR</b>	Open occurred through the raw/character interface for the device
<b>OTYP_LYR</b>	Open a layered process. This flag is used when one driver calls another driver's <b>open</b> or <b>close</b> (9E) routine. The calling driver will make sure that there is one layered close for each layered open. This flag applies to both block and character devices.
<i>cred_p</i>	Pointer to the user credential structure.
<i>q</i>	A pointer to the read <b>queue</b> .
<i>devp</i>	Pointer to a device number. For STREAMS modules, <i>devp</i> always points to the device number associated with the driver at the end (tail) of the stream.
<i>oflag</i>	Valid <i>oflag</i> values are <b>FEXCL</b> , <b>FNDELAY</b> , <b>FREAD</b> , and <b>FWRITEL</b> , the same as those listed above for <i>flag</i> . For STREAMS modules, <i>oflag</i> is always set to <b>0</b> .
<i>sflag</i>	Valid values are as follows:
<b>CLONEOPEN</b>	Indicates that the <b>open</b> routine is called through the clone driver. The driver should return a unique device number.
<b>MODOPEN</b>	Modules should be called with <i>sflag</i> set to this value. Modules should return an error if they are called with <i>sflag</i> set to a different value. Drivers should return an error if they are called with <i>sflag</i> set to this value.
<b>0</b>	Indicates a driver is opened directly, without calling the clone driver.
<i>cred_p</i>	Pointer to the user credential structure.

## DESCRIPTION

The driver's **open()** routine is called by the kernel during an **open(2)** or a **mount(2)** on the special file for the device. The routine should verify that the minor number component of *\*devp* is valid, that the type of access requested by *otyp* and *flag* is appropriate for the device, and, if required, check permissions using the user credentials pointed to by *cred\_p*.

The **open()** routine is passed a pointer to a device number so that the driver can change the minor number. This allows drivers to dynamically create minor instances of the device. An example of this might be a pseudo-terminal driver that creates a new pseudo-terminal whenever it is opened. A driver that chooses the minor number dynamically, normally creates only one minor device node in **attach(9E)** with **ddi\_create\_minor\_node(9F)**, then changes the minor number component of *\*devp* using **makedevice(9F)** and **getmajor(9F)**. The driver needs to keep track of available minor numbers internally.

**\*devp = makedevice(getmajor(\*devp), new\_minor);**



**RETURN VALUES**

The **open()** routine should return 0 for success, or the appropriate error number.

**SEE ALSO**

**close(2)**, **exit(2)**, **mmap(2)**, **mount(2)**, **munmap(2)**, **open(2)**, **intro(9E)**, **attach(9E)**, **close(9E)**, **ddi\_create\_minor\_node(9F)**, **getmajor(9F)**, **getminor(9F)**, **makedevice(9F)**, **nulldev(9F)**

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**WARNINGS**

Do not attempt to change the major number.

<b>NAME</b>	pm – power management properties
<b>DESCRIPTION</b>	<p>There is a property, <b>pm-hardware-state</b>, that may be used to influence the behavior of the power management portion of the DDI framework. Its syntax and interpretation is described below.</p> <p>Note that this property is only interpreted by the system immediately after the device has successfully attached. Changes in the property made by the driver after the driver has attached will not be recognized.</p> <p><b>pm-hardware-state</b> is a string-valued property. The existence of the <b>pm-hardware-state</b> property indicates that a device needs special handling by the power management framework with regard to its hardware state.</p> <p>If the value of this property is <i>needs-suspend-resume</i>, the device has a hardware state that cannot be deduced by the framework. The framework definition of a device with hardware state is one with a <b>reg</b> property. Some drivers, such as SCSI disk and tape drivers, have no <b>reg</b> property but manage devices with "remote" hardware. Such a device must have a <b>pm-hardware-state</b> property with a value of <i>needs-suspend-resume</i> in order for the system to identify it as needing a call to its <b>detach(9E)</b> entry point with command <b>DDI_SUSPEND</b> or <b>DDI_PM_SUSPEND</b> before power is removed from the device, and a call to <b>attach(9E)</b> with command <b>DDI_RESUME</b> or <b>DDI_PM_RESUME</b> after power is restored.</p> <p>A value of <i>no-suspend-resume</i> indicates that, in spite of the existence of a <b>reg</b> property, a device has no hardware state that needs saving and restoring. A device exporting this property will not have its <b>detach()</b> entry point called with command <b>DDI_SUSPEND</b> or <b>DDI_PM_SUSPEND</b> before power is removed from the device, nor will its <b>attach()</b> entry point be called with command <b>DDI_RESUME</b> after power is restored to the device.</p> <p>A value of <i>parental-suspend-resume</i> indicates that the device does not implement the <b>detach()</b> <b>DDI_SUSPEND</b> or <b>DDI_PM_SUSPEND</b> semantics, nor the <b>attach()</b> <b>DDI_RESUME</b> or <b>DDI_PM_RESUME</b> semantics, but that a call should be made up the device tree by the framework to effect the saving and/or restoring of hardware state for this device.</p>
<b>EXAMPLES</b>	<p>Because the <b>sd</b> driver drives a device with no <b>reg</b> property, but needs to know when power will be removed from its controller, it exports a property with the name <b>pm-hardware-state</b> and the value <i>needs-suspend-resume</i>.</p> <p>On an x86 system with Advanced Power Management (APM) BIOS support, a device that can have its state saved by the APM BIOS can create a <b>pm-hardware-state</b> property with the value <i>parental-suspend-resume</i>, and requests to save the state of the device will be passed up the device tree to the platform-specific power management driver that will call into the BIOS.</p>
<b>SEE ALSO</b>	<b>power.conf(4)</b> , <b>pm(7D)</b> , <b>attach(9E)</b> , <b>detach(9E)</b> , <b>pm_busy_component(9F)</b> , <b>pm_create_components(9F)</b> , <b>pm_destroy_components(9F)</b> , <b>pm_idle_component(9F)</b>

<b>NAME</b>	power – power a device attached to the system
<b>SYNOPSIS</b>	<pre>#include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt; int prefixpower(dev_info_t *dip, int component, int level)</pre>
<b>INTERFACE LEVEL</b>	Solaris DDI specific (Solaris DDI). This entry point is required. If the driver writer does not supply this entry point, the <b>nulldev(9F)</b> function must be used.
<b>ARGUMENTS</b>	<p><i>dip</i>                    A pointer to the device's <b>dev_info</b> structure.</p> <p><i>component</i>            The component of the driver to be managed.</p> <p><i>level</i>                    The desired power level for the component.</p>
<b>DESCRIPTION</b>	<p>The <b>power()</b> function is the device-specific power management entry point. This function is called when the system wants the driver to set the power level of <i>component</i> to <i>level</i>.</p> <p>The <i>level</i> argument is the driver-defined power level to which <i>component</i> is set. Except for power level 0 which is defined by the framework to mean “powered off”, the interpretation of <i>level</i> is entirely up to the driver.</p> <p>The <i>component</i> argument is the component of the device to be power-managed. Except for component 0, which must represent the entire device, the interpretation of <i>component</i> is entirely up to the driver.</p> <p>The <b>power()</b> function can assume that the driver will be suspended (using <b>detach(9E)</b> with command <b>DDI_PM_SUSPEND</b>), before a request is made to set component 0 to power level 0 and resumed (using <b>attach(9E)</b> with command <b>DDI_PM_RESUME</b>) after setting component 0 from power level 0 to a non-zero power level.</p>
<b>RETURN VALUES</b>	<p>The <b>power()</b> function returns:</p> <p><b>DDI_SUCCESS</b>    Successfully set the power to the requested level.</p> <p><b>DDI_FAILURE</b>    Failed to set the power to the requested level.</p>
<b>CONTEXT</b>	The <b>power()</b> function is called from user or kernel context only.
<b>SEE ALSO</b>	<p><b>attach(9E)</b>, <b>detach(9E)</b>, <b>nulldev(9F)</b>, <b>pm_busy_component(9F)</b>,  <b>pm_create_components(9F)</b>, <b>pm_destroy_components(9F)</b>, <b>pm_idle_component(9F)</b></p> <p><i>Writing Device Drivers</i></p>

<b>NAME</b>	print – display a driver message on system console
<b>SYNOPSIS</b>	<pre>#include &lt;sys/types.h&gt; #include &lt;sys/errno.h&gt; #include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt;  int prefixprint(dev_t dev, char *str);</pre>
<b>INTERFACE LEVEL</b>	Architecture independent level 1 (DDI/DKI). This entry point is <b>required</b> for block devices.
<b>ARGUMENTS</b>	<p><i>dev</i>        Device number.</p> <p><i>str</i>        Pointer to a character string describing the problem.</p>
<b>DESCRIPTION</b>	The <b>print()</b> routine is called by the kernel when it has detected an exceptional condition (such as out of space) in the device. To display the message on the console, the driver should use the <b>cmn_err(9F)</b> kernel function. The driver should print the message along with any driver specific information.
<b>RETURN VALUES</b>	The <b>print()</b> routine should return <b>0</b> for success, or the appropriate error number. The <b>print</b> routine can fail if the driver implemented a non-standard <b>print()</b> routine that attempted to perform error logging, but was unable to complete the logging for whatever reason.
<b>SEE ALSO</b>	<p><b>cmn_err(9F)</b></p> <p><i>Writing Device Drivers</i></p>

<b>NAME</b>	probe – determine if a non-self-identifying device is present								
<b>SYNOPSIS</b>	<pre>#include &lt;sys/conf.h&gt; #include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt;  static int prefixprobe(dev_info_t *dip);</pre>								
<b>INTERFACE LEVEL</b>	Solaris DDI specific (Solaris DDI). This entry point is <b>required</b> for non-self-identifying devices. You must write it for such devices. For self-identifying devices, <b>nulldev(9F)</b> should be specified in the <b>dev_ops(9S)</b> structure if a probe routine is not necessary.								
<b>ARGUMENTS</b>	<i>dip</i> Pointer to the device's <b>dev_info</b> structure.								
<b>DESCRIPTION</b>	<p><b>probe()</b> determines whether the device corresponding to <i>dip</i> actually exists and is a valid device for this driver. <b>probe()</b> is called after <b>identify(9E)</b> and before <b>attach(9E)</b> for a given <i>dip</i>. For example, the <b>probe()</b> routine can map the device registers using <b>ddi_map_regs(9F)</b> then attempt to access the hardware using <b>ddi_peek(9F)</b> and/or <b>ddi_poke(9F)</b> and determine if the device exists. Then the device registers should be unmapped using <b>ddi_unmap_regs(9F)</b>.</p> <p><b>probe()</b> should only probe the device – it should not create or change any software state. Device initialization should be done in <b>attach(9E)</b>.</p> <p>For a self-identifying device, this entry point is not necessary. However, if a device exists in both self-identifying and non-self-identifying forms, a <b>probe()</b> routine can be provided to simplify the driver. <b>ddi_dev_is_sid(9F)</b> can then be used to determine whether <b>probe()</b> needs to do any work. See <b>ddi_dev_is_sid(9F)</b> for an example.</p>								
<b>RETURN VALUES</b>	<table border="0"> <tr> <td><b>DDI_PROBE_SUCCESS</b></td> <td>if the probe was successful.</td> </tr> <tr> <td><b>DDI_PROBE_FAILURE</b></td> <td>if the probe failed.</td> </tr> <tr> <td><b>DDI_PROBE_DONTCARE</b></td> <td>if the probe was unsuccessful, yet <b>attach(9E)</b> should still be called.</td> </tr> <tr> <td><b>DDI_PROBE_PARTIAL</b></td> <td>if the instance is not present now, but may be present in the future.</td> </tr> </table>	<b>DDI_PROBE_SUCCESS</b>	if the probe was successful.	<b>DDI_PROBE_FAILURE</b>	if the probe failed.	<b>DDI_PROBE_DONTCARE</b>	if the probe was unsuccessful, yet <b>attach(9E)</b> should still be called.	<b>DDI_PROBE_PARTIAL</b>	if the instance is not present now, but may be present in the future.
<b>DDI_PROBE_SUCCESS</b>	if the probe was successful.								
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<b>DDI_PROBE_PARTIAL</b>	if the instance is not present now, but may be present in the future.								
<b>SEE ALSO</b>	<p><b>attach(9E)</b>, <b>identify(9E)</b>, <b>ddi_dev_is_sid(9F)</b>, <b>ddi_map_regs(9F)</b>, <b>ddi_peek(9F)</b>, <b>ddi_poke(9F)</b>, <b>nulldev(9F)</b>, <b>dev_ops(9S)</b></p> <p><i>Writing Device Drivers</i></p>								

<b>NAME</b>	prop_op – report driver property information
<b>SYNOPSIS</b>	<pre>#include &lt;sys/types.h&gt; #include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt;  int prefixprop_op(dev_t dev, dev_info_t *dip, ddi_prop_op_t prop_op, int flags,     char *name, caddr_t valuep, int *lengthp)</pre>
<b>INTERFACE LEVEL</b>	Solaris DDI specific (Solaris DDI). This entry point is <i>required</i> , but it can be <b>ddi_prop_op(9F)</b> .
<b>ARGUMENTS</b>	<p><i>dev</i> Device number associated with this device.</p> <p><i>dip</i> A pointer to the device information structure for this device.</p> <p><i>prop_op</i> Property operator. Valid operators are:</p> <p><b>PROP_LEN</b> Get property length only. (<i>valuep</i> unaffected)</p> <p><b>PROP_LEN_AND_VAL_BUF</b> Get length and value into caller's buffer. (<i>valuep</i> used as input)</p> <p><b>PROP_LEN_AND_VAL_ALLOC</b> Get length and value into allocated buffer. (<i>valuep</i> returned as pointer to pointer to allocated buffer)</p> <p><i>flags</i> The only possible flag value is:</p> <p><b>DDI_PROP_DONTPASS</b> Don't pass request to parent if property not found.</p> <p><i>name</i> Pointer to name of property to be interrogated.</p> <p><i>valuep</i> If <i>prop_op</i> is <b>PROP_LEN_AND_VAL_BUF</b>, this should be a pointer to the users buffer. If <i>prop_op</i> is <b>PROP_LEN_AND_VAL_ALLOC</b>, this should be the <i>address</i> of a pointer.</p> <p><i>lengthp</i> On exit, <i>*lengthp</i> will contain the property length. If <i>prop_op</i> is <b>PROP_LEN_AND_VAL_BUF</b> then before calling <b>prop_op()</b>, <i>lengthp</i> should point to an <b>int</b> that contains the length of caller's buffer.</p>
<b>DESCRIPTION</b>	<b>prop_op()</b> is an entry point which reports the values of certain "properties" of the driver or device to the system. Each driver must have an <i>prefixprop_op</i> entry point, but most drivers which do not need to create or manage their own properties can use <b>ddi_prop_op()</b> for this entry point. Then the driver can use <b>ddi_prop_update(9F)</b> to create properties for its device.

## RETURN VALUES

**prop\_op()** should return:

<b>DDI_PROP_SUCCESS</b>	Property found and returned.
<b>DDI_PROP_NOT_FOUND</b>	Property not found.
<b>DDI_PROP_UNDEFINED</b>	Prop explicitly undefined.
<b>DDI_PROP_NO_MEMORY</b>	Property found, but unable to allocate memory. <i>lengthp</i> has the correct property length.
<b>DDI_PROP_BUF_TOO_SMALL</b>	Property found, but the supplied buffer is too small. <i>lengthp</i> has the correct property length.

## EXAMPLES

In the following example, **prop\_op()** intercepts requests for the *temperature* property. The driver tracks changes to *temperature* using a variable in the state structure in order to avoid frequent calls to **ddi\_prop\_update(9F)**. The *temperature* property is only updated when a request is made for this property. It then uses the system routine **ddi\_prop\_op(9F)** to process the property request. If the property request is not specific to a device, the driver does not intercept the request. This is indicated when the value of the *dev* parameter is equal to **DDI\_DEV\_T\_ANY**.

```
int temperature;          /* current device temperature */
.
.
.

static int
xxprop_op(dev_t dev, dev_info_t *dip, ddi_prop_op_t prop_op,
          int flags, char *name, caddr_t valuep, int *lengthp)
{
    int instance;
    struct xxstate *xsp;

    if (dev == DDI_DEV_T_ANY)
        goto skip;

    instance = getminor(dev);
    xsp = ddi_get_soft_state(statep, instance);
    if (xsp == NULL)
        return (DDI_PROP_NOTFOUND);

    if (strcmp(name, "temperature") == 0) {
        ddi_prop_update_int(dev, dip, "temperature", temperature);
    }
    /*      other cases...      */

    skip:
    return (ddi_prop_op(dev, dip, prop_op, flags, name, valuep, lengthp));
}
```

**SEE ALSO**

**ddi\_prop\_op(9F), ddi\_prop\_update(9F)**

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<b>NAME</b>	put – receive messages from the preceding queue
<b>SYNOPSIS</b>	<pre>#include &lt;sys/types.h&gt; #include &lt;sys/stream.h&gt; #include &lt;sys/stropts.h&gt; #include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt;  int prefixrput(queue_t *q, mblk_t *mp); /* read side */ int prefixwput(queue_t *q, mblk_t *mp); /* write side */</pre>
<b>INTERFACE LEVEL ARGUMENTS</b>	<p>Architecture independent level 1 (DDI/DKI). This entry point is <b>required</b> for STREAMS.</p> <p><i>q</i>        Pointer to the <b>queue(9S)</b> structure.</p> <p><i>mp</i>        Pointer to the message block.</p>
<b>DESCRIPTION</b>	<p>The primary task of the <b>put()</b> routine is to coordinate the passing of messages from one queue to the next in a stream. The <b>put()</b> routine is called by the preceding stream component (stream module, driver, or stream head). <b>put()</b> routines are designated “write” or “read” depending on the direction of message flow.</p> <p>With few exceptions, a streams module or driver must have a <b>put()</b> routine. One exception is the read side of a driver, which does not need a <b>put()</b> routine because there is no component downstream to call it. The <b>put()</b> routine is always called before the component’s corresponding <b>srv(9E)</b> (service) routine, and so <b>put()</b> should be used for the immediate processing of messages.</p> <p>A <b>put()</b> routine must do at least one of the following when it receives a message:</p> <ul style="list-style-type: none"> <li>• pass the message to the next component on the stream by calling the <b>putnext(9F)</b> function</li> <li>• process the message, if immediate processing is required (for example, to handle high priority messages)</li> <li>• enqueue the message (with the <b>putq(9F)</b> function) for deferred processing by the service <b>srv(9E)</b> routine</li> </ul> <p>Typically, a <b>put()</b> routine will switch on message type, which is contained in the <b>db_type</b> member of the <b>datab</b> structure pointed to by <i>mp</i>. The action taken by the <b>put()</b> routine depends on the message type. For example, a <b>put()</b> routine might process high priority messages, enqueue normal messages, and handle an unrecognized <b>M_IOCTL</b> message by changing its type to <b>M_IOCNAK</b> (negative acknowledgement) and sending it back to the stream head using the <b>qreply(9F)</b> function.</p>

The **putq**(9F) function can be used as a module's **put()** routine when no special processing is required and all messages are to be enqueued for the **srv** (9E) routine.

**RETURN VALUES**

Ignored.

**CONTEXT**

**put()** routines do not have user context.

**SEE ALSO**

**srv**(9E), **putctl**(9F), **putctl1**(9F), **putnext**(9F), **putnextctl**(9F), **putnextctl1**(9F), **putq**(9F), **qreply**(9F), **queue**(9S), **streamtab**(9S)

*Writing Device Drivers*

*STREAMS Programming Guide*

<b>NAME</b>	read – read data from a device
<b>SYNOPSIS</b>	<pre>#include &lt;sys/types.h&gt; #include &lt;sys/errno.h&gt; #include &lt;sys/open.h&gt; #include &lt;sys/uio.h&gt; #include &lt;sys/cred.h&gt; #include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt;  int prefixread(dev_t dev, struct uio *uio_p, cred_t *cred_p);</pre>
<b>INTERFACE LEVEL</b>	Architecture independent level 1 (DDI/DKI). This entry point is <i>optional</i> .
<b>ARGUMENTS</b>	<p><i>dev</i>            Device number.</p> <p><i>uio_p</i>          Pointer to the <b>uio</b>(9S) structure that describes where the data is to be stored in user space.</p> <p><i>cred_p</i>          Pointer to the user credential structure for the I/O transaction.</p>
<b>DESCRIPTION</b>	The driver <b>read()</b> routine is called indirectly through <b>cb_ops</b> (9S) by the <b>read</b> (2) system call. The <b>read()</b> routine should check the validity of the minor number component of <i>dev</i> and the user credential structure pointed to by <i>cred_p</i> (if pertinent). The <b>read()</b> routine should supervise the data transfer into the user space described by the <b>uio</b> (9S) structure.
<b>RETURN VALUES</b>	The <b>read()</b> routine should return <b>0</b> for success, or the appropriate error number.
<b>EXAMPLES</b>	<p>The following is an example of a <b>read()</b> routine using <b>physio</b>(9F) to perform reads from a non-seekable device:</p> <pre>static int xxread(dev_t dev, struct uio *uiop, cred_t *credp) {     int                            rval;     offset_t                      off;     int                            instance;     xx_t                          xx;      instance = getminor(dev);     xx = ddi_get_soft_state(xxstate, instance);     if (xx == NULL)         return (ENXIO);     off = uiop-&gt;uio_loffset;     rval = physio(xxstrategy, NULL, dev, B_READ,                  xxmin, uiop);     uiop-&gt;uio_loffset = off;</pre>

```
        return (rval);  
    }
```

**SEE ALSO** [read\(2\)](#), [write\(9E\)](#), [physio\(9F\)](#), [cb\\_ops\(9S\)](#), [uio\(9S\)](#)  
*Writing Device Drivers*

<b>NAME</b>	segmap – map device memory into user space																																
<b>SYNOPSIS</b>	<pre>#include &lt;sys/types.h&gt; #include &lt;sys/mman.h&gt; #include &lt;sys/param.h&gt; #include &lt;sys/vm.h&gt; #include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt;  int prefixsegmap(dev_t dev, off_t off, struct as *asp, caddr_t *addrp, off_t len,     unsigned int prot, unsigned int maxprot, unsigned int flags, cred_t *cred_p);</pre>																																
<b>INTERFACE LEVEL</b>	Architecture independent level 2 (DKI only).																																
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<b>DESCRIPTION</b>	The <b>segmap()</b> entry point is an optional routine for character drivers that support memory mapping. The <b>mmap(2)</b> system call, when applied to a character special file, allows device memory to be mapped into user space for direct access by the user application.																																

Typically, a character driver that needs to support the **mmap(2)** system call supplies either an **devmap(9E)** entry point, or both an **devmap(9E)** and a **segmap()** entry point routine (see the **devmap(9E)** reference page). If no **segmap()** entry point is provided for the driver, **devmap\_setup(9F)** is used as a default.

A driver for a memory-mapped device would provide a **segmap()** entry point if it:

- needs to maintain a separate context for each user mapping. See **devmap\_setup(9F)** for details.
- needs to assign device access attributes to the user mapping.

The responsibilities of a **segmap()** entry point are:

- Verify that the range, defined by *offset* and *len*, to be mapped is valid for the device. Typically, this task is performed by calling the **devmap(9E)** entry point. Note that if you are using **ddi\_devmap\_segmap(9E)** or **devmap\_setup(9E)** to set up the mapping, it will call your **devmap(9E)** entry point for you to validate the range to be mapped.
- Assign device access attributes to the mapping. See **ddi\_devmap\_segmap(9F)**, and **ddi\_device\_acc\_attr(9S)** for details.
- Set up device contexts for the user mapping if your device requires context switching. See **devmap\_setup(9F)** for details.
- Perform the mapping with **ddi\_devmap\_segmap(9E)**, or **devmap\_setup(9E)** and return the status if it fails.

#### RETURN VALUES

The **segmap()** routine should return **0** if the driver is successful in performing the memory map of its device address space into the specified address space.

The **segmap()** must return an error number on failure. For example, valid error numbers would be **ENXIO** if the offset/length pair specified exceeds the limits of the device memory, or **EINVAL** if the driver detects an invalid type of mapping attempted.

If one of the mapping routines **ddi\_devmap\_segmap()**, or **devmap\_setup()** fails, you must return the error number returned by the respective routine.

#### SEE ALSO

**mmap(2)**, **devmap(9E)**, **devmap\_setup(9F)**, **ddi\_devmap\_segmap(9F)**, **ddi\_device\_acc\_attr(9S)**

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<b>NAME</b>	srv – service queued messages
<b>SYNOPSIS</b>	<pre>#include &lt;sys/types.h&gt; #include &lt;sys/stream.h&gt; #include &lt;sys/stropts.h&gt; #include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt;  int prefixrsrv(queue_t *q); /* read side */ int prefixwsrv(queue_t *q); /* write side */</pre>
<b>INTERFACE LEVEL ARGUMENTS</b>	<p>Architecture independent level 1 (DDI/DKI). This entry point is <b>required</b> for STREAMS.</p> <p><i>q</i>        Pointer to the <b>queue(9S)</b> structure.</p>
<b>DESCRIPTION</b>	<p>The optional service (<b>srv()</b>) routine may be included in a STREAMS module or driver for many possible reasons, including:</p> <ul style="list-style-type: none"> <li>• to provide greater control over the flow of messages in a stream</li> <li>• to make it possible to defer the processing of some messages to avoid depleting system resources</li> <li>• to combine small messages into larger ones, or break large messages into smaller ones</li> <li>• to recover from resource allocation failure. A module's or driver's <b>put(9E)</b> routine can test for the availability of a resource, and if it is not available, enqueue the message for later processing by the <b>srv</b> routine.</li> </ul> <p>A message is first passed to a module's or driver's <b>put(9E)</b> routine, which may or may not do some processing. It must then either:</p> <ul style="list-style-type: none"> <li>• Pass the message to the next stream component with <b>putnext(9F)</b>.</li> <li>• If a <b>srv</b> routine has been included, it may call <b>putq(9F)</b> to place the message on the queue.</li> </ul> <p>Once a message has been enqueued, the STREAMS scheduler controls the service routine's invocation. The scheduler calls the service routines in FIFO order. The scheduler cannot guarantee a maximum delay <b>srv</b> routine to be called except that it will happen before any user level process are run.</p> <p>Every stream component (stream head, module or driver) has limit values it uses to implement flow control. Each component should check the tunable high and low water marks to stop and restart the flow of message processing. Flow control limits apply only between two adjacent components with <b>srv</b> routines.</p> <p>STREAMS messages can be defined to have up to 256 different priorities to support requirements for multiple bands of data flow. At a minimum, a stream must distinguish between normal (priority zero) messages and high priority messages (such as <b>M_IOCACK</b>). High priority messages are always placed at the head of the <b>srv</b> routine's</p>

queue, after any other enqueued high priority messages. Next are messages from all included priority bands, which are enqueued in decreasing order of priority. Each priority band has its own flow control limits. If a flow controlled band is stopped, all lower priority bands are also stopped.

Once the STREAMS scheduler calls a **srv** routine, it must process all messages on its queue. The following steps are general guidelines for processing messages. Keep in mind that many of the details of how a **srv** routine should be written depend of the implementation, the direction of flow (upstream or downstream), and whether it is for a module or a driver.

1. Use **getq(9F)** to get the next enqueued message.
2. If the message is high priority, process (if appropriate) and pass to the next stream component with **putnext(9F)**.
3. If it is not a high priority message (and therefore subject to flow control), attempt to send it to the next stream component with a **srv** routine. Use **bcanputnext(9F)** to determine if this can be done.
4. If the message cannot be passed, put it back on the queue with **putbq(9F)**. If it can be passed, process (if appropriate) and pass with **putnext()**.

**RETURN VALUES**

Ignored.

**SEE ALSO**

**put(9E)**, **bcanput(9F)**, **bcanputnext(9F)**, **canput(9F)**, **canputnext(9F)**, **getq(9F)**, **nulldev(9F)**, **putbq(9F)**, **putnext(9F)**, **putq(9F)**, **qinit(9S)**, **queue(9S)**

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**WARNINGS**

Each stream module must specify a read and a write service (**srv()**) routine. If a service routine is not needed (because the **put()** routine processes all messages), a **NULL** pointer should be placed in module's **qinit(9S)** structure. Do not use **nulldev(9F)** instead of the **NULL** pointer. Use of **nulldev(9F)** for a **srv()** routine may result in flow control errors.



<b>NAME</b>	strategy – perform block I/O
<b>SYNOPSIS</b>	<pre>#include &lt;sys/types.h&gt; #include &lt;sys/buf.h&gt; #include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt;  int prefixstrategy(struct buf *bp);</pre>
<b>INTERFACE LEVEL</b>	Architecture independent level 1 (DDI/DKI). This entry point is <b>required</b> for block devices.
<b>ARGUMENTS</b>	<i>bp</i> Pointer to the <b>buf</b> (9S) structure.
<b>DESCRIPTION</b>	The <b>strategy()</b> routine is called indirectly (through <b>cb_ops</b> (9S)) by the kernel to read and write blocks of data on the block device. <b>strategy()</b> may also be called directly or indirectly to support the raw character interface of a block device ( <b>read</b> (9E), <b>write</b> (9E) and <b>ioctl</b> (9E)). The <b>strategy()</b> routine's responsibility is to set up and initiate the transfer.
<b>RETURN VALUES</b>	The <b>strategy()</b> routine should always return 0. On an error condition, it should OR the <b>b_flags</b> member of the <b>buf</b> (9S) structure with <b>B_ERROR</b> , set the <b>b_error</b> member to the appropriate error value, and call <b>biodone</b> (9F). Note that a partial transfer is <i>not</i> considered to be an error.
<b>SEE ALSO</b>	<b>ioctl</b> (9E), <b>read</b> (9E), <b>write</b> (9E), <b>biodone</b> (9F), <b>buf</b> (9S), <b>cb_ops</b> (9S) <i>Writing Device Drivers</i>

<b>NAME</b>	tran_abort – abort a SCSI command
<b>SYNOPSIS</b>	<b>#include</b> <sys/scsi/scsi.h> <b>int</b> <i>prefix</i> tran_abort(struct scsi_address *ap, struct scsi_pkt *pkt);
<b>INTERFACE LEVEL ARGUMENTS</b>	Solaris architecture specific (Solaris DDI). <i>ap</i> Pointer to a <b>scsi_address</b> (9S) structure. <i>pkt</i> Pointer to a <b>scsi_pkt</b> (9S) structure.
<b>DESCRIPTION</b>	The <b>tran_abort()</b> vector in the <b>scsi_hba_tran</b> (9S) structure must be initialized during the HBA driver's <b>attach</b> (9E) to point to an HBA entry point to be called when a target driver calls <b>scsi_abort</b> (9F). <b>tran_abort()</b> should attempt to abort the command <i>pkt</i> that has been transported to the HBA. If <i>pkt</i> is NULL, the HBA driver should attempt to abort all outstanding packets for the target/logical unit addressed by <i>ap</i> . Depending on the state of a particular command in the transport layer, the HBA driver may not be able to abort the command. While the abort is taking place, packets issued to the transported layer may or may not be aborted. For each packet successfully aborted, <b>tran_abort()</b> must set the <b>pkt_reason</b> to <b>CMD_ABORTED</b> , and <b>pkt_statistics</b> must be OR'ed with <b>STAT_ABORTED</b> .
<b>RETURN VALUES</b>	<b>tran_abort()</b> must return: <b>1</b> on success or partial success. <b>0</b> on failure.
<b>SEE ALSO</b>	<b>attach</b> (9E), <b>scsi_abort</b> (9F), <b>scsi_hba_attach</b> (9F), <b>scsi_address</b> (9S), <b>scsi_hba_tran</b> (9S), <b>scsi_pkt</b> (9S) <i>Writing Device Drivers</i>
<b>NOTES</b>	If <b>pkt_reason</b> already indicates that an earlier error had occurred, <b>tran_abort()</b> should not overwrite <b>pkt_reason</b> with <b>CMD_ABORTED</b> .

<b>NAME</b>	tran_dmafree – SCSI HBA DMA deallocation entry point
<b>SYNOPSIS</b>	<b>#include</b> <sys/scsi/scsi.h> <b>void prefix</b> tran_dmafree(struct scsi_address *ap, struct scsi_pkt *pkt);
<b>INTERFACE LEVEL</b>	Solaris architecture specific (Solaris DDI).
<b>ARGUMENTS</b>	<i>ap</i> A pointer to a <b>scsi_address</b> (9S) structure. <i>pkt</i> A pointer to a <b>scsi_pkt</b> (9S) structure.
<b>DESCRIPTION</b>	The <b>tran_dmafree()</b> vector in the <b>scsi_hba_tran</b> (9S) structure must be initialized during the HBA driver's <b>attach</b> (9E) to point to an HBA entry point to be called when a target driver calls <b>scsi_dmafree</b> (9F). <b>tran_dmafree()</b> must deallocate any DMA resources previously allocated to this <i>pkt</i> in a call to <b>tran_init_pkt</b> (9E). <b>tran_dmafree()</b> should not free the structure pointed to by <i>pkt</i> itself. Since <b>tran_destroy_pkt</b> (9E) must also free DMA resources, it is important that the HBA driver keeps accurate note of whether <b>scsi_pkt</b> (9S) structures have DMA resources allocated.
<b>SEE ALSO</b>	<b>attach</b> (9E), <b>tran_destroy_pkt</b> (9E), <b>tran_init_pkt</b> (9E), <b>scsi_dmafree</b> (9F), <b>scsi_dmaget</b> (9F), <b>scsi_hba_attach</b> (9F), <b>scsi_init_pkt</b> (9F), <b>scsi_address</b> (9S), <b>scsi_hba_tran</b> (9S), <b>scsi_pkt</b> (9S) <i>Writing Device Drivers</i>
<b>NOTES</b>	A target driver may call <b>tran_dmafree()</b> on packets for which no DMA resources were allocated.

<b>NAME</b>	tran_getcap, tran_setcap – get/set SCSI transport capability
<b>SYNOPSIS</b>	<pre>#include &lt;sys/scsi/scsi.h&gt; int prefixtran_getcap(struct scsi_address *ap, char *cap, int whom); int prefixtran_setcap(struct scsi_address *ap, char *cap, int value, int whom);</pre>
<b>INTERFACE LEVEL</b>	Solaris architecture specific (Solaris DDI).
<b>ARGUMENTS</b>	<p><i>ap</i> Pointer to the <b>scsi_address</b>(9S) structure.</p> <p><i>cap</i> Pointer to the string capability identifier.</p> <p><i>value</i> Defines the new state of the capability.</p> <p><i>whom</i> Specifies whether all targets or only the specified target is affected.</p>
<b>DESCRIPTION</b>	<p>The <b>tran_getcap()</b> and <b>tran_setcap()</b> vectors in the <b>scsi_hba_tran</b>(9S) structure must be initialized during the HBA driver's <b>attach</b>(9E) to point to HBA entry points to be called when a target driver calls <b>scsi_ifgetcap</b>(9F) and <b>scsi_ifsetcap</b>(9F).</p> <p><b>tran_getcap()</b> is called to get the current value of a capability specific to features provided by the HBA hardware or driver. The name of the capability <i>cap</i> is the NULL terminated capability string.</p> <p>If <i>whom</i> is non-zero, the request is for the current value of the capability defined for the target specified by the <b>scsi_address</b>(9S) structure pointed to by <i>ap</i>; if <i>whom</i> is <b>0</b>, all targets are affected; else, the target specified by the <b>scsi_address</b> structure pointed to by <i>ap</i> is affected.</p> <p><b>tran_setcap()</b> is called to set the value of the capability <i>cap</i> to the value of <i>value</i>. If <i>whom</i> is non-zero, the capability should be set for the target specified by the <b>scsi_address</b>(9S) structure pointed to by <i>ap</i>; if <i>whom</i> is <b>0</b>, all targets are affected; else, the target specified by the <b>scsi_address</b> structure pointed to by <i>ap</i> is affected. It is recommended that HBA drivers do not support setting capabilities for all targets (that is, <i>whom</i> is <b>0</b>).</p> <p>A device may support only a subset of the defined capabilities.</p> <p>Refer to <b>scsi_ifgetcap</b>(9F) for the list of defined capabilities.</p> <p>HBA drivers should use <b>scsi_hba_lookup_capstr</b>(9F) to match <i>cap</i> against the canonical capability strings.</p>
<b>RETURN VALUES</b>	<p><b>tran_setcap()</b> must return <b>1</b> if the capability was successfully set to the new value, <b>0</b> if the HBA driver does not support changing the capability, and <b>-1</b> if the capability was not defined.</p> <p><b>tran_getcap()</b> must return the current value of a capability or <b>-1</b> if the capability was not defined.</p>

**SEE ALSO**

**attach(9E), scsi\_hba\_attach(9F), scsi\_hba\_lookup\_capstr(9F), scsi\_ifgetcap(9F),  
scsi\_address(9S), scsi\_hba\_tran(9S)**

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<b>NAME</b>	tran_init_pkt, tran_destroy_pkt – SCSI HBA packet preparation and deallocation
<b>SYNOPSIS</b>	<pre>#include &lt;sys/scsi/scsi.h&gt; <b>struct scsi_pkt</b> *prefixtran_init_pkt(<b>struct scsi_address</b> *ap, <b>struct scsi_pkt</b> *pkt, <b>struct buf</b> *bp, <b>int cmdlen</b>, <b>int statuslen</b>, <b>int tgtlen</b>, <b>int flags</b>, <b>int</b> (*callback)(<b>caddr_t</b>, <b>caddr_t arg</b>); <b>void</b> prefixtran_destroy_pkt(<b>struct scsi_address</b> *ap, <b>struct scsi_pkt</b> *pkt);</pre>
<b>INTERFACE LEVEL</b>	Solaris architecture specific (Solaris DDI).
<b>ARGUMENTS</b>	<p><i>ap</i> Pointer to a <b>scsi_address</b>(9S) structure.</p> <p><i>pkt</i> Pointer to a <b>scsi_pkt</b>(9S) structure allocated in an earlier call, or <b>NULL</b>.</p> <p><i>bp</i> Pointer to a <b>buf</b>(9S) structure if DMA resources are to be allocated for the <i>pkt</i>, or <b>NULL</b>.</p> <p><i>cmdlen</i> The required length for the SCSI command descriptor block (CDB) in bytes.</p> <p><i>statuslen</i> The required length for the SCSI status completion block (SCB) in bytes.</p> <p><i>tgtlen</i> The length of the packet private area within the <b>scsi_pkt</b> to be allocated on behalf of the SCSI target driver.</p> <p><i>flags</i> Flags for creating the packet.</p> <p><i>callback</i> Pointer to either <b>NULL_FUNC</b> or <b>SLEEP_FUNC</b>.</p> <p><i>arg</i> Always <b>NULL</b>.</p>
<b>DESCRIPTION</b>	<p>The <b>tran_init_pkt()</b> and <b>tran_destroy_pkt()</b> vectors in the <b>scsi_hba_tran</b> structure must be initialized during the HBA driver's <b>attach</b>(9E) to point to HBA entry points to be called when a target driver calls <b>scsi_init_pkt</b>(9F) and <b>scsi_destroy_pkt</b>(9F).</p> <p><b>tran_init_pkt()</b> is the entry point into the HBA which is used to allocate and initialize a <b>scsi_pkt</b> structure on behalf of a SCSI target driver. If <i>pkt</i> is <b>NULL</b>, the HBA driver must use <b>scsi_hba_pkt_alloc</b>(9F) to allocate a new <b>scsi_pkt</b> structure.</p> <p>If <i>bp</i> is non-<b>NULL</b>, the HBA driver must allocate appropriate DMA resources for the <i>pkt</i>, for example, via <b>ddi_dma_buf_setup</b>(9F) or <b>ddi_dma_buf_bind_handle</b>(9F).</p> <p>If the <b>PKT_CONSISTENT</b> bit is set in <i>flags</i>, the buffer was allocated by <b>scsi_alloc_consistent_buf</b>(9F). For packets marked with <b>PKT_CONSISTENT</b>, the HBA driver must synchronize any cached data transfers before calling the target driver's command completion callback.</p> <p>If the <b>PKT_DMA_PARTIAL</b> bit is set in <i>flags</i>, the HBA driver should set up partial data transfers, such as setting the <b>DDI_DMA_PARTIAL</b> bit in the <i>flags</i> argument if interfaces such as <b>ddi_dma_buf_setup</b>(9F) or <b>ddi_dma_buf_bind_handle</b>(9F) are used.</p>

If only partial DMA resources are available, **tran\_init\_pkt()** must return in the **pkt\_resid** field of *pkt* the number of bytes of DMA resources not allocated.

If both *pkt* and *bp* are non-NULL, if the **PKT\_DMA\_PARTIAL** bit is set in *flags*, and if DMA resources have already been allocated for the *pkt* with a previous call to **tran\_init\_pkt()** that returned a non-zero **pkt\_resid** field, this request is to move the DMA resources for the subsequent piece of the transfer.

The contents of **scsi\_address(9S)** pointed to by *ap* are copied into the **pkt\_address** field of the **scsi\_pkt(9S)** by **scsi\_hba\_pkt\_alloc(9F)**.

*tgrlen* is the length of the packet private area in the **scsi\_pkt** structure to be allocated on behalf of the SCSI target driver.

*statuslen* is the required length for the SCSI status completion block. If the requested status length is greater than or equal to **sizeof(struct scsi\_arq\_status)** and the **auto\_rqsense** capability has been set, automatic request sense is enabled for this packet. If the status length is less than **sizeof(struct scsi\_arq\_status)**, automatic request sense must be disabled for this *pkt*.

*cmdlen* is the required length for the SCSI command descriptor block.

**Note:** *tgrlen*, *statuslen*, and *cmdlen* are used only when the HBA driver allocates the **scsi\_pkt(9S)**, in other words, when *pkt* is NULL.

*callback* indicates what the allocator routines should do when resources are not available:

- NULL\_FUNC**     Do not wait for resources. Return a NULL pointer.
- SLEEP\_FUNC**    Wait indefinitely for resources.

#### tran\_destroy\_pkt()

**tran\_destroy\_pkt()** is the entry point into the HBA that must free all of the resources that were allocated to the **scsi\_pkt(9S)** structure during **tran\_init\_pkt()**.

#### RETURN VALUES

**tran\_init\_pkt()** must return a pointer to a **scsi\_pkt(9S)** structure on success, or NULL on failure.

If *pkt* is NULL on entry, and **tran\_init\_pkt()** allocated a packet via **scsi\_hba\_pkt\_alloc(9F)** but was unable to allocate DMA resources, **tran\_init\_pkt()** must free the packet via **scsi\_hba\_pkt\_free(9F)** before returning NULL.

#### SEE ALSO

**attach(9E)**, **tran\_sync\_pkt(9E)**, **ddi\_dma\_buf\_bind\_handle(9F)**, **ddi\_dma\_buf\_setup(9F)**, **scsi\_alloc\_consistent\_buf(9F)**, **scsi\_destroy\_pkt(9F)**, **scsi\_hba\_attach(9F)**, **scsi\_hba\_pkt\_alloc(9F)**, **scsi\_hba\_pkt\_free(9F)**, **scsi\_init\_pkt(9F)**, **buf(9S)**, **scsi\_address(9S)**, **scsi\_hba\_tran(9S)**, **scsi\_pkt(9S)**

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#### NOTES

If a DMA allocation request fails with **DDI\_DMA\_NOMAPPING**, the **B\_ERROR** flag should be set in *bp*, and the **b\_error** field should be set to **EFAULT**.

If a DMA allocation request fails with **DDI\_DMA\_TOOBIG**, the **B\_ERROR** flag should be set in *bp*, and the **b\_error** field should be set to **EINVAL**.

<b>NAME</b>	tran_reset – reset a SCSI bus or target
<b>SYNOPSIS</b>	<b>#include</b> <sys/scsi/scsi.h> <b>int</b> <i>prefix</i> tran_reset(struct scsi_address *ap, int level);
<b>INTERFACE LEVEL</b>	Solaris architecture specific (Solaris DDI).
<b>ARGUMENTS</b>	<i>ap</i> Pointer to the scsi_address(9S) structure. <i>level</i> The level of reset required.
<b>DESCRIPTION</b>	The tran_reset() vector in the scsi_hba_tran(9S) structure must be initialized during the HBA driver's attach(9E) to point to an HBA entry point to be called when a target driver calls scsi_reset(9F). <b>tran_reset()</b> must reset the SCSI bus or a SCSI target as specified by <i>level</i> . <i>level</i> must be one of the following: <b>RESET_ALL</b> reset the SCSI bus. <b>RESET_TARGET</b> reset the target specified by <i>ap</i> . <b>tran_reset</b> should set the <b>pkt_reason</b> field of all outstanding packets in the transport layer associated with each target that was successfully reset to <b>CMD_RESET</b> and the <b>pkt_statistics</b> field must be OR'ed with either <b>STAT_BUS_RESET</b> or <b>STAT_DEV_RESET</b> . The HBA driver should use a SCSI Bus Device Reset Message to reset a target device. Packets that are in the transport layer but not yet active on the bus should be returned with <b>pkt_reason</b> set to <b>CMD_RESET</b> , and <b>pkt_statistics</b> OR'ed with <b>STAT_ABORTED</b> .
<b>RETURN VALUES</b>	<b>tran_reset()</b> should return: <b>1</b> on success. <b>0</b> on failure.
<b>SEE ALSO</b>	<b>attach(9E)</b> , <b>ddi_dma_buf_setup(9F)</b> , <b>scsi_hba_attach(9F)</b> , <b>scsi_reset(9F)</b> , <b>scsi_address(9S)</b> , <b>scsi_hba_tran(9S)</b> <i>Writing Device Drivers</i>
<b>NOTES</b>	If <b>pkt_reason</b> already indicates that an earlier error had occurred for a particular <i>pkt</i> , <b>tran_reset()</b> should not overwrite <b>pkt_reason</b> with <b>CMD_RESET</b> .



<b>NAME</b>	tran_reset_notify – request to notify SCSI target of bus reset								
<b>SYNOPSIS</b>	<pre>#include &lt;sys/scsi/scsi.h&gt; int prefixtran_reset_notify(struct scsi_address *ap, int flag, void (*callback)(caddr_t),     caddr_t arg);</pre>								
<b>INTERFACE LEVEL</b>	Solaris architecture specific (Solaris DDI).								
<b>ARGUMENTS</b>	<table border="0"> <tr> <td style="padding-right: 20px;"><i>ap</i></td> <td>Pointer to the <b>scsi_address</b>(9S) structure.</td> </tr> <tr> <td><i>flag</i></td> <td>A flag indicating registration or cancellation of a notification request.</td> </tr> <tr> <td><i>callback</i></td> <td>A pointer to the target driver's reset notification function.</td> </tr> <tr> <td><i>arg</i></td> <td>The callback function argument.</td> </tr> </table>	<i>ap</i>	Pointer to the <b>scsi_address</b> (9S) structure.	<i>flag</i>	A flag indicating registration or cancellation of a notification request.	<i>callback</i>	A pointer to the target driver's reset notification function.	<i>arg</i>	The callback function argument.
<i>ap</i>	Pointer to the <b>scsi_address</b> (9S) structure.								
<i>flag</i>	A flag indicating registration or cancellation of a notification request.								
<i>callback</i>	A pointer to the target driver's reset notification function.								
<i>arg</i>	The callback function argument.								
<b>DESCRIPTION</b>	<p>The <b>tran_reset_notify()</b> entry point is called when a target driver requests notification of a bus reset.</p> <p>The <b>tran_reset_notify()</b> vector in the <b>scsi_hba_tran</b>(9S) structure may be initialized in the HBA driver's <b>attach</b>(9E) routine to point to the HBA entry point to be called when a target driver calls <b>scsi_reset_notify</b>(9F).</p> <p>The argument <i>flag</i> is used to register or cancel the notification. The supported values for <i>flag</i> are as follows:</p> <table border="0" style="margin-left: 40px;"> <tr> <td style="padding-right: 20px;"><b>SCSI_RESET_NOTIFY</b></td> <td>Register <i>callback</i> as the reset notification function for the target.</td> </tr> <tr> <td><b>SCSI_RESET_CANCEL</b></td> <td>Cancel the reset notification request for the target.</td> </tr> </table> <p>The HBA driver maintains a list of reset notification requests registered by the target drivers. When a bus reset occurs, the HBA driver notifies registered target drivers by calling the callback routine, <i>callback</i>, with the argument, <i>arg</i>, for each registered target.</p>	<b>SCSI_RESET_NOTIFY</b>	Register <i>callback</i> as the reset notification function for the target.	<b>SCSI_RESET_CANCEL</b>	Cancel the reset notification request for the target.				
<b>SCSI_RESET_NOTIFY</b>	Register <i>callback</i> as the reset notification function for the target.								
<b>SCSI_RESET_CANCEL</b>	Cancel the reset notification request for the target.								
<b>RETURN VALUES</b>	<p>For <b>SCSI_RESET_NOTIFY</b> requests, <b>tran_reset_notify()</b> must return <b>DDI_SUCCESS</b> if the notification request has been accepted, and <b>DDI_FAILURE</b> otherwise.</p> <p>For <b>SCSI_RESET_CANCEL</b> requests, <b>tran_reset_notify()</b> must return <b>DDI_SUCCESS</b> if the notification request has been canceled, and <b>DDI_FAILURE</b> otherwise.</p>								
<b>SEE ALSO</b>	<b>attach</b> (9E), <b>scsi_ifgetcap</b> (9F), <b>scsi_reset_notify</b> (9F), <b>scsi_address</b> (9S), <b>scsi_hba_tran</b> (9S) <i>Writing Device Drivers</i>								

<b>NAME</b>	tran_start – request to transport a SCSI command
<b>SYNOPSIS</b>	<pre>#include &lt;sys/scsi/scsi.h&gt; int prefixtran_start(struct scsi_address *ap, struct scsi_pkt *pkt);</pre>
<b>INTERFACE LEVEL</b>	Solaris architecture specific (Solaris DDI).
<b>ARGUMENTS</b>	<p><i>pkt</i>            Pointer to the <b>scsi_pkt</b>(9S) structure that is about to be transferred.</p> <p><i>ap</i>             Pointer to a <b>scsi_address</b>(9S) structure.</p>
<b>DESCRIPTION</b>	<p>The <b>tran_start()</b> vector in the <b>scsi_hba_tran</b>(9S) structure must be initialized during the HBA driver's <b>attach</b>(9E) to point to an HBA entry point to be called when a target driver calls <b>scsi_transport</b>(9F).</p> <p><b>tran_start()</b> must perform the necessary operations on the HBA hardware to transport the SCSI command in the <i>pkt</i> structure to the target/logical unit device specified in the <i>ap</i> structure.</p> <p>If the flag <b>FLAG_NOINTR</b> is set in <b>pkt_flags</b> in <i>pkt</i>, <b>tran_start()</b> should not return until the command has been completed. The command completion callback <b>pkt_comp</b> in <i>pkt</i> must not be called for commands with <b>FLAG_NOINTR</b> set, since the return is made directly to the function invoking <b>scsi_transport</b>(9F).</p> <p>When the flag <b>FLAG_NOINTR</b> is not set, <b>tran_start()</b> must queue the command for execution on the hardware and return immediately. The member <b>pkt_comp</b> in <i>pkt</i> indicates a callback routine to be called upon command completion.</p> <p>Refer to <b>scsi_pkt</b>(9S) for other bits in <b>pkt_flags</b> for which the HBA driver may need to adjust how the command is managed.</p> <p>If the <b>auto_rqsense</b> capability has been set, and the status length allocated in <b>tran_init_pkt</b>(9E) is greater than or equal to <b>sizeof(struct scsi_arq_status)</b>, automatic request sense is enabled for this <i>pkt</i>. If the command terminates with a Check Condition, the HBA driver must arrange for a Request Sense command to be transported to that target/logical unit, and the members of the <b>scsi_arq_status</b> structure pointed to by <b>pkt_scbp</b> updated with the results of this Request Sense command before the HBA driver completes the command pointed by <i>pkt</i>.</p> <p>The member <b>pkt_time</b> in <i>pkt</i> is the maximum number of seconds in which the command should complete. A <b>pkt_time</b> of 0 means no timeout should be performed.</p> <p>For a command which has timed out, the HBA driver must perform some recovery operation to clear the command in the target, typically an Abort message, or a Device or Bus Reset. The <b>pkt_reason</b> member of the timed out <i>pkt</i> should be set to <b>CMD_TIMEOUT</b>, and <b>pkt_statistics</b> OR'ed with <b>STAT_TIMEOUT</b>. If the HBA driver can successfully recover from the timeout, <b>pkt_statistics</b> must also be OR'ed with one of <b>STAT_ABORTED</b>, <b>STAT_BUS_RESET</b>, or <b>STAT_DEV_RESET</b>, as appropriate. This informs the target driver that timeout recovery has already been successfully accomplished for the timed out</p>

command. The **pkt\_comp** completion callback, if not NULL, must also be called at the conclusion of the timeout recovery.

If the timeout recovery was accomplished with an Abort Tag message, only the timed out packet is affected, and the packet must be returned with **pkt\_statistics** OR'ed with **STAT\_ABORTED** and **STAT\_TIMEOUT**.

If the timeout recovery was accomplished with an Abort message, all commands active in that target are affected. All corresponding packets must be returned with **pkt\_reason**, **CMD\_TIMEOUT**, and **pkt\_statistics** OR'ed with **STAT\_TIMEOUT** and **STAT\_ABORTED**.

If the timeout recovery was accomplished with a Device Reset, all packets corresponding to commands active in the target must be returned in the transport layer for this target. Packets corresponding to commands active in the target must be returned with **pkt\_reason** set to **CMD\_TIMEOUT**, and **pkt\_statistics** OR'ed with **STAT\_DEV\_RESET** and **STAT\_TIMEOUT**. Currently inactive packets queued for the device should be returned with **pkt\_reason** set to **CMD\_RESET** and **pkt\_statistics** OR'ed with **STAT\_ABORTED**.

If the timeout recovery was accomplished with a Bus Reset, all packets corresponding to commands active in the target must be returned in the transport layer. Packets corresponding to commands active in the target must be returned with **pkt\_reason** set to **CMD\_TIMEOUT** and **pkt\_statistics** OR'ed with **STAT\_TIMEOUT** and **STAT\_BUS\_RESET**. All queued packets for other targets on this bus must be returned with **pkt\_reason** set to **CMD\_RESET** and **pkt\_statistics** OR'ed with **STAT\_ABORTED**.

Note that, after either a Device Reset or a Bus Reset, the HBA driver must enforce a reset delay time of '**scsi-reset-delay**' milliseconds, during which time no commands should be sent to that device, or any device on the bus, respectively.

**tran\_start()** should initialize the following members in *pkt* to **0**. Upon command completion, the HBA driver should ensure that the values in these members are updated to accurately reflect the states through which the command transitioned while in the transport layer.

<b>pkt_resid</b>	For commands with data transfer, this member must be updated to indicate the residual of the data transferred.
<b>pkt_reason</b>	The reason for the command completion. This field should be set to <b>CMD_CMPLT</b> at the beginning of <b>tran_start()</b> , then updated if the command ever transitions to an abnormal termination state. To avoid losing information, do not set <b>pkt_reason</b> to any other error state unless it still has its original <b>CMD_CMPLT</b> value.
<b>pkt_statistics</b>	Bit field of transport-related statistics
<b>pkt_state</b>	Bit field with the major states through which a SCSI command can transition.

**Note:** The members listed above, and **pkt\_hba\_private** member, are the only fields in the **scsi\_pkt(9S)** structure which may be modified by the transport layer.

**RETURN VALUES**

**tran\_start()** must return:

**TRAN\_ACCEPT**

The packet was accepted by the transport layer.

**TRAN\_BUSY**

The packet could not be accepted because there was already a packet in progress for this target/logical unit, the HBA queue was full, or the target device queue was full.

**TRAN\_BADPKT**

The DMA count in the packet exceeded the DMA engine's maximum DMA size, or the packet could not be accepted for other reasons.

**TRAN\_FATAL\_ERROR**

A fatal error has occurred in the HBA.

**SEE ALSO**

**attach(9E)**, **tran\_init\_pkt(9E)**, **scsi\_hba\_attach(9F)**, **scsi\_transport(9F)**, **scsi\_address(9S)**, **scsi\_arq\_status(9S)**, **scsi\_hba\_tran(9S)**, **scsi\_pkt(9S)**

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<b>NAME</b>	tran_sync_pkt – SCSI HBA memory synchronization entry point
<b>SYNOPSIS</b>	<b>#include</b> <sys/scsi/scsi.h> <b>void prefix</b> tran_sync_pkt(struct scsi_address *ap, struct scsi_pkt *pkt);
<b>INTERFACE LEVEL</b>	Solaris architecture specific (Solaris DDI).
<b>ARGUMENTS</b>	<i>ap</i> A pointer to a <b>scsi_address</b> (9S) structure. <i>pkt</i> A pointer to a <b>scsi_pkt</b> (9S) structure.
<b>DESCRIPTION</b>	The <b>tran_sync_pkt()</b> vector in the <b>scsi_hba_tran</b> (9S) structure must be initialized during the HBA driver's <b>attach</b> (9E) to point to an HBA driver entry point to be called when a target driver calls <b>scsi_sync_pkt</b> (9F). <b>tran_sync_pkt()</b> must synchronize a CPU's or device's view of the data associated with the <i>pkt</i> , typically by calling <b>ddi_dma_sync</b> (9F). The operation may also involve HBA hardware-specific details, such as flushing I/O caches, or stalling until hardware buffers have been drained.
<b>SEE ALSO</b>	<b>attach</b> (9E), <b>tran_init_pkt</b> (9E), <b>ddi_dma_sync</b> (9F), <b>scsi_hba_attach</b> (9F), <b>scsi_init_pkt</b> (9F), <b>scsi_sync_pkt</b> (9F), <b>scsi_address</b> (9S), <b>scsi_hba_tran</b> (9S), <b>scsi_pkt</b> (9S) <i>Writing Device Drivers</i>
<b>NOTES</b>	A target driver may call <b>tran_sync_pkt()</b> on packets for which no DMA resources were allocated.

<b>NAME</b>	tran_tgt_free – request to free HBA resources allocated on behalf of a target								
<b>SYNOPSIS</b>	<pre>#include &lt;sys/scsi/scsi.h&gt; void prefixtran_tgt_free(dev_info_t *hba_dip, dev_info_t *tgt_dip,     scsi_hba_tran_t *hba_tran, struct scsi_device *sd);</pre>								
<b>INTERFACE LEVEL</b>	Solaris architecture specific (Solaris DDI).								
<b>ARGUMENTS</b>	<table border="0"> <tr> <td style="padding-right: 1em;"><i>hba_dip</i></td> <td>Pointer to a <b>dev_info_t</b> structure, referring to the HBA device instance.</td> </tr> <tr> <td><i>tgt_dip</i></td> <td>Pointer to a <b>dev_info_t</b> structure, referring to the target device instance.</td> </tr> <tr> <td><i>hba_tran</i></td> <td>Pointer to a <b>scsi_hba_tran(9S)</b> structure, consisting of the HBA's transport vectors.</td> </tr> <tr> <td><i>sd</i></td> <td>Pointer to a <b>scsi_device(9S)</b> structure, describing the target.</td> </tr> </table>	<i>hba_dip</i>	Pointer to a <b>dev_info_t</b> structure, referring to the HBA device instance.	<i>tgt_dip</i>	Pointer to a <b>dev_info_t</b> structure, referring to the target device instance.	<i>hba_tran</i>	Pointer to a <b>scsi_hba_tran(9S)</b> structure, consisting of the HBA's transport vectors.	<i>sd</i>	Pointer to a <b>scsi_device(9S)</b> structure, describing the target.
<i>hba_dip</i>	Pointer to a <b>dev_info_t</b> structure, referring to the HBA device instance.								
<i>tgt_dip</i>	Pointer to a <b>dev_info_t</b> structure, referring to the target device instance.								
<i>hba_tran</i>	Pointer to a <b>scsi_hba_tran(9S)</b> structure, consisting of the HBA's transport vectors.								
<i>sd</i>	Pointer to a <b>scsi_device(9S)</b> structure, describing the target.								
<b>DESCRIPTION</b>	The <b>tran_tgt_free()</b> vector in the <b>scsi_hba_tran(9S)</b> structure may be initialized during the HBA driver's <b>attach(9E)</b> to point to an HBA driver function to be called by the system when an instance of a target device is being detached. The <b>tran_tgt_free()</b> vector, if not <b>NULL</b> , is called after the target device instance has returned successfully from its <b>detach(9E)</b> entry point, but before the <b>dev_info</b> node structure is removed from the system. The HBA driver should release any resources allocated during its <b>tran_tgt_init()</b> or <b>tran_tgt_probe()</b> initialization performed for this target device instance.								
<b>SEE ALSO</b>	<b>attach(9E)</b> , <b>detach(9E)</b> , <b>tran_tgt_init(9E)</b> , <b>tran_tgt_probe(9E)</b> , <b>scsi_device(9S)</b> , <b>scsi_hba_tran(9S)</b> <i>Writing Device Drivers</i>								

<b>NAME</b>	tran_tgt_init – request to initialize HBA resources on behalf of a particular target								
<b>SYNOPSIS</b>	<pre>#include &lt;sys/scsi/scsi.h&gt; void prefixtran_tgt_init(dev_info_t *hba_dip, dev_info_t *tgt_dip,     scsi_hba_tran_t *hba_tran, struct scsi_device *sd);</pre>								
<b>INTERFACE LEVEL</b>	Solaris architecture specific (Solaris DDI).								
<b>ARGUMENTS</b>	<table border="0"> <tr> <td style="padding-right: 20px;"><i>hba_dip</i></td> <td>Pointer to a <b>dev_info_t</b> structure, referring to the HBA device instance.</td> </tr> <tr> <td><i>tgt_dip</i></td> <td>Pointer to a <b>dev_info_t</b> structure, referring to the target device instance.</td> </tr> <tr> <td><i>hba_tran</i></td> <td>Pointer to a <b>scsi_hba_tran(9S)</b> structure, consisting of the HBA's transport vectors.</td> </tr> <tr> <td><i>sd</i></td> <td>Pointer to a <b>scsi_device(9S)</b> structure, describing the target.</td> </tr> </table>	<i>hba_dip</i>	Pointer to a <b>dev_info_t</b> structure, referring to the HBA device instance.	<i>tgt_dip</i>	Pointer to a <b>dev_info_t</b> structure, referring to the target device instance.	<i>hba_tran</i>	Pointer to a <b>scsi_hba_tran(9S)</b> structure, consisting of the HBA's transport vectors.	<i>sd</i>	Pointer to a <b>scsi_device(9S)</b> structure, describing the target.
<i>hba_dip</i>	Pointer to a <b>dev_info_t</b> structure, referring to the HBA device instance.								
<i>tgt_dip</i>	Pointer to a <b>dev_info_t</b> structure, referring to the target device instance.								
<i>hba_tran</i>	Pointer to a <b>scsi_hba_tran(9S)</b> structure, consisting of the HBA's transport vectors.								
<i>sd</i>	Pointer to a <b>scsi_device(9S)</b> structure, describing the target.								
<b>DESCRIPTION</b>	<p>The <b>tran_tgt_init()</b> vector in the <b>scsi_hba_tran(9S)</b> structure may be initialized during the HBA driver's <b>attach(9E)</b> to point to an HBA driver function to be called by the system when an instance of a target device is being created. The <b>tran_tgt_init()</b> vector, if not <b>NULL</b>, is called after the <b>dev_info</b> node structure is created for this target device instance, but before <b>probe(9E)</b> for this instance is called. Before receiving transport requests from the target driver instance, the HBA may perform any initialization required for this particular target during the call of the <b>tran_tgt_init()</b> vector.</p> <p>Note that <i>hba_tran</i> will point to a cloned copy of the <b>scsi_hba_tran_t</b> structure allocated by the HBA driver if the <b>SCSI_HBA_TRAN_CLONE</b> flag was specified in the call to <b>scsi_hba_attach(9F)</b>. In this case, the HBA driver may choose to initialize the <i>tran_tgt_private</i> field in the structure pointed to by <i>hba_tran</i>, to point to the data specific to the particular target device instance.</p>								
<b>RETURN VALUES</b>	<p><b>tran_tgt_init()</b> must return:</p> <table border="0"> <tr> <td style="padding-right: 20px;"><b>DDI_SUCCESS</b></td> <td>the HBA driver can support the addressed target, and was able to initialize per-target resources.</td> </tr> <tr> <td><b>DDI_FAILURE</b></td> <td>the HBA driver cannot support the addressed target, or was unable to initialize per-target resources. In this event, the initialization of this instance of the target device will not be continued, the target driver's <b>probe(9E)</b> will not be called, and the <i>tgt_dip</i> structure destroyed.</td> </tr> </table>	<b>DDI_SUCCESS</b>	the HBA driver can support the addressed target, and was able to initialize per-target resources.	<b>DDI_FAILURE</b>	the HBA driver cannot support the addressed target, or was unable to initialize per-target resources. In this event, the initialization of this instance of the target device will not be continued, the target driver's <b>probe(9E)</b> will not be called, and the <i>tgt_dip</i> structure destroyed.				
<b>DDI_SUCCESS</b>	the HBA driver can support the addressed target, and was able to initialize per-target resources.								
<b>DDI_FAILURE</b>	the HBA driver cannot support the addressed target, or was unable to initialize per-target resources. In this event, the initialization of this instance of the target device will not be continued, the target driver's <b>probe(9E)</b> will not be called, and the <i>tgt_dip</i> structure destroyed.								
<b>SEE ALSO</b>	<p><b>attach(9E)</b>, <b>probe(9E)</b>, <b>tran_tgt_free(9E)</b>, <b>tran_tgt_probe(9E)</b>, <b>scsi_hba_attach_setup(9F)</b>, <b>scsi_device(9S)</b>, <b>scsi_hba_tran(9S)</b></p> <p><i>Writing Device Drivers</i></p>								

<b>NAME</b>	tran_tgt_probe – request to probe SCSI bus for a particular target
<b>SYNOPSIS</b>	<b>#include</b> <sys/scsi/scsi.h> <b>int</b> <i>prefix</i> tran_tgt_probe(struct scsi_device *sd, int (*waitfunc)(void));
<b>INTERFACE LEVEL</b>	Solaris architecture specific (Solaris DDI).
<b>ARGUMENTS</b>	<i>sd</i> Pointer to a <b>scsi_device</b> (9S) structure. <i>waitfunc</i> Pointer to either <b>NULL_FUNC</b> or <b>SLEEP_FUNC</b> .
<b>DESCRIPTION</b>	The <b>tran_tgt_probe()</b> vector in the <b>scsi_hba_tran</b> (9S) structure may be initialized during the HBA driver's <b>attach</b> (9E) to point to a function to be called by <b>scsi_probe</b> (9F) when called by a target driver during <b>probe</b> (9E) and <b>attach</b> (9E) to probe for a particular SCSI target on the bus. In the absence of an HBA-specific <b>tran_tgt_probe()</b> function, the default <b>scsi_probe</b> (9F) behavior is supplied by the function <b>scsi_hba_probe</b> (9F). The possible choices the HBA driver may make are: <ul style="list-style-type: none"> <li>• Initialize the <b>tran_tgt_probe</b> vector to point to <b>scsi_hba_probe</b>(9F), which results in the same behavior.</li> <li>• Initialize the <b>tran_tgt_probe</b> vector to point to a private function in the HBA, which may call <b>scsi_hba_probe</b>(9F) before or after any necessary processing, as long as all the defined <b>scsi_probe</b>(9F) semantics are preserved.</li> </ul> <i>waitfunc</i> indicates what <b>tran_tgt_probe()</b> should do when resources are not available: <b>NULL_FUNC</b> Do not wait for resources. See <b>scsi_probe</b> (9F) for defined return values if no resources are available. <b>SLEEP_FUNC</b> Wait indefinitely for resources.
<b>SEE ALSO</b>	<b>attach</b> (9E), <b>probe</b> (9E), <b>tran_tgt_free</b> (9E), <b>tran_tgt_init</b> (9E), <b>scsi_hba_probe</b> (9F), <b>scsi_probe</b> (9F), <b>scsi_device</b> (9S), <b>scsi_hba_tran</b> (9S) <i>Writing Device Drivers</i>



<b>NAME</b>	write – write data to a device						
<b>SYNOPSIS</b>	<pre>#include &lt;sys/types.h&gt; #include &lt;sys/errno.h&gt; #include &lt;sys/open.h&gt; #include &lt;sys/cred.h&gt; #include &lt;sys/ddi.h&gt; #include &lt;sys/sunddi.h&gt;  int prefixwrite(dev_t dev, struct uio *uio_p, cred_t *cred_p);</pre>						
<b>INTERFACE LEVEL</b>	Architecture independent level 1 (DDI/DKI). This entry point is <i>optional</i> .						
<b>ARGUMENTS</b>	<table border="0"> <tr> <td style="padding-right: 20px;"><i>dev</i></td> <td>Device number.</td> </tr> <tr> <td><i>uio_p</i></td> <td>Pointer to the <b>uio</b>(9S) structure that describes where the data is to be stored in user space.</td> </tr> <tr> <td><i>cred_p</i></td> <td>Pointer to the user credential structure for the I/O transaction.</td> </tr> </table>	<i>dev</i>	Device number.	<i>uio_p</i>	Pointer to the <b>uio</b> (9S) structure that describes where the data is to be stored in user space.	<i>cred_p</i>	Pointer to the user credential structure for the I/O transaction.
<i>dev</i>	Device number.						
<i>uio_p</i>	Pointer to the <b>uio</b> (9S) structure that describes where the data is to be stored in user space.						
<i>cred_p</i>	Pointer to the user credential structure for the I/O transaction.						
<b>DESCRIPTION</b>	<p>Used for character or raw data I/O, the driver <b>write()</b> routine is called indirectly through <b>cb_ops</b>(9S) by the <b>write(2)</b> system call. The <b>write()</b> routine supervises the data transfer from user space to a device described by the <b>uio</b>(9S) structure.</p> <p>The <b>write()</b> routine should check the validity of the minor number component of <i>dev</i> and the user credentials pointed to by <i>cred_p</i> (if pertinent).</p>						
<b>RETURN VALUES</b>	The <b>write()</b> routine should return <b>0</b> for success, or the appropriate error number.						
<b>EXAMPLES</b>	<p>The following is an example of a <b>write()</b> routine using <b>physio</b>(9F) to perform writes to a seekable device:</p> <pre>static int xxwrite(dev_t dev, struct uio *uiop, cred_t *credp) {     int          instance;     xx_t         xx;      instance = getminor(dev);     xx = ddi_get_soft_state(xxstate, instance);     if (xx == NULL)         return (ENXIO);     return (physio(xxstrategy, NULL, dev, B_WRITE,                   xxmin, uiop)); }</pre>						

**SEE ALSO**

**read(2), write(2), read(9E), physio(9F), cb\_ops(9S), uio(9S)**

*Writing Device Drivers*

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