Writing FCode 3.x Programs
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Preface

This manual, Writing FCode 3.x Programs (802-5379), describes how to write, debug, and test FCode programs for SPARC-based systems and interface card devices. It replaces all previous manuals describing how to write FCode programs. IEEE Standard 1275-1994 is based on the Sun™ OpenBoot™ 2.x implementation. OpenBoot 3.x from Sun is compliant with IEEE Standard 1275.

Throughout this book, the reference to FCode PROM might refer to any type of ROM device (ROM, PROM, FlashPROM, etc.).

Who Should Use This Book

This manual is written for designers of SBus interface cards and other devices that use the FCode programming language. It is written for those designers who have some familiarity with SBus card design requirements and Forth programming. The sample code in this book is not guaranteed to be free of bugs.

The material in this manual is for developers of FCode applications for SBus peripherals on OpenBoot 3.x. With proper programming precautions, these applications should run on OpenBoot 3.x and earlier versions. By following the IEEE 1275 standard, the same FCode application can also run on non-SPARC systems which are IEEE 1275-compliant. The FCode language is defined by IEEE Standard 1275-1994 Standard for Boot Firmware.
This manual is written for designers who have read and understood the corresponding SBus specifications and SBus binding to IEEE Standard 1275-1994 1.2 (or later).

How This Book Is Organized

- **Chapter 1, “SBus Cards and FCode”**, introduces the basic relationships between FCode device drivers and the hardware that they control.
- **Chapter 2, “Elements of FCode Programming”**, introduces the basic elements of FCode, stack notation, and programming style.
- **Chapter 3, “Testing FCode Programs”**, describes the process of producing FCode programs, from source files to testing working programs.
- **Chapter 4, “Packages”**, describes the basic units of FCode program function.
- **Chapter 5, “Properties”**, describes properties, which define how an FCode device driver program recognizes the hardware that it controls.
- **Chapter 6, “Block and Byte Devices”** through **Chapter 10, “Serial Devices”** describe currently-defined device types, programming requirements, and give examples of device drivers for the various device types.
- **Chapter 11, “FCode Dictionary”**, describes currently-defined FCode words and their functions and use, with brief programming examples.
- **Appendix A, “FCode Reference”**, lists all currently-defined FCode words according to functional group, name, and byte value.
- **Appendix B, “FCode Memory Allocation”**, describes guidelines for memory allocation and de-allocation in FCode.
- **Appendix C, “Coding Style”**, contains an OpenBoot coding guideline.

Related Books

This manual does not cover all you need to know in order to write FCode drivers for SBus cards. The following books are also useful.

For information about SBus and OpenBoot:

- *IEEE Standard 1275-1994 Standard for Boot (Initialization Configuration) Firmware, Core Requirements and Practices*
• IEEE 1496-1993 Standard for Chip and Module Interconnect Bus: SBus 1-55937-353-9
• OpenBoot 2.x Command Reference (802-3241)
• OpenBoot 3.x Command Reference (802-3242)

Forth and Forth Programming

For further information about Forth and Forth programming:
• Mastering Forth, Anita Anderson and Martin Tracy, Brady Communications Co., Inc., 1989
• ANSI Forth X3J14
• Forth: A Text and Reference, Mahlon G. Kelly and Nicholas Spies. Prentice-Hall, 1986
• Starting Forth, Leo Brody. Forth, Inc., second edition, 1987
• Forth Interest Group http://forth.org/fig.html

SBus/SCSI Developer’s Kit

The SBus/SCSI developer’s Kit, Release III (ISBN 0-13-107202-1) consists of four manuals:
• SunOS 5.3 Writing Device Drivers (ISBN 0-13-107228-5) includes a diskette of sample drivers
• Writing FCode 2.x Programs (p/n 802-1941)
• OpenBoot 2.x Command Reference (802-3241)
This kit is available at PTR PH Magnet Stores, or directly from Prentice Hall. For single copies, phone 515/284-6751, fax 515/284-2607 or e-mail orders@prenhall.com. For quantity orders of more than 10 books, phone 201/592-2498, fax 201/592-2249. International customers: Simon & Schuster International, Customer Service, Attention: Joanne Edwing, 200 Old Tappan Road, Old Tappan, NJ 07675, phone 201/767-4990, fax 201/767-5625. The individual manuals are available through local technical bookstores worldwide.

What Typographic Changes Mean

<table>
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<th>Typeface or Symbol</th>
<th>Meaning</th>
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<tr>
<td>AaBbCc123</td>
<td>The names of commands, files, and directories; on-screen computer output</td>
<td>Edit your .login file. Use ls -a to list all files. system% You have mail.</td>
</tr>
<tr>
<td>AaBbCc123</td>
<td>What you type, contrasted with on-screen computer output</td>
<td>system% su Password:</td>
</tr>
<tr>
<td>AaBbCc123</td>
<td>Variable: replace with a real name or value</td>
<td>To delete a file, type rm filename.</td>
</tr>
<tr>
<td>AaBbCc123</td>
<td>Book titles, new words or terms, or words to be emphasized</td>
<td>Read Chapter 6 in User’s Guide. These are called class options. You must be root to do this.</td>
</tr>
<tr>
<td>[optional]</td>
<td>Optional command-line entry</td>
<td>lpr [-Pprinter]</td>
</tr>
</tbody>
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Code samples are included in boxes and may display the following:

- ok The OpenBoot Forth Monitor prompt
- % UNIX C shell prompt
- $ UNIX Bourne and Korn shell prompt
- # Superuser prompt, all shells
Ordering Sun Documents

The SunDocs Order Desk is a distribution center for Sun Microsystems technical documentation. You can use major credit cards and company purchase orders. You can order documentation in the following ways:

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<tr>
<td>Fax 1-801-373-6798</td>
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You can also email or fax your comments to us. Please include the part number of your document in the subject line of your email or fax message.

- Email: smcc-docs@sun.com
- Fax: SMCC Document Feedback
  1-415-786-6443
Each SBus card must have a PROM whose contents identify the device and its characteristics.

The SBus card’s PROM may also include an optional software driver that lets you use the card as a boot device or a display device during booting.

In addition to designing hardware, the process of developing SBus devices may include writing, testing, and installing FCode drivers for the device. These drivers, if present, perform three functions:

- Exercising the device during development, and verifying its functionality
- Providing the necessary driver to be used by the system boot PROM during power-up
- Providing device configuration information

In practice, these functions overlap substantially. The same code needed by the system boot PROM usually serves to significantly test the device as well, although additional code may be desired to fully verify proper behavior of the device. The PROM code is used before and during the boot sequence. After the boot sequence finishes, and while not using the OpenBoot Forth Monitor, most SBus device use is through Solaris drivers.

SBus device PROMs must be written in the FCode programming language, which is similar to ANS Forth. FCode is described in more detail in Chapter 2, “Elements of FCode Programming”.
FCode PROM Format

An FCode PROM begins at address 0 in the SBus card’s physical address space. Its size can range from 30 bytes up to 32K bytes. Typical sizes are 60 bytes (for a simple card that identifies itself but does not need a driver) and 5 to 15K bytes (for a card with a boot driver). It is good practice to make FCode boot drivers as short as is practical.

An FCode PROM must be organized as follows:

- Header (8 bytes: consisting of magic number, version number, length, checksum).
- Body (FCode program; 0 or more bytes).
- End Token (either End0, a zero byte, or End1, an alternative all 1s byte).

Interpreting FCode

For each SBus slot, the FCode program is interpreted during bootup as follows:

- Location 0 of the SBus PROM is read with an 8-bit or 32-bit access. If there is no response (as when there is no card in that slot), the slot is subsequently ignored.
- If the high-order byte of the value returned from the first access is not the FCode magic numbers 0xfd or 0xf1, the slot is subsequently ignored.
- If the high-order byte is 0xfd or 0xf1, the PROM is assumed to contain a valid FCode program. The FCode is then interpreted by starting at location 0 and reading one byte at a time, executing a procedure associated with each FCode value.
- Interpretation ceases when the FCode 0x00 or 0xff (End0 or End1) is encountered.

Device Identification

An FCode PROM must identify its device. This identification must include, at a minimum, the driver name, used to link the device to its SunOS driver. Identification information may include additional characteristics of the device for the benefit of the operating system and the CPU boot PROM.
In most systems, the CPU’s FCode interpreter will store each device’s identification information in a device tree that has a node for each device. Each device node has a property list that identifies and describes the device. The property list is created as a result of interpreting the program in the FCode PROM.

Each property must have a name and a value. The name is a string and the value is an array of bytes, which may encode strings, numbers, and various other data types.

See Chapter 5, “Properties” for more information.

Creating and Executing FCode Definitions

Many FCode programs create executable routines, called colon definitions (or methods) that typically read from and write to device locations to control device functions. These definitions are also stored in the device tree node for that device.

Once defined, these routines may typically be executed under any of the following circumstances:

- Interactively at the OpenBoot \texttt{ok} prompt
- By the OpenBoot system (for using this boot or display system during system start-up)
- Automatically during FCode interpretation (for power-on initialization or other purposes)
FCode is a computer programming language defined by IEEE Standard 1275-1994. FCode is semantically similar to ANS Forth, but is encoded as a sequence of binary byte codes representing a defined set of Forth definitions.

FCode has these characteristics:

• The source format is machine and system independent.

• The binary format (FCode) is machine, system, and position independent.

• The binary format is compact.

• The binary format can be interpreted easily and efficiently.

• Programs are easy to develop and debug.

• The source format can easily be translated to binary format.

• The binary format can be translated back to source format.

Forth commands are called words, and are roughly analogous to procedures in other languages. Unlike other languages, such as C, which have operators, syntactic characters and procedures, in Forth every word is a procedure.

A Forth word is named by a sequence of between one to 31 printable characters. A Forth program is written as a sequence of Forth word names separated by one or more white space characters (such as spaces, tabs, or line terminators). Forth uses a left-to-right reverse Polish notation, like some scientific calculators. The basic structure of Forth is: do this, now do that, now do something else, and so on.
New Forth words are defined as sequences of previously existing words. Subsequently, new words may be used to create still more words.

FCode is a byte-coded translation of a Forth program. Translating Forth source code to FCode involves replacing the Forth word names (stored as text strings) with their equivalent FCode numbers. The tokenized FCode takes up less space in PROM than the text form of the Forth program from which it was derived, and can be interpreted more easily and rapidly than the text form.

For purposes of this manual, the term FCode indicates both binary-coded FCode and the Forth programs written as ASCII text files for later conversion to binary-coded FCode.

Except where a distinction between the two forms is explicitly stated, the use of FCode in this manual can be assumed to apply equally to both FCode and Forth.

Colon Definitions

Three concepts are critical to understanding FCode (or Forth):

• A colon definition creates a new word with the same behavior as a sequence of existing words. A colon definition begins with a colon and ends with a semicolon.

• Once a new word has been created, it is immediately available, either for direct execution or for use in future colon definitions.

• Most parameter passing is done through a pushdown, last-in, first-out stack.

Normally, the action associated with an FCode Function is performed when the FCode Function is encountered. This is called interpret state. However, the state may switch between interpret state to compile state.

In interpret state, FCode Functions are executed as they are encountered. Interpret state operates until encountering a “.”. The word “.” does the following:

• Allocates an FCode Number and associates it with the name immediately following the colon.

• Switches to compile state.
In compile state, FCodes are saved for later execution, rather than being executed immediately. The sequence thus compiled is installed in the action table as a new word, and can be used later in the same way as if it were a built-in word.

Compile state continues until a “;” is read. The word “;” does the following:

- Compiles an end-of-procedure FCode word
- Switches to interpret state

After compilation, the newly-assigned FCode word can be either interpreted or compiled as part of yet another new word.

If you define a new word having the same spelling as an existing word, the new definition supersedes the older one(s), but only for subsequent usages of that word.

Here’s an example of a colon definition for a new FCode word dac!

```
: dac!  ( data offset -- ) dac + r! ;
```

**Stack Operations**

Each FCode word is specified by its effect on the stack and any side effects, such as accessing memory. Most FCode words affect only the stack, by removing arguments from it, performing some operation, and putting the result(s) back on the stack.

A stack comment, included in the colon definition, describes the effect on the stack of the execution of an FCode word.

In the previous example, the stack comment, beginning with “(” and ending with “)”, shows that dac! takes two parameters from the stack, and doesn’t replace them with anything when it’s done.

Stack comments can be put anywhere in a colon definition. They should be included wherever their use will enhance the clarity of the definition.

The rightmost argument is on top of the stack, with any preceding arguments “under it”. In other words, arguments are pushed onto the stack in left to right order, leaving the most recent argument (the rightmost one in the stack diagram) on the top.
In a stack diagram, parameters shown to the left of the double dashes are expected to be on the stack prior to the execution of the word. Parameters shown to the right of the double dashes are those which are left on the stack after execution of the word. Stack comments use the same convention but detail changes to the stack during execution of the word.

Stack comments and stack diagrams are essentially the same thing. Stack diagrams show the net effect to the stack of any Forth word. Stack comments are embedded in the definition of a word and are used to convey intermediate stack results or changes.

A series of words that describe the behavior of \texttt{dac!} follow the stack comment in the preceding example. Executing \texttt{dac!} is the same as executing the list of words in its colon definition.

Note that FCode words are separated by spaces, tabs, or newlines, so in the previous example, “\texttt{( data}” is not the same as “\texttt{(data}”. Any visible character is part of a word, and not a separator.

\section*{Programming Style}

Some people have described Forth as a write-only language. While it sometimes ends up that way due to poorly-written or uncommented code, it \textit{is} possible to write Forth (and FCode) programs that can be read easily and understood. Well-written Forth programs will meet these criteria. See Appendix C, “Coding Style” for detailed information about the style used in the existing OpenBoot FCode source base.

Although case is not significant, by convention FCode is written in lower case.

\section*{Commenting Code}

Comment code extravagantly, then consider adding more comments. The comments will help with maintenance of the code, and don’t add to the final size of the resulting FCode PROM.

Adopt the useful convention of using “\texttt{( )}” for stack comments and “\texttt{\textbackslash}” for other descriptive text and comments.
In comments, describe the purpose of the Forth words, any interface assumptions and requirements, and unusual aspects of the algorithm you used. Try to avoid simply translating low-level details of the code into English. Comments like, “increment the variable” are rarely helpful.

**Coding Style**

By studying the examples in this book, you can note the indentation and phrasing style that is widely used in OpenBoot source code. Adoption of this style will allow your Forth code to be read more easily by the many programmers who are accustomed to the style.

**Definition Length**

*Keep word definitions short.* If your definition exceeds half a page, it should be rewritten as two or more smaller definitions. This will help to make each definition more readable. Readable code is easier to maintain.

A good size for a word definition is one or two lines of code. Keeping definitions short and limited in functionality improves readability, speeds debugging and increases the likelihood that the word will be reusable. Remember: reuse of Forth words is a principal contributor to compact PROM images.
Stack Comments

Always include stack comments in word definitions. It can be useful to compare intended function with what the code really does. Here’s an example of a word definition with acceptable style.

```fcode
\ xyz-map  establishes a virtual-to-physical mapping for each of the
\ useful addressable regions on the board

0 value status-register

: xyz-map ( -- )

\ Base-address Size create-mapping then save virtual address

  my-address 4 map-low       ( virtaddr )
  to status-register           ( )
  my-address 10.0000 d+ frame-buf-size map-low ( virtaddr )
  to frame-buffer-adr          ( )

;
```

Note the stack diagram following the word `xyz-map`, and the use of stack comments in the word’s definition code.

Stack diagrams are generally written using descriptive parameter names to clarify correct usage. See the table below for stack parameter abbreviations used in this manual.

### Table 2-1 Stack Parameter Abbreviations

<table>
<thead>
<tr>
<th>Notation</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Alternate stack results shown with space, e.g. (input -- addr len false</td>
</tr>
<tr>
<td></td>
<td>Alternate stack items shown without space, e.g. (input -- addr len</td>
</tr>
<tr>
<td>?? or ?</td>
<td>Unknown stack item(s)</td>
</tr>
<tr>
<td>...</td>
<td>Unknown stack item(s). If used on both sides of a stack comment, means the same stack items are present on both sides.</td>
</tr>
<tr>
<td>&lt; &gt; &lt;space&gt;</td>
<td>Space delimiter. Leading spaces are ignored.</td>
</tr>
<tr>
<td>a-addr</td>
<td>Variable-aligned address</td>
</tr>
<tr>
<td>addr</td>
<td>Memory address (generally a virtual address).</td>
</tr>
<tr>
<td>addr len</td>
<td>Address and length for memory region</td>
</tr>
<tr>
<td>Notation</td>
<td>Description</td>
</tr>
<tr>
<td>---------------</td>
<td>-----------------------------------------------------------------------------</td>
</tr>
<tr>
<td>byte bxxx</td>
<td>8-bit value (low order byte in a 32-bit word).</td>
</tr>
<tr>
<td>char</td>
<td>7-bit value (low order byte), high bit unspecified.</td>
</tr>
<tr>
<td>cnt or len or size</td>
<td>Count or length</td>
</tr>
<tr>
<td>dxxx</td>
<td>Double (extended-precision) numbers. 2 stack items, hi quadlet on top of stack.</td>
</tr>
<tr>
<td>&lt;eol&gt;</td>
<td>End-of-line delimiter</td>
</tr>
<tr>
<td>false</td>
<td>0 (false flag)</td>
</tr>
<tr>
<td>ihandle</td>
<td>Pointer for an instance of a package</td>
</tr>
<tr>
<td>n or n1 or n2 or n3 nxxx</td>
<td>Normal signed values (32-bit)</td>
</tr>
<tr>
<td>nu or nul</td>
<td>Signed or unsigned values (32-bit)</td>
</tr>
<tr>
<td>&lt;nothing&gt;</td>
<td>Zero stack items</td>
</tr>
<tr>
<td>phandle</td>
<td>Pointer for a package</td>
</tr>
<tr>
<td>phys</td>
<td>Physical address (actual hardware address)</td>
</tr>
<tr>
<td>phys.lo phys.hi</td>
<td>Lower/upper cell of physical address</td>
</tr>
<tr>
<td>pstr</td>
<td>Packed string</td>
</tr>
<tr>
<td>quad or qxxx</td>
<td>Quadlet (32-bit value)</td>
</tr>
<tr>
<td>qaddr</td>
<td>Quadlet (32-bit) aligned address</td>
</tr>
<tr>
<td>{text}</td>
<td>Optional text. Causes default behavior if omitted.</td>
</tr>
</tbody>
</table>
| "text<delim>" | Input buffer text, parsed when command is executed. Text delimiter is enclosed in <>.
| [text<delim>] | Text immediately following on the same line as the command, parsed immediately. Text delimiter is enclosed in <>.
| true          | -1 (true flag)                                                              |
| uxxx          | Unsigned value, positive values (32-bit)                                    |
| virt          | Virtual address (address used by software)                                 |
| waddr         | Doublet (16-bit) aligned address                                            |
| word or wxxx  | Doublet (16-bit value, low order two bytes in a 32-bit word)                |
| x or xl       | Arbitrary stack item                                                        |
| x.lo x.hi     | Low/high significant bits of a data item                                    |
| xt            | Execution token                                                             |
| xxx?          | Flag. Name indicates usage (e.g. done? ok? error?).                         |
| xyz-str xyz-len | Address and length for unpacked string                                    |
| xyz-sys       | Control-flow stack items, implementation-dependent                         |

Table 2-1  Stack Parameter Abbreviations (Continued)
A Minimal FCode Program

If a peripheral bus card is not needed during the boot process, a minimal FCode program that merely declares the name of the device and the location and size of on-board registers will often suffice.

A minimum program for an SBus device is:

```fcode
fcode-version1
" SUNW,bison" encode-string " name" property
my-address h# 20.0000 + my-space encode-phys
h# 100 encode-int encode+
" reg" property
end0
```
Note the following about this SBus example:

- `my-address` and `my-space` each leave only a single number on the stack representing the `phys.lo phys.hi` address representation of an SBus node. (The value of `#address-cells` is 2 for SBus which is reflected by this format.)

- An offset of 0x200000 is being added to the value returned by `my-address`.

- The `size` argument of `" reg"` is a single number (since `#size-cells` is 1 for SBus) reflecting SBus’s 32-bit address space.

The example program above creates a `" name"` property that will be used to identify the device whose value is “SUNW, bison”. Begin the `name` attribute’s value with an identification of your company. The preferred form of this identification is an organizationally unique identifier (OUI), a sequence of six uppercase hexadecimal digits assigned by the IEEE Registration Authority Committee. OUIs are guaranteed to be unique world-wide. (For more information about obtaining an OUI, please see the glossary entry for `name` in IEEE Standard 1275-1994.)

As an alternative to the OUI, you may use a sequence of from one to five uppercase letters representing the stock symbol of your company on any stock exchange whose symbols do not conflict with the symbols of the New York Stock Exchange and the NASDAQ Exchange. All stock exchanges in the United States satisfy this requirement. If a non-US company’s stock is traded on US stock exchanges by “depository equivalents”, those symbols also satisfy this requirement.

The preceding SBus example program can also be written using the following shorthand form. The FCode program generated will be equivalent to the minimum SBus program given above.

```
fcode-version1
" SUNW, bison" name
my-address h# 20.0000 + my-space h# 100 reg
end0
```
There are four general classes of FCode source words:

**Table 2-2  FCode Source Word Classes**

<table>
<thead>
<tr>
<th>Class</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Primitives</td>
<td>These words generally correspond directly to conventional Forth words, and implement functions such as addition, stack manipulation, and control structures.</td>
</tr>
<tr>
<td>System</td>
<td>These are extension words implemented in the boot PROMs, and implement functions such as memory allocation and device property reporting.</td>
</tr>
<tr>
<td>Interface</td>
<td>These are specific to particular types of devices, and implement functions such as <code>draw-character</code> for a display device.</td>
</tr>
<tr>
<td>Local</td>
<td>These are private words, implemented and used only by the device that created the definition.</td>
</tr>
</tbody>
</table>

Each FCode primitive is represented in a peripheral card’s PROM as a single byte. Other FCodes are represented in the PROM as two consecutive bytes. The first byte, a value from 1 to 0x0f, may be thought of as an escape code.

One-byte FCode numbers range in value from 0x10 to 0xFE. Two-byte FCode numbers begin with a byte in the range 0x01 to 0x0f, and end with a byte in the range 0x00 to 0xff. The single-byte values 0x00 and 0xff signify “end of program” (either value will do; conventionally, 0x00 is used).

Currently-defined FCodes are listed in functional groups, in alphabetic order by name and in numeric order by FCode value in Appendix A, “FCode Reference”.

**Primitive FCode Functions**

There are more than 300 primitive FCode functions, most of which exactly parallel ANS Forth words, divided into three groups:

- FCode words that generate a single FCode byte
- tokenizer macros
- tokenizer directives
Primitive FCode functions that have an exact parallel with standard ANS Forth words are given the same name as the equivalent ANS Forth word. Chapter 11, “FCode Dictionary”, contains further descriptions of primitive FCodes.

There are about another 70 tokenizer macros, most of which also have direct ANS Forth equivalents. These are convenient source code words translated by the tokenizer into short sequences of FCode primitives.

tokenizer directives are words that generate no FCodes, but are used to control the interpretation process. tokenizer directives include the following words:

- decimal, hex, and octal
- d#, h#, and o#
- headers and headerless
- \ and 
- .
- alias

System FCode Functions

System FCode functions are used by all classes of FCode drivers for various system-related functions. System FCode functions can be either service words or configuration words.

- Service words are available to the device’s FCode driver when needed for functions such as memory mapping or diagnostic routines.
- Configuration words are included in the driver to document characteristics of the driver itself. These properties are made available for use by the operating system.
Interface FCode Functions

Interface FCode functions are standard routines used by the workstation's CPU to perform the functions of the peripheral card's device. Different classes of devices will each use only the appropriate set of interface FCodes.

For example, in order to display a character on the screen, OpenBoot calls the interface FCode draw-character. Previously, the FCode driver for the device controlling that screen must have assigned a device-specific implementation to draw-character. It does this as follows:

```
: my-draw ( char -- ) \ "local" word to draw a character.
    ... \ Definition contents.
; \ end of my-draw definition.
: my-install ( -- ) \ local word to install all interfaces.
    ... 
    ['] my-draw to draw-character
    ... 
; 
```

When my-install executes, draw-character is assigned the behavior of my-draw.

Local FCode Functions

Local FCode functions are assigned to words defined in the body of an FCode program. There are over 2000 FCode byte values allocated for local FCodes. The byte values are meaningful only in the context of a particular driver. Different drivers reuse the same set of byte values.
Testing FCode Programs

FCode Source

An FCode source file is essentially a Forth language source code file. The basic Forth words available to the programmer are listed in Chapter 11, “FCode Dictionary”.

FCode programs have the following format:

\ Title comment describing the program that follows
fcode-version1
< body of the FCode program >
end0

fcode-version1 is a macro which directs the tokenizer to create an FCode header. For a description of the FCode header see “FCode Binary Format” on page 19. fcode-version1 produces a header including the version1 FCode. The macro fcode-version2 is similar except that it produces a header containing the start1 FCode. This macro may also be used to begin the FCode source. However since OpenBoot version 1 systems only recognize version1, plug-in device FCode that must run in OpenBoot version 1 systems must use fcode-version1.

dend0 is an FCode that marks the end of an FCode program. It must be at the end of the program or erroneous results may occur. end1 is an alternative but end0 is preferred.
The comment in the first line is not necessary in many cases but it is recommended since it allows some OpenBoot tools to recognize the file as a Forth source file.

**Tokenizing FCode Source**

The process of converting FCode source to FCode binary is referred to as *tokenizing*. A tokenizer program converts FCode source words to their corresponding byte-codes, as indicated in Chapter 11, “FCode Dictionary”. A tokenizer program with instructions describing its use is available from SunExpress®. It is part of the SBus/SCSI Developer’s Kit mentioned in the Preface.

An FCode program’s source can reside in multiple files. The `fload` tokenizer directive directs the tokenizer input stream to load another file. `fload` acts like an `#include` statement in C. When `fload` is encountered, the tokenizer begins processing the file named by the `fload` directive. When the named file is completed, tokenizing continues with the file that issued the `fload`. `fload` directives may be nested.

Typically, the tokenizer produces a file in the following format:

- Header - 32 bytes
- FCode header - 8 bytes
- FCode binary - remainder of file

The header has the following format:

- 4 bytes - 0x01030107 (hexadecimal)
- 4 bytes - Size in bytes of the FCode binary
- 4 bytes - 0x0
- 4 bytes - 0x0
- 4 bytes - 0x0
- 4 bytes - Load point of the file
- 4 bytes - 0x0
- 4 bytes - 0x0
You can use this file to load either an FCode PROM or system memory for debugging as described in “Using the Forth Monitor to Test FCode Programs” on page 23.

The load point of the file is not used when burning an FCode PROM, but is used by Forth Monitor commands that load FCode files into system memory. The tokenizer available from SunExpress sets the load point to be the recommended 0x4000 address.

**FCode Binary Format**

The format of FCode binary that is required by the OpenBoot FCode evaluator is as follows:

**Table 3-1  FCode Binary Format**

<table>
<thead>
<tr>
<th>Element</th>
<th>Structure</th>
</tr>
</thead>
<tbody>
<tr>
<td>FCode header</td>
<td>Eight bytes</td>
</tr>
<tr>
<td>Body</td>
<td>0 or more bytes</td>
</tr>
<tr>
<td>End byte-code</td>
<td>1 byte, the end0 byte-code</td>
</tr>
</tbody>
</table>
The format of the FCode header is:

Table 3-2  FCode Header Format

<table>
<thead>
<tr>
<th>Byte(s)</th>
<th>Content</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>One of the FCodes: version1, start0, start1, start2, start4</td>
</tr>
<tr>
<td>1</td>
<td>reserved</td>
</tr>
<tr>
<td>2 and 3</td>
<td>16-bit checksum of the FCode body</td>
</tr>
<tr>
<td>4 through 7</td>
<td>count of bytes in the FCode binary image including the header</td>
</tr>
</tbody>
</table>

Testing FCode Programs on the Target Machine

Once you have created the FCode binary, you can test it using the OpenBoot Forth Monitor. The Forth Monitor provides facilities to allow you to load your program into system memory and direct the FCode evaluator to interpret it from there. This allows you to debug your FCode without having to create a PROM and attach it to your plug-in board for each FCode revision during the debug process. See the *OpenBoot 3.x Command Reference* for complete documentation of the use of the Forth Monitor.

The FCode testing process generally involves the following steps:

1. Configuring the target machine. This includes installing the hardware associated with the FCode program in the target machine and powering-up the machine to the Forth Monitor.

2. Loading the FCode program into memory from a serial line, a network, a hard disk, or a floppy disk.

3. Interpreting the FCode program to create a device node(s) on the OpenBoot device tree.

4. Browsing the device node(s) to verify proper FCode interpretation.

5. Exercising the FCode program’s device driver methods compiled into the device node, if any.

If the FCode program does not include any methods which involve using the actual hardware (for example, a driver which only publishes properties) then the program can be tested without installing the hardware.
Configuring the Target Machine

Setting Appropriate Configuration Parameters

Before powering-down the target machine to install the target hardware, a few NVRAM configuration variables should be set to appropriate values. You can set them from the Forth Monitor as follows:

```
ok setenv auto-boot? false
ok setenv fcode-debug? true
```

Setting `auto-boot?` to `false` tells OpenBoot not to boot the OS on a machine reset but rather to enter the Forth Monitor at the `ok` prompt.

Setting `fcode-debug?` to `true` tells the OpenBoot FCode evaluator to save the names of words created by interpreting FCode words which were tokenized with `headers` on. This is in addition to words defined after the tokenizer processed an `external` directive (in other words, words whose names are always saved). `fcode-debug?` defaults to `false` to conserve RAM space in normal machine operation. With the names saved, the debugging methods described in later sections will be easier since it will be easier to read decompiled FCode.

Modifying the Expansion Bus Probe Sequence

The start-up sequence in the machine’s OpenBoot implementation normally examines all expansion buses for the presence of plug-in devices and their on-board FCode PROM programs. It then invokes the FCode evaluator to interpret any programs found. This process is called `probing`.

When using the Forth Monitor to load and interpret an FCode program in system memory, it is better to configure OpenBoot to avoid probing that device automatically. The probing can then be done manually (as explained later) from the Forth Monitor.

Configuring an OpenBoot implementation to avoid probing a given slot on a given expansion bus can be done in various implementation-dependent ways. That is, they will be different for different systems and different expansion buses.
Many machines with SBus have an NVRAM configuration variable named `sbus-probe-list`. It defines which SBus card slots will be probed during start up and the order in which they will be probed.

For example, a machine with four SBus slots might have the `sbus-probe-list` configuration variable set to a default value of 0123. Setting `sbus-probe-list` to 013 directs OpenBoot during start-up to probe first SBus slots 0, 1, and 3. This leaves SBus slot 2 unprobed, free for use by the device under development.

Methods to prevent probing a given slot for other types of expansion buses can involve using the NVRAMRC script. Among other uses, an NVRAMRC script can:

- Patch an implementation-specific OpenBoot word that defines the bus’s probe sequence
- Modify a property of the expansion bus’s device node that describes the sequence.

After the FCode program is debugged and programmed in PROM on the device, you can do a full system test (including automatic probing of the new device), by restoring the expansion bus probing configuration to the default.

**Getting to the Forth Monitor**

After completing the configuration described above, power the machine down and install the device. Then power the system up. The display should stop scrolling at the `ok` prompt, ready to accept Forth Monitor commands.

---

**Note** – On the SPARCstation™ 1 and SPARCstation 1+, SBus slot 3 may be used only for SBus slave devices, such as frame buffers. Unlike slots 1 and 2, it may not be used for SBus master devices, such as disk drive or network interfaces.

---

**Using the Command Line Editor of the Forth Monitor**

Refer to the *OpenBoot 3.x Command Reference* for a list and description of the line-editing commands available with the Forth Monitor.
Using the Forth Monitor to Test FCode Programs

Complete directions for using the Forth Monitor to download files to system memory are provided in the *OpenBoot 3.x Command Reference*. A synopsis of FCode words for executing FCode source files is shown below.

<table>
<thead>
<tr>
<th>FCode</th>
<th>Stack Notation</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>begin-package</td>
<td>(arg-addr arg-len reg-addr reg-len path-addr path-len --)</td>
<td>Initializes device tree for executing FCode.</td>
</tr>
<tr>
<td>end-package</td>
<td>(--)</td>
<td>Completes a device tree entry and returns to the Forth Monitor environment.</td>
</tr>
<tr>
<td>open-dev</td>
<td>(path-addr path-len --)</td>
<td>Opens the specified device node and all of its parents.</td>
</tr>
<tr>
<td>device-end</td>
<td>(--)</td>
<td>Closes the current node and returns to the Forth Monitor environment.</td>
</tr>
<tr>
<td>select-dev</td>
<td>(path-addr path-len --)</td>
<td>Opens the specified device node and all of its parents, and makes the device the current instance.</td>
</tr>
<tr>
<td>unselect-dev</td>
<td>(--)</td>
<td>Closes the specified device node and all of its parents, and unselects the active package and current instance leaving none selected.</td>
</tr>
<tr>
<td>set-args</td>
<td>(arg-addr arg-len reg-addr reg-len --)</td>
<td>Sets values returned by my-args, my-space and my-address for the current node.</td>
</tr>
<tr>
<td>execute-device-method</td>
<td>(... path-addr path-len cmd-addr cmd-len -- ... ok?)</td>
<td>Executes the named command in the specified device tree node.</td>
</tr>
</tbody>
</table>
Using `dload` to Load from Ethernet

dload loads files over Ethernet at a specified address, as shown below.

```
ok 4000 dload filename
```

In the above example, `filename` must be relative to the server’s root. Use `4000` (hex) as the address for `dload` input.

FCode programs loaded with `dload` must be in the format described in “Tokenizing FCode Source” on page 18. The tokenizer provided by SunExpress can output these files.

dload uses the trivial file transfer protocol (TFTP), so the server may need to have its permissions adjusted for this to work.

Using `dlbin` to Load From Serial Port A

dlbin may be used to load files over serial line A. Connect the target system’s serial port A to a machine that is able to transfer a file on request. The following example assumes a `tip` window setup on a Sun system which will provide the FCode file. (See the OpenBoot 3.x Command Reference for information on setting `tip` connections.)

1. At the `ok` prompt, type:

```
ok dlbin
```

2. In the `tip` window of the other system, send the file:

```
~C (local command) cat filename
(Away two seconds)
```

The `ok` prompt will reappear on the screen of the target system.

FCode programs loaded with `dlbin` must be in the format described in “Tokenizing FCode Source” on page 18. `dlbin` loads the files at the entry point indicated in the file header. It is suggested that this address be 0x4000.
**Using `boot` to Load From Hard Disk, Diskette, or Ethernet**

You can also load an FCode program with `boot`, the command normally used to boot the operating system. Use the following format:

```
ok boot [device-specifier] [filename] -h
```

`device-specifier` is either a full device path or a device alias. See the OpenBoot 3.x Command Reference for information on device path and aliases.

For a hard disk or diskette partition, `filename` is relative to the resident file system. See the OpenBoot 3.x Command Reference for information on creating a bootable diskette. For a network, `filename` is relative to the system’s root partition on its root server. In both cases, the leading `/` must be omitted from the file path.

The `-h` flag specifies that the program should be loaded, but not executed. This flag must be included since otherwise `boot` will attempt to automatically execute the file assuming it is executable binary.

`boot` uses intermediate booters to accomplish its task. When loading from a hard disk or diskette, the OpenBoot firmware first loads the disk’s boot block, which in turn loads a second-level booter. When loading over a network, the firmware uses TFTP to load the second-level booter. In both cases, `filename` and `-h` are passed to these intermediate booters.

The output file produced by a tokenizer may need to be converted to the format required by the secondary boot program. For example, Solaris 2.x intermediate booters require ELF format. `fakeboot`, a program available from SunExpress, may be useful in this process.

The location in memory where the FCode program is loaded depends on the secondary boot program and the `fakeboot` program.
Using dl to Load Forth Over Serial Port A

Forth programs loaded with dl must be ASCII files.

To load the file over the serial line, connect the system’s serial port A to a machine that is able to transfer a file on request. One method is to set up a TIP window on another Sun system. (See OpenBoot 3.x Command Reference for information on this procedure.) The following example assumes a TIP window setup.

1. At the ok prompt, type:

   ok dl

2. In the TIP window of the other system, send the file, and follow it with a Control-D to signal the end of the file.

   ~C (local command) cat filename
   (Away two seconds)
   ^D

The ok prompt appears on the screen of the system to which the file is loaded.
dl normally loads the file at 4000 (hex). The file is automatically interpreted after it is loaded.

Using the Forth Monitor to Interpret an FCode Program

FCode program interpretation involves creating a device node on the device tree. Device nodes are also known as packages. Creating a device node from downloaded FCode involves the following steps:

1. Set up the environment with begin-package.

   For example, a begin-package call for creating a device node for a SBus card installed in SBus Slot 3 of a SPARCstation 2 looks like:

   ok 0 0 " 3,0" " /sbus" begin-package
In the example, the string, `/sbus`, indicates that the device node which will be created by the FCode program is to be a child node of the `/sbus` node in the device tree.

In general, parent nodes, which support child nodes, can be used as this argument to `begin-package`. The device node defined by the FCode program will be created as a child of that node. Give the full device pathname from the root node. Other types of parent nodes define different address spaces. Another example of an SBus parent node is on a SPARCstation 10 where its device pathname is `/iommu/sbus`.

In the example, the string, "3,0" indicates the SBus slot number, 3 and byte-offset 0 in the slot’s address space where the device node is to be based.

In general, this string is a pair of values separated by a comma which identify the physical address associated with the expansion slot. The form of this physical address depends on the physical address space defined by the parent node. For children of an SBus node, the form is `slot-number, byte-offset`. Other parent nodes will define different address spaces.

The physical address pair value is retrieved in the FCode program with both the `my-address` and `my-space` FCodes. The slot ID string is converted to a binary form consisting of three values. Those values can be retrieved with the FCode program by using `my-address` for the `phys.lo` and `phys.mid` components and `my-space` for the `phys.hi` component.

In the preceding example, the initial 0 0 represents a null argument string passed to the FCode program.

This argument string is retrieved in the FCode program with the `my-args` FCode. Generally, FCode programs do not take arguments at interpretation time so this will usually be the null string. (For the SPARCstation 2, when the FCode PROM on an SBus card is automatically interpreted during system power-on, this is set to a null string.)

`begin-package` is defined as:

```plaintext
: begin-package ( arg-addr arg-len reg-addr reg-len dev-addr dev-len -- )
    select-dev new-device set-args
;
```
select-dev (parent-dev-addr parent-dev-len --) opens the input device node (the parent node) and makes it the current instance.

new-device (--) initializes a new device node as a child of the currently active node and makes it the current instance.

set-args (arg-addr arg-len reg-addr reg-len --) sets the values returned by my-args, my-space, and my-address for the current instance.

2. Interpret the loaded FCode with byte-load

byte-load is the Forth Monitor command that invokes the FCode evaluator to compile the FCode program into the current instance.

For FCode programs downloaded with byte-load use:

```
ok load-base ' c@ byte-load
```

load-base is the system default load address. The argument, ' c@, tells byte-load to use c@ as the access routine for reading the FCode.

3. Close the environment with end-package

end-package finishes up the creation of the device tree node.

```
ok end-package
```

It is defined as:

```
: end-package ( -- ) finish-device unselect-dev ;
```

finish-device ( -- ) Completes the device tree node initialized by new-device and changes the current instance to the parent node.

unselect-dev ( -- ) Closes the parent device tree node and returns to the normal Forth Monitor environment. That is, there is no longer a current instance or active package.
Using the Forth Monitor to Browse a Device Node

The Forth Monitor has many built-in commands to navigate the device tree. Table 3-4 lists the Forth Monitor commands supporting device node browsing:

<table>
<thead>
<tr>
<th>Command</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>.properties</td>
<td>Display the names and values of the current node’s properties.</td>
</tr>
<tr>
<td>dev device-path</td>
<td>Choose the indicated device node, making it the current node.</td>
</tr>
<tr>
<td>dev node-name</td>
<td>Search for a node with the given name in the subtree below the current node, and choose the first such node found.</td>
</tr>
<tr>
<td>dev ..</td>
<td>Choose the device node that is the parent of the current node.</td>
</tr>
<tr>
<td>dev /</td>
<td>Choose the root machine node.</td>
</tr>
<tr>
<td>device-end</td>
<td>De-select the current device node, leaving no node selected.</td>
</tr>
<tr>
<td>&quot; device-path&quot; find-device</td>
<td>Choose device node, similar to dev.</td>
</tr>
<tr>
<td>get-inherited-property</td>
<td>( name-addr name-len -- true</td>
</tr>
<tr>
<td>get-my-property</td>
<td>( name-addr name-len -- true</td>
</tr>
<tr>
<td>ls</td>
<td>Display the names of the current node’s children.</td>
</tr>
<tr>
<td>pwd</td>
<td>Display the device path that names the current node.</td>
</tr>
<tr>
<td>see wordname</td>
<td>Decompile the specified word.</td>
</tr>
<tr>
<td>show-devs [device-path]</td>
<td>Display all the devices known to the system directly beneath a given level in the device hierarchy. show-devs used by itself shows the entire device tree.</td>
</tr>
<tr>
<td>words</td>
<td>Display the names of the current node’s methods.</td>
</tr>
</tbody>
</table>

Table 3-4  Commands for Browsing the Device Tree

Once a device node has been created, you can use the Forth Monitor to browse the node. See the OpenBoot 3.x Command Reference for a more complete discussion. Here is a brief synopsis of the available commands:

- show-devs displays all known devices in the device tree.
- dev sets the active package to a named node so its contents can be viewed.

For example, to make the ACME company’s SBus device named “ACME,widget” the active package:

```
ok dev /sbus/ACME,widget
```
• find-device is essentially identical to dev differing only in the way the input path is passed.

```forth
ok " /sbus/ACME,widget" find-device
```

• .properties displays the names and values of all the properties created for the active package.
• get-my-property returns the value of the specified property from the active package.
• get-inherited-property returns the location and length of the property value array of the specified property from the active package or its parents. dump can then be used to display the property value array.
• ls displays the names of all child nodes, if any, of the active package.
• words shows the names of the device node methods, if any, created by the FCode program. It shows all words which were defined with external and, if fcode-debug? was true when the FCode was interpreted, the words defined with headers.
• see wordname displays the source code (without comments) for wordname.
• device-end reverses the effects of the dev or find-device command.
• pwd displays the device path of the active package.

### Using the Forth Monitor to Test a Device Driver

The Forth Monitor provides the capability to test the methods of an FCode program by allowing you to execute individual methods from the Forth Monitor prompt.
Device Node Methods

Using select-dev

select-dev initializes an execution environment for the methods of the package specified by its stack arguments. It allows the user to subsequently execute the device node’s methods directly by name. For example:

```
ok " /sbus/ACME,widget" select-dev
```

select-dev performs the following steps:

1. Effectively calls “dev /sbus/ACME,widget” to make the named device the active package. This enables the recognition of the device methods by the Forth Monitor.

2. Establishes a chained set of package instances for each node in the path. In particular, this makes the package’s instance-specific data items available to its methods.

3. Opens all device nodes in the path by calling the open method of each.

Once these steps are performed, you can execute the methods of the current device node by typing their names at the prompt. For example:

```
ok clear-widget-register
ok fetch-widget-register .
0
```
As is generally true of the Forth language, if execution of a method exposes an error in the code, the error can be isolated by executing the component words of the method step-by-step. Use `see` to decompile the method, then type the component words individually until the error is apparent. For example:

```
ok  see clear-widget-register
: clear-widget-register
  enable-register-write
  0 widget-register rl!
  disable-register-write
;
ok  enable-register-write
ok 0 widget-register rl!
ok disable-register-write
```

This process can be performed recursively by decompiling the component words and then individually executing their component words. This is much easier if most of the words were defined with the `headers` directive, since `see` can then display the names of the component words instead of hexadecimal codes.

This process is also enhanced by executing `showstack`. `showstack` causes the stack’s contents to be displayed prior to every `ok` prompt. For example:

```
ok 1 2
ok showstack
1 2 ok . clear 3 4
2
3 4 ok
```

Device nodes can also be modified as needed with any of the following techniques:

- Entering new methods definitions. These methods are compiled into the device node like the methods in the FCode program that created the node.
• Redefining a method to include a function neglected in the first definition.
(Previously defined words using the original definition of the method are unaffected.) For example:

```
ok : open open initialize-widget-register-2 ;
```

In general, such redefinitions affect only external uses of the named method (for instance, calls from other packages via $call-method and the like) and interactive use via the Forth Monitor. Previously compiled calls to the method in the same package are unaffected unless the method is called by name (for example, with $call-self).

• Use `patch` to edit word definitions. Such patches affect all uses of the method, both internal and external. (See the OpenBoot 3.x Command Reference Manual for information about using `patch`.)

• Resetting the machine causes all such corrections to be lost. Consequently, once your words are debugged you’ll probably want to include any modifications in the FCode program source.

`unselect-dev` reverses the effects of `select-dev` by calling the close method of each device in the path of the current active node, destroying the package instance of each node, and returning to the normal Forth Monitor environment. Execute `unselect-dev` as follows:

```
ok unselect-dev
```

**Using `begin-select-dev`**

Sometimes, `select-dev` will not work because the `open` method of a newly-written package does not work correctly. In this case, `begin-select-dev` can be used since it does everything that `select-dev` does except for opening the last child node. For example:

```
ok " /sbus/ACME,widget" begin-select-dev
```
Using `execute-device-method`  

`execute-device-method` executes a method directly from the normal Forth Monitor environment. That is, it is not necessary to manually make the device node the current instance before executing the method. For example:

```forth
ok " /sbus/ACME,widget" " test-it" execute-device-method
```

`execute-device-method` returns `false` if the method could not be executed; otherwise it returns `true` on top of whatever results were placed on the stack by the successful execution of the method.

`execute-device-method` performs the following steps:

1. Establishes a chained set of package instances for each node in the path. In particular, this makes an instance of all data items of the device node available to its methods.

2. Opens all device nodes in the named device path except the last device node in the pathname.

3. Invokes the named method.

4. Closes all the device nodes in the path (except the last one) destroying their package instances.

5. Restores the current instance to the one that was current prior to beginning this process.

6. Restores the active package to the one that was active prior to beginning this process.

7. Returns the results.

Note that, in contrast to `select-dev`, `execute-device-method` does not call the `open` method of the last device node in the path. Consequently, any method invoked in this manner must not require any pre-established state which normally is created by `open`.

In summary, `execute-device-method` is provided to allow execution of device node methods designed to provide their own state initialization, and therefore to execute without previous execution of the `open` method. A typical example is a `selftest` method.
**Using apply**

apply provides an alternative manner of invoking execute-device-method in that it takes its arguments from the input stream instead of from the stack. The above example would be invoked with apply as follows:

```
ok apply test-it /sbus/ACME,widget
```

Since apply invokes execute-device-method, all of the restrictions listed above for execute-device-method must be followed.

**Testing FCode Programs in Source Form**

The Forth Monitor enables you to skip the tokenizer and download FCode program source directly. This practice is not recommended since the only advantage is to save a small amount of time tokenizing the program. There are also some disadvantages:

- It may cause problems in the long run since generally the Forth Monitor recognizes a larger number of words than the FCode evaluator does. So the FCode program developer who tests with FCode source may develop and test a program only to find that some of the words used are not FCode words, and will not be accepted by the tokenizer and the FCode evaluator.
- To load source you should comment out fcode-version1 and end0.
- Since the download commands accept only one file, replace fload with the actual file.

To load an ASCII Forth source file over serial line A, you use the command dl. In addition to loading the file over the serial line, dl compiles the Forth source while it is loading, without requiring an extra command. Therefore, you must execute begin-package before downloading. See “Using dl to Load Forth Over Serial Port A” on page 26 for details.

**Producing an FCode PROM**

The output of the tokenizer program is used to make an actual FCode PROM. If your PROM burning tools do not accept the raw binary format of the tokenizer, you may need to develop a format conversion utility.
Exercising an Installed FCode PROM

You can either let OpenBoot automatically evaluate the FCode program from the PROM or you can remove the device from the OpenBoot probing as discussed earlier in “Configuring the Target Machine” on page 21.

The same process discussed for testing FCode programs loaded to system memory can be used to test FCode programs already loaded into PROM on the device.

If you take the device out of the probing sequence, a device node can be built manually as in the following example for a device installed in SBus slot 1:

```
ok 10000 constant rom-size
ok " /sbus" select-dev
ok " 1" decode-unit ( phys.lo phys.mid phys.hi )
ok rom-size map-in ( virt )
ok new-device ( virt )
ok " " 1,0" set-args ( virt )
ok dup 1 byte-load ( virt )
ok finish-device ( virt )
ok rom-size map-out ( virt )
ok unselect-dev
```

This is essentially the same sequence as outlined for evaluating FCode loaded into system memory, except that you must map in and map out the FCode PROM by using the decode-unit, map-in, and map-out methods of the parent device node. For more information about these methods, see Chapter 8, “Memory-Mapped Buses”.

You can browse the device node and exercise the device methods in the same way as described earlier. You can also define new methods and patch existing ones. Of course, these modifications will only remain until a system reset.
A package is the set of methods and properties that resides in a device node. A support package is a group of functions or methods that implements a specific interface. A package implements a library of functions that may then be called by FCode programs.

For many devices, this is not particularly useful, but it will be useful for FCode programs that:

• Implement bootable devices
• Call functions or properties from other packages
• Implement functions intended to be called from other packages

A plug-in package is a package that is not permanently resident in the main OpenBoot PROM. Plug-in packages are written in FCode. Since FCode is represented with a machine-independent binary format, it lets the same plug-in packages be used on machines with different CPU instruction sets.

During the linking process, a package’s references to OpenBoot PROM system functions are resolved and the functions defined by the package made available to other parts of OpenBoot. This occurs at run-time, when OpenBoot interprets (probes) the package. Thus, plug-in packages do not need to be pre-linked with a particular OpenBoot implementation.

OpenBoot only needs the beginning address of the package in order to probe it. Once probed, the package becomes a working part of OpenBoot, until the system is reset or turned off. A package exports its interface to OpenBoot, and to other packages, as a vocabulary of Forth words.
Many packages implement a specific interface; a standard set of functions. Different packages may implement the same interface. For example, there may be two display device driver packages, each implementing the standard display device interface, but for two different display devices.

There may also be multiple instances of a single package. For example, a plug-in disk driver may have as many instances as there are disks of that type.

**Package Instances**

A package consists of:

- methods (software procedures)
- properties (externally-visible information describing the package), and
- data (information used internally by the package).

The active package is the package whose methods are currently visible. `dev` and `find-device` can be used to change the active package. However, they only make a package’s methods visible; they do not enable the execution of those methods.

Before a package’s methods may be executed, create an instance of the package. Think of an instance as a working copy of the package. An instance contains a working copy of all of the package’s private data.

An instance is created from a package by opening that package. The act of opening a package allocates memory for the instance’s data and sets the contents of that memory to the initial values stored in the package. The instance exists until it is terminated by closing it. When it is closed, the memory used to hold that instance’s private data is freed. Multiple instances may be created from the same package, and exist simultaneously.

The current instance is the instance whose private data and methods are available for direct use (i.e. directly by name without having to use `$call-method`).

When a package method accesses a data item, it refers to the copy of that data item associated with the current instance. The private data of the current instance is accessible; the private data of all other instances is inaccessible. Furthermore, to use the methods of a package, an instance of that package must be (at least temporarily) the current instance.
A package to be opened is described by a device path or device alias. The process of opening the package includes opening each of the nodes in the device path from the root to the specified device (i.e. from the top of the chain to the bottom). As each of these nodes is opened, an instance is created for the node and all of these instances are linked together in an instance chain as shown in Figure 4-1. When a method is accessed using the ihandle of the chain, each node in the chain is able to access the methods of its parent with $\text{call-parent}$ using the links provided by the instance chain.

![Instance Chain Device Tree](image)

Figure 4-1  An Instance Chain for /iommu/sbus/SUNW,cgsix

When the chain is no longer needed, the individual instances of the chain may be closed or the entire chain may be closed. When closing the entire chain, the chain is closed from bottom to top to enable a given node’s \text{close} method to use parental methods.

The current instance is a dynamic entity. It is changed in several different ways under several different circumstances. Specifically:

- When a package is first created, \text{new-device}:
  - Creates a new device node that is a child of the active package.
  - Makes that new node the active package.
  - Makes that new node’s instance the current instance.

This causes any instance data/methods that are subsequently created (prior to the execution of \text{finish-device}) to be added to this node, and enables their later execution when an instance of this node is made current.
• When open-dev creates an instance chain, the current instance is repeatedly changed as each node of the instance chain is added to the instance chain (i.e. the root of the chain is first made current while it is being added to the instance chain, then the first child node is made current while it is added to the chain, and so on down to the leaf node). Immediately before terminating, open-dev restores the value in my-self to the value that my-self contained prior to the execution of open-dev. open-dev returns the ihandle of the leaf node of the newly-created instance chain. By manipulating the current instance in this way, open-dev is able to use instance-specific data as required.

• To execute a method not contained in the current instance, $call-method (or one of its derivatives) is used. $call-method:
  • Saves the current value of my-self.
  • Stores its ihandle argument in my-self (thus changing the current instance).
  • Executes the specified method.
  • Restores the saved value of my-self.

• From the user interface, you can change the current instance by setting the value of my-self directly. This is most useful in a debugging scenario when testing the methods of an opened package. (The select-dev method discussed in Chapter 3, “Testing FCode Programs” resets my-self for just this purpose.)

If a package is in the node /packages, $open-package can be used to create an instance of the package. Unlike packages opened with open-dev, packages opened with $open-package are opened without opening their ancestors. Each time a package instance is created by $open-package, that instance is attached to the one that called $open-package. Figure 4-2 shows the modified instance chain that results when the /iommu/sbus/ledma/le instance opens the obp-tftp support package using $open-package.

Notice that the only additional instance created is one for the obp-tftp package, and that this instance is linked to the /iommu/sbus/ledma/le instance. If another instance of obp-tftp were opened by an instance in another instance chain, the resulting instance of obp-tftp would have no association with the instance shown in Figure 4-2.
Package Data

Package data is named, read/write RAM storage used by package methods. Individual data items can be either initialized or zero-filled and either static or instance-specific.

- Static data can be accessed at any time, regardless of whether or not the package has been opened. There is only one copy of each static data item, regardless of the number of currently-open instances of that package. The process of opening a package does not in itself alter the values of static data items (although you can, of course, write code to do so explicitly).
• Instance-specific data can only be accessed when a previously-opened instance of its package is the current instance. The process of opening a package creates copies of its instance-specific data items and establishes their initial values.

• Zero-filled data items are set to zero when a package is opened.

• Initialized data items are set to possibly-non-zero initial values when a package is opened. The initial values are established during the creation of the package.

Initialized data items are created by the Forth defining words defer, value and variable. Uninitialized data items are created by buffer:. Preceding the defining word with the Forth word instance causes the defining word to create an instance-specific item; otherwise it creates a static data item.

Static data items are used for information that applies equally to all instances of the associated package. For example, virtual addresses of shared hardware resources, reference counts and hardware dependent configuration data are often stored as static data.

Instance-specific data items are used for information that differs between instances of the same package. For example, a package that provides a driver for a SCSI host adapter might have several simultaneous instances on behalf of several different target devices; each instance might need to maintain individual state information (e.g. the negotiated synchronous transfer rate) for its target.

**Static and Instance-specific Methods**

There are several different kinds of package methods, depending on the environment in which they are called and their use of static and instance-specific data.

Static methods do not:

• Access instance-specific data either directly or by calling other instance-specific methods.

• Attempt to call methods of their parent.

Static methods can be called when there is no open instance of their package. When there is no instance, there is also no parent instance (which is the reason for the prohibition about calling parent methods).
The most important example of static methods is the decode-unit method which is called by the system during the process of searching the device tree without opening all of the nodes that are encountered.

Instance-specific methods are permitted to:

- Use instance-specific data
- Call the methods of their parent.

There is no structural difference between static and instance-specific methods. The concept of static methods is just a terse way of saying that some methods have to obey the restrictions outlined above. Instance-specific methods are the usual case; the static methods restrictions apply only to a very small set of special-purpose methods.

**Execution Tokens**

A method is identified by its execution token, xt. For words in the package being defined, the Forth word ['] returns an execution token. The execution token is returned by find-method for other packages. (See the following sections for more details.)

The execution token is used to execute a method in another package, and also to schedule a method for automatic, repeated execution by the system clock interrupt. See the alarm FCode.

**Intra-package Calling Methods**

A package can call its own methods directly simply by naming the target method in a Forth colon definition. Such calls require neither a call-time name search nor a change of the current instance. The binding of name to execution behavior occurs at compile time, so subsequent redefinitions of a name do not affect previously-compiled references to old versions of that named method.

Infrequently, it may be desirable to call a method in the same package so that the name search happens at run-time. To do so, use either $call-method or find-method/call-package with my-self as the ihandle argument. (See the next section for details.)
Accessing Other Packages

Packages often use methods of other previously-defined packages. There are two types of packages whose methods can be used directly:

- The parent of the package being defined.
- Support packages in the /packages node of the device tree.

Phandles and Ihandles

A package definition is identified by its phandle. find-package returns the phandle of a package in the /packages node. The phandle can then be used to open that support package or to examine its properties. For example:

```
" deblocker" find-package
```
returns either false (package not found), or phandle true.

Opening a support package with open-package returns an ihandle. This ihandle is used primarily to call the methods of the support package, and to close the support package when it is no longer needed.

The ihandle of the current instance is stored in word my-self. An instance argument string must be supplied when opening any package (it may be null). The instance argument string can then be accessed from in the opened package with the my-args FCode (see below for details). For example (assume that phandle has already been found):

```
" 5,3,0" phandle open-package ( ihandle )
```

If the package cannot be opened, an ihandle of 0 is returned.

$open-package includes the functions of find-package and open-package. In most cases, it can be used in their place. The primitive functions find-package and open-package are rarely used directly, although find-package is sometimes used when it’s necessary to examine a support package’s properties without opening it.
The following FCode functions are used to find and open packages (in the /packages node):

Table 4-1  Package Access FCodes

<table>
<thead>
<tr>
<th>Name</th>
<th>Stack Comment</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>find-package</td>
<td>(name-str name-len -- false</td>
<td>phandle true)</td>
</tr>
<tr>
<td>open-package</td>
<td>(arg-str arg-len phandle -- ihandle</td>
<td>false)</td>
</tr>
<tr>
<td>$open-package</td>
<td>(arg-str arg-len name-addr name-len -- ihandle</td>
<td>false)</td>
</tr>
</tbody>
</table>

Here is an example of using $open-package:

"5,3,0" "deblocker" $open-package (ihandle | 0 )

Table 4-2  Manipulating phandles and ihandles

<table>
<thead>
<tr>
<th>Name</th>
<th>Stack Comment</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>my-self</td>
<td>( -- ihandle )</td>
<td>Return the instance handle of the currently-executing package instance.</td>
</tr>
<tr>
<td>my-parent</td>
<td>( -- ihandle )</td>
<td>Return the instance handle of the parent of the currently-executing package instance.</td>
</tr>
<tr>
<td>ihandle&gt;phandle</td>
<td>(ihandle -- phandle )</td>
<td>Convert an instance handle to a package handle.</td>
</tr>
<tr>
<td>close-package</td>
<td>(ihandle -- )</td>
<td>Close a package instance.</td>
</tr>
</tbody>
</table>

Don’t confuse phandle with ihandle. Here’s how to use them:

1. **Open the package with $open-package which returns an ihandle.**
2. Use the ihandle to call the methods of the package.

3. When done calling the methods of the package, use the ihandle to close the instance of the package with close-package.

A package’s phandle is primarily used to access the package’s properties which are never instance-specific. Use ihandle>phandle to find the phandle of an open package. my-self and my-parent return ihandles, which can be converted into phandles with ihandle>phandle.

Inter-package Calling Methods

The following FCode functions enable the calling of methods of other packages:

<table>
<thead>
<tr>
<th>Name</th>
<th>Stack Comment</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>$call-method</td>
<td>( ... method-str method-len ihandle -- ??? )</td>
<td>Shortcut word that finds and executes the method method-str method-len in the package instance ihandle.</td>
</tr>
<tr>
<td>call-package</td>
<td>( ... xt ihandle -- ??? )</td>
<td>Executes the method xt in the instance ihandle.</td>
</tr>
<tr>
<td>$call-parent</td>
<td>( ... method-str method-len -- ??? )</td>
<td>Executes the method method-str method-len in the parent’s package instance. Identical to calling my-parent $call-method.</td>
</tr>
<tr>
<td>execute-device-method</td>
<td>( ... dev-str dev-len method-str method-len -- ... false</td>
<td>?? true )</td>
</tr>
<tr>
<td>find-method</td>
<td>( method-str method-len phandle -- false</td>
<td>xt true )</td>
</tr>
</tbody>
</table>

$call-parent is used most-often, but is the least flexible of the above methods; it is exactly equivalent to the sequence “my-parent $call-method”. Most inter-package method calling involves calling the methods of one’s parent; $call-parent conveniently encapsulates the process of doing so.
$call-method$ can call methods of non-parent packages. It is most commonly used for calling methods of support packages. The ihandle argument of $call-method$ identifies the package instance whose method is to be called.

For example:

```
$call-parent
$open-package $call-method
```

Both $call-parent and $call-method identify their target method by name. The method-str method-len arguments denote a text string that $call-parent or $call-method uses to search for a method of the same name in the target instance’s list of methods. Obviously, this run-time name search is not as fast as directly executing a method whose address is already known. However:

1. Most packages have a relatively small number of methods,
2. Systems typically implement a reasonably-efficient name search mechanism, and
3. Inter-package calls tend to occur relatively infrequently.

Consequently, the length of time spent searching is usually not a limiting factor.

A more complete example demonstrates the use of $open-package and $call-method:

```
: add-offset ( x.byte# -- x.byte#' )
  my-args " disk-label" $open-package( ihandle )
  " offset" rot ( name-addr name-len ihandle )
  $call-method

;
```

When method name search time is a limiting factor, use find-method to perform the name search once. Then use call-package repetitively thereafter. find-method returns, and call-package expects, an execution token by which a method can be called quickly.
A more complex example that is somewhat faster if called repeatedly:

```fcode
: init ( -- )
   my-args " disk-label" $open-package ( ihandle ) to label-ihandle
   " offset" label-ihandle ihandle>phandle ( name-addr name-len phandle )
   find-method if
      ( xt ) to offset-method
   else ." Error: can't find method"
   then
;
: add-offset ( d.byte# -- d.byte#' )
   offset-method label-ihandle call-package
;
```

Because device access time often dominates I/O operations, the benefit of this extra code probably won’t be noticed. It is only justified if the particular method will be called often.

Another use of find-method is to determine whether or not a package has a method with a particular name. This allows you to add new methods to an existing package interface definition without requiring version numbers to denote which new or optional methods a package implements.

With $call-method and $call-parent, the method name search is performed on every call. Consequently, if a new method (either one with a new name or with the same name as a previously-existing name) is created, any subsequent uses of $call-method or $call-parent naming that method will find the new one. On the other hand, find-method binds a name to an execution token and subsequent redefinitions of that name do not affect the previous execution token, so subsequent uses of $call-method continue to call the previous definition. In practice, this difference is rarely important, since it is quite unusual for new methods to be created when a package is already open. The one case where methods are routinely redefined under these circumstances is when a programmer does it explicitly during a debugging session; making such redefinitions is a powerful debugging technique.
All of the method calling functions described above change the current instance to the instance of the callee for the duration of the call, restoring it to the instance of the caller on return.

**execute-device-method and apply**

In addition to the inter- and intra-package method calling techniques just described, there is another way of calling methods that is rarely used by FCode Programs. `execute-device-method` and its variant `apply` allow a user to invoke a method of a particular package as a self-contained operation without explicitly opening and closing the package as separate operations. `execute-device-method` first opens all the package’s parents, then calls the named method, and then closes all the parents.

`apply` performs the same functions as `execute-device-method`, but it takes its arguments from the command line instead of from the Forth stack. It is consequently somewhat more convenient to use interactively.

`execute-device-method` and `apply` are most often used for methods like `selftest`. `selftest` methods are usually called with the `test` user interface command, which is usually implemented with `execute-device-method`.

Methods that are intended to be called with `execute-device-method` or its equivalent must not assume that the package’s `open` method has been called, because `execute-device-method` does not call the `open` method of the package containing the target method although it opens all of the package’s parents. Consequently, the target method must explicitly perform whatever initialization actions it requires, perhaps by calling the `open` method in the same package, or by executing some sub-sequence thereof. Before exiting, the target method must perform the corresponding `close` actions to undo its initialization actions.

`execute-device-method` was intentionally designed not to call the target’s `open` and `close` methods automatically since the complete initialization sequence of `open` is not always appropriate for methods intended for use with `execute-device-method`. In particular, an `open` method usually puts its device in a fully operational state, while methods like `selftest` often need to perform a partial initialization of selected device functions.

`execute-device-method` is used with these methods:

- `abort?` (used in the keyboard driver.)
Plug-in Device Drivers

Plug-in device drivers are plug-in packages implementing simple device drivers. The interfaces to these drivers are designed to provide basic I/O capability.

Plug-in drivers are used for such functions as booting the operating system from a device or displaying text on a device before the operating system has activated its own drivers. Plug-in drivers are added to the device tree during the probing phase of the OpenBoot PROM start-up sequence.

Plug-in drivers must be programmed to handle portability issues, such as hardware alignment restrictions and byte ordering of external devices. With care, you can write a driver so that it is portable to all of the systems in which the device could be used.

Plug-in drivers are usually stored in PROM located on the device itself, so that the act of installing the device automatically makes its plug-in driver available to the OpenBoot PROM.

For devices with no provision for such a plug-in driver PROM, the plug-in driver can be located elsewhere, perhaps in PROM located on a different device or in an otherwise unused portion of the main OpenBoot PROM. However, use of such a strategy limits such a device to certain systems and/or system configurations.
Common Package Methods

Different packages have different collections of methods depending on the job(s) that the packages have to do. The following four methods are found in many device drivers. None of them can be considered to be required, however, since the nature of a given driver governs the methods that the driver needs.

open and close are found in many drivers, but even they are not universally required. open and close are needed only if the device will be used with open-dev or another method that calls open-dev. Any device that has read and/or write methods needs open and close, as does any parent device whose children could possibly be opened.

Another way of looking at this is that open and close are needed for devices that are used to perform a series of related operations distributed over a period of time, relative to some other calling package. open initializes the device state that is maintained during the series of later operations, and close destroys that state after the series is complete.

To illustrate, a series of write calls generated by another package is such a series. Conversely, selftest is not such a series; selftest happens “atomically” as an indivisible self-contained operation.

Basic Methods

open ( -- ok? )

Prepares a package for subsequent use. open typically allocates resources, maps, initializes devices, and performs a brief sanity check (making no check at all may be acceptable). true is returned if successful, false if not. When open is called, the parent instance chain has already been opened, so this method may call its parent’s methods.
**close** ( -- )

Restores a package to its “not in use” state. close typically turns off devices, unmaps, and de-allocates resources. close is executed before the package’s parent is closed, so the parent’s methods are available to close. It is an error to close a package which is not open.

**Recommended Methods**

The following methods are highly recommended.

**reset** ( -- )

Put the package into a “quiet” state. reset is primarily for packages that do not automatically assume a quiet state after a hardware reset, such as devices that turn on with interrupt requests asserted.

**selftest** ( -- error# )

Test the package. selftest is invoked by the OpenBoot test word. It returns 0 if no error is found or a package-specific error number if a failure is noticed.

test does not open the package before executing selftest, so selftest is responsible for establishing any state necessary to perform its function prior to starting the tests, and for releasing any resources allocated after completing the tests. There should be no user interaction with selftest, as the word may be called from a program with no user present.

If the device was already open when selftest is called, a new instance will still be created and destroyed. A well-written selftest should handle this possibility correctly, if appropriate.

If the device is already open, but it is not possible to perform a complete selftest without destroying the state of the device, the integrity of the open device should take precedence, and the selftest process should test only those aspects of the device that can be tested without destroying device state. The inability to fully test the device should not be reported as an error result; an error result should occur only if selftest actually finds a device fault.
The "device already open" case happens most commonly for display devices, which are often used as the console output device, and thus remain open for long periods of time. When testing a display device that is already open, it is not necessary to preserve text that may already be on the screen, but the device state should be preserved to the extent that further text output can occur and be visible after selftest exits. Any error messages that are displayed by the selftest method will be sent to the console output device, so when testing an already-open display device, such error messages should be avoided during times when selftest has the device in a state where it is unable to display text.

selftest is not executed in an open/close pair. When selftest executes, a new instance is created (and destroyed). It will have its own set of variables, values, and so forth. These quantities are not normally shared with an instance opened with the normal open routine for the package.

Note – selftest should be written to do its own mapping and unmapping.

Package Data Definitions

The following examples show how to create static data items:

```
variable bar
5 value grinch
defer stub
create ival x , y , z ,
7 buffer: foo
ival foo 7 move \ One way to initialize a buffer
```

The data areas defined above are shared among all open instances of the package. If a value is changed, for instance, the new value will persist until it is changed again, independent of the creation and destruction of package instances.

Any open instance of a package can access and change the value of a static data item, which changes it for all other instances.
The following examples show how to create instance-specific data items, whose values are not shared among open instances:

```
instance variable bar
5 instance value grinch
instance defer stub
7 instance buffer: foo
```

Instance-specific data areas are re-initialized when a package instance is created (usually by opening the package), so each instance gets its own copy of the data area. For example, changes to `bar` in one instance will not affect the contents of it in another instance. (Note that `create` operates across all the instances, and cannot be made instance-specific.)

The total amount of data space needed for a package’s instance-specific data items is remembered as part of the package definition when `finish-device` finishes the package definition. Also, the contents of all the `variables`, `values`, and `defers` at the time `finish-device` executes are stored as part of the package definition.

An instance of the package is created when that package is opened. Data space is allocated for that instance (the amount of which was remembered in the package definition). The portion of that data space created with `variable`, `value`, or `defer` is initialized from the values stored in the package definition. Data space created with `buffer:` is set to zero.

You can add new methods and new properties to a package definition at any time, even after `finish-device` has been executed for that package. To do so, select the package and create definitions or properties.

However, you cannot add new data items to a package definition after `finish-device` has been executed for that package. `finish-device` sets the size of the data space for that package, and subsequently the size is fixed.

**Instance Arguments and Parameters**

An instance argument (`my-args`) is a string that is passed to a package when it is opened. The string may contain parameters of any sort, based on the requirements of the package, or may simply be a null-string if no parameters are needed. A null string can be generated with either " " or 0 0.
The instance argument passed can be accessed from inside the package with the `my-args` FCode.

**Note** – A package is not required to inspect the passed arguments.

If the argument string contains several parameters separated by delimiter characters, you can extract the subsections from the package with `left-parse-string`. You can use any character as the delimiter; a comma is commonly used for this.

**Note** – Avoid using blanks or the `/` character, since these will confuse the parsing of pathnames.

A new value for `my-args` is passed when a package is opened. This can happen under a number of circumstances:

1. The `my-args` string will be null when FCode on a SBus card is interpreted automatically by the OpenBoot system at power-on.

2. The `my-args` string is set by a parameter to `begin-package`, which is used to set up the device tree when Forth source code is downloaded and interpreted interactively.

3. The `my-args` string can be set with `set-args` before a particular slot is probed, if SBus probing is being controlled from `nvramrc`.

The above three instances happen only once, when the package FCode is interpreted for the first time. If you want to preserve the initial value for `my-args`, the FCode program should copy it into a static buffer to preserve the information.

Whenever a package is re-opened, a new value for `my-args` is supplied. The method for supplying this new value depends on the method used to open the package, as described below.

- The instance argument (`my-args`) is supplied as a string parameter to the commands `open-package` or `$open-package`. 
• User Interface commands, such as open-dev, execute-device-method and test, supply the entire pathname to the device being opened. This approach lets an instance argument be included in the pathname. For example, to open the SBus device SUNW, bwtwo with the argument string 5,3,0, enter:

```
ok " /sbus/SUNW,bwtwo:5,3,0" open-dev
ok
```

Here is a more complicated (and fictitious) example:

```
ok " /sbus/SUNW,fremly:test/grumpin@7,32:print/SUNW,fht:1034,5"
ok open-dev
ok
```

Here the string test is passed to the SUNW, fremly package as it is opened, the string print is passed to the grumpin package as it is opened, and the string 1034,5 is passed to the SUNW, fht package as it is opened.

**Package Addresses**

A package’s address relative to its parent package is another piece of information available to a package. Again, there are two main ways to pass this address to the package:

• Part of the pathname of the package

• A string parameter given to the probe words

As an example of the first method, suppose the following package is being opened:

```
ok "/sbus/esp/sd@3,0:b" open-dev
```

Then the address of the /sd package relative to the /esp package is 3, 0.
The package can find its relative address with my-unit, which returns the address as a pair of numbers. The first number (high) is the number before the comma in the example above, and the second number (low) is the number after the comma. Note that these are numbers, not strings.

As an example of the second method, suppose a test version of an FCode package is being interpreted:

```plaintext
ok 0 0 "3,0"="/sbus" begin-package
```

Here the my-args parameters for the new FCode are null, the initial address is 3, 0 and it will be placed under the /sbus node.

The initial address can be obtained through my-address and my-space. Typically, you use my-space and my-address (plus an offset) to create the package’s "reg" property, and also to map in needed regions of the device.

**Package Mappings**

Mappings set up by a package persist across instances unless they are explicitly unmapped. It is usually best for each new instance to do its own mappings, being sure to unmap resources as they are no longer needed.

However, if it is unlikely that a particular package will have several concurrent open instances, it is usually a good idea to maintain only one mapping for all the open instances, using a reference counter to count them. The variables that store the reference counter and the mapped address must be static, not instance-specific. When the last instance is closed, the resources should be unmapped.

**nvramrc**

Machines that support packages will generally also support the nvramrc facility. nvramrc is a special area in the NVRAM that can contain user interface commands to be executed by OpenBoot as the machine powers on. These commands can be used to specify behavior during start up or to define changes for later execution.
For example: assume a card in SBus slot #2 (named XYZ, me) needs custom attributes set by the user. `nvramrc` contents would include:

```plaintext
probe-all
dev /sbus/XYZ,me
 " type5" encode-string " xyzmode" property
device-end
install-console
banner
```

After editing `nvramrc`, turn on the NVRAM parameter `use-nvramrc?` and reset the machine to activate the contents of `nvramrc`. See `nvedit` in Chapter 11, “FCode Dictionary” for more about editing `nvramrc` contents.

### Modifying Package Properties

To modify the properties of a package, first probe the package to get it into memory, then create or modify properties by executing `property` or one of its short-hand forms. Normally, probing is done automatically after the `nvramrc` commands are executed. 

See Chapter 5, “Properties“, for more information about properties.

### Standard Support Packages

The `/packages` node of the device tree is unique. It has children, but instead of describing a physical bus, `/packages` serves as a parent node for support packages. The children of `/packages` are general-purpose software packages not attached to any particular hardware device. The “physical address space” defined by `/packages` is a trivial one: there are no addresses. Its children are distinguished by name alone.

The children of `/packages` are used by other packages to perform commonly used functions. They may be opened with the FCodes `open-package` or `$open-package`, and closed with `close-package`. IEEE Standard 1275-1994 defines three support packages that are children of `/packages`. 
Sun Disk-Label Support Package

Disk (block) devices are random-access, block-oriented storage devices with fixed-length blocks. Disks may be subdivided into several logical partitions, as defined by a disk label—a special disk block, usually the first one, containing information about the disk. The disk driver is responsible for appropriately interpreting a disk label. The driver may use the standard support package /disk-label if it does not implement a specialized label.

/disk-label interprets a standard Sun disk label, reading any partitioning information contained in it. It includes a first-stage disk boot protocol for the standard label. load is the most important method defined by this package.

This package uses the read and seek methods of its parent (in practice, the package which opens this one to use the support routines). /disk-label defines the following methods:

Table 4-4  Sun Disk Label Package Methods

<table>
<thead>
<tr>
<th>Name</th>
<th>Stack diagram</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>open</td>
<td>(&lt; flag)</td>
<td>Reads and verifies the disk label accessed by the read and seek methods of its parent instance. Selects a disk partition based on the text string returned by my-args. For the standard Sun disk label format, the argument is interpreted as follows:</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Argument</td>
</tr>
<tr>
<td></td>
<td></td>
<td>&lt;none&gt;</td>
</tr>
<tr>
<td></td>
<td></td>
<td>a or A</td>
</tr>
<tr>
<td></td>
<td></td>
<td>b or B</td>
</tr>
<tr>
<td></td>
<td></td>
<td>...</td>
</tr>
<tr>
<td></td>
<td></td>
<td>h or H</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Returns -1 if the operation succeeds. As a special case, if the argument is the string “nolabel”, open returns -1 (success) without attempting to read or verify the label.
Table 4-4  Sun Disk Label Package Methods

<table>
<thead>
<tr>
<th>Name</th>
<th>Stack diagram</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>close</td>
<td>(--)</td>
<td>Frees all resources that were allocated by open.</td>
</tr>
<tr>
<td>load</td>
<td>(adr -- size)</td>
<td>Reads a stand-alone program from the standard disk boot block location for the partition specified when the package was opened. Puts the program at memory address adr, returning its length size. For the standard Sun disk format, the stand-alone program is 7.5K bytes beginning 512 bytes from the start of the partition.</td>
</tr>
<tr>
<td>offset</td>
<td>(x.rel-- x.abs)</td>
<td>Returns the 64-bit absolute byte offset x.abs corresponding to the 64-bit partition-relative byte offset x.rel. In other words, adds the byte location of the beginning of the selected partition to the number on the stack.</td>
</tr>
</tbody>
</table>

### TFTP Booting Support Package

The /obp-tftp package implements the Internet Trivial File Transfer Protocol (TFTP) for use in network booting. It is typically used by a network device driver for its first stage network boot protocol. Again, load is the most important method defined by this package.

This package uses the read and write methods of its parent, and defines the following methods:

Table 4-5  TFTP Package Methods

<table>
<thead>
<tr>
<th>Name</th>
<th>Stack diagram</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>open</td>
<td>(--)</td>
<td>Prepares the package for subsequent use, returning -1 if the operation succeeds and 0 otherwise.</td>
</tr>
<tr>
<td>close</td>
<td>(--)</td>
<td>Frees all resources that were allocated by open.</td>
</tr>
<tr>
<td>load</td>
<td>(adr -- size)</td>
<td>Reads the default stand-alone program from the default TFTP server, putting the program at memory address adr and returning its length size. For the standard Sun TFTP booting protocol, RARP (Reverse Address Resolution Protocol) is used to acquire the IP address corresponding to the system’s MAC address (equivalent to its Ethernet address). From the IP address, the default file name is constructed, of the form &lt;Hex-IP-Address&gt;.&lt;architecture&gt; (for example, C0092E49.SUN4C). Then obp-tftp tries to TFTP read that file, first trying the server that responded to the RARP request, and if that fails, then broadcasting the TFTP read request.</td>
</tr>
</tbody>
</table>
Deblocker Support Package

The /deblocker package makes it easy to implement byte-oriented device methods, using the block-oriented or record-oriented methods defined by devices such as disks or tapes. It provides a layer of buffering between the high-level byte-oriented interface and the low-level block-oriented interface. /deblocker uses the max-transfer, block-size, read-blocks and write-blocks methods of its parent, and defines the following methods:

Table 4-6  Deblocker Package Methods

<table>
<thead>
<tr>
<th>Name</th>
<th>Stack diagram</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>open</td>
<td>(-- flag)</td>
<td>Prepares the package for subsequent use, allocating the buffers used by the deblocking process based on the values returned by the parent instance’s max-transfer and block-size methods. Returns -1 if the operation succeeds, 0 otherwise.</td>
</tr>
<tr>
<td>close</td>
<td>(--)</td>
<td>Frees all resources that were allocated by open.</td>
</tr>
<tr>
<td>read</td>
<td>(adr len -- actual)</td>
<td>Reads at most len bytes from the device into the memory buffer beginning at adr. Returns actual, the number of bytes actually read, or 0 if the read operation failed. Uses the parent’s read-blocks method as necessary to satisfy the request, buffering any unused bytes for the next request.</td>
</tr>
<tr>
<td>write</td>
<td>(adr len -- actual)</td>
<td>Writes at most len bytes from the device into the memory buffer beginning at adr. Returns actual, the number of bytes actually read, or 0 if the write operation failed. Uses the parent’s write-blocks method as necessary to satisfy the request, buffering any unused bytes for the next request.</td>
</tr>
<tr>
<td>seek</td>
<td>(x.position -- flag)</td>
<td>Sets the device position at which the next read or write will take place. The position is specified by the 64-bit number x.position. Returns 0 if the operation succeeds or -1 if it fails.</td>
</tr>
</tbody>
</table>
Properties

This chapter describes characteristics of hardware devices, software and user selections. Properties are associated with the device node in which they are created and are accessible both by OpenBoot routines and by client programs. Properties can be inspected and, in some cases, modified.

Each property has a *property name* and a *property value*.

- Property names are human-readable strings consisting of one to 31 printable, lower-case letters and symbols not including blanks, “/”, “\”, “:”, “[“, “]” or “@”. Property names beginning with “+” are reserved for future use by IEEE Standard 1275-1994.

- Property values specify the contents, or value, of a particular property. The value is an array of bytes that may be used to encode integer numbers, text strings, or other forms of information.

Properties are accessed by name. Given a property’s name, it is possible to determine whether that property has been defined and, if so, what its value is.

Property values are encoded as arrays of zero or more bytes for portability across machine architectures. The encoding and decoding procedures are defined by IEEE Standard 1275-1994. The encoding format is independent of hardware byte order and alignment characteristics. The encoded byte order is big-endian and the bytes are stored in successive memory locations without any padding.
The format of the property value array associated with a given property name is specific to that property name. There are five basic types of property value array formats:

- **flag**
  
  Since property value arrays may be of zero length, properties may convey “true” or “false” information by their presence or absence.

- **byte**
  
  An array of 1 or more bytes is stored in a property value array as a series of sequential bytes in the property value array.

- **32-bit integer**
  
  A 32-bit integer is stored in a property value array in four successive bytes. The most significant byte of the integer in the next available address in the property value array is followed by the high middle, low middle and least significant bytes of the integer (i.e. in big-endian format).

- **text string**
  
  A text string of n printable characters is stored in a property value array in n+1 successive locations by storing the string in the first n locations followed by a byte of zero value (i.e. a null terminated string).

- **composite**
  
  A composite value is made up of the concatenation of encoded bytes, encoded 32-bit integers and/or encoded strings. Each such primitive is stored immediately after the preceding primitive with no intervening space (i.e. the items are packed). Here are examples of composite values:
  
  a. *physical address range*. Encoded as 4 integers: phys.lo phys.mid phys.hi size
  
  b. *array*. The concatenation of n items of some type.

The standard defines a number of standard properties with specified names and value formats. If a package uses one of these standard properties then the value format of the property must be as defined by the standard. Packages may define other properties whose names do not conflict with the list of standard properties. Such newly defined properties may have any value format.
Properties may be created by FCode programs. The CPU’s OpenBoot is able to use property names that tell it such things as the device type (e.g. disk, tape, network, display, etc.) to determine how to use the device (if at all) during the boot process.

Solaris recognizes other property names that give information for configuring the operating system automatically. These properties include the driver name, the addresses and sizes of the device’s registers, and interrupt levels and interrupt vectors used by the device.

Other properties may be used by individual operating system device drivers. The names of such properties and the interpretation of their values is subject to agreement between the writers of the FCode programs and the operating system driver, but may otherwise be arbitrarily chosen. For example, a display device might declare width, height, and depth properties to allow a single operating system driver to automatically configure itself for one of several similar but different devices.

A package’s properties identify the characteristics of the package and its associated physical device, if any. You can create a property either with the property FCode, or with the name, reg, model, and device-type FCodes, described below.

For example, a frame buffer package might export its register addresses, interrupt levels, and frame buffer size. Every package has an associated property list, which is arbitrarily extensible. The user interface command .properties displays the names and values of the current node’s properties.

For example, if a property named foo is created in a device node which already has a property named foo, the new property supersedes the old one.

New properties can be added during the lifetime of a product. For backward compatibility, an FCode or device driver program that needs the value of a particular property should determine whether or not the property exists, and if not the program should supply its own default value.

### Standard FCode Properties

*IEEE Standard 1275-1994* defines the following standard properties. A package should never create any property using any of the following names, unless the defined meanings and structures are used.
Standard Property Names

This group of properties applies to all device nodes regardless of type. The name property is required in all packages. The remaining properties are optional.

<table>
<thead>
<tr>
<th>Property</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>name</td>
<td>Name of the package.</td>
</tr>
<tr>
<td>reg</td>
<td>Package’s registers.</td>
</tr>
<tr>
<td>device_type</td>
<td>Characteristics that the device is expected to have.</td>
</tr>
<tr>
<td>model</td>
<td>Manufacturer’s model number.</td>
</tr>
<tr>
<td>interrupts</td>
<td>Interrupts used by the device.</td>
</tr>
<tr>
<td>address</td>
<td>Virtual addresses of one or more memory-mapped regions of the device.</td>
</tr>
<tr>
<td>compatible</td>
<td>List of devices with which this device is compatible.</td>
</tr>
<tr>
<td>status</td>
<td>Operational status of the device.</td>
</tr>
</tbody>
</table>

Display Device Properties

Display devices include bit-mapped frame buffers, graphics displays and character-mapped displays. Display devices are typically used for console output. The following properties are specific to display devices:

<table>
<thead>
<tr>
<th>Property</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>character-set</td>
<td>Character set (e.g. ISO8859-1).</td>
</tr>
<tr>
<td>depth</td>
<td>Number of bits in each pixel of the display.</td>
</tr>
<tr>
<td>height</td>
<td>Number of pixels in the “y” direction of the display.</td>
</tr>
<tr>
<td>linebytes</td>
<td>Number of pixels between consecutive scan lines of the display.</td>
</tr>
<tr>
<td>width</td>
<td>Number of pixels in the “x” direction of the display.</td>
</tr>
</tbody>
</table>
Network Device Properties

Network devices are packet-oriented devices capable of sending and receiving Ethernet packets. Network devices are typically used for booting.

<table>
<thead>
<tr>
<th>Property</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>mac-address</td>
<td>Last used network address.</td>
</tr>
<tr>
<td>address-bits</td>
<td>Number of address bits needed to address this device on the physical layer.</td>
</tr>
<tr>
<td>max-frame-size</td>
<td>Maximum packet size that the device can transmit at one time.</td>
</tr>
</tbody>
</table>

Memory Device Properties

Memory devices are traditional random-access memory, suitable for temporary storage of data.

<table>
<thead>
<tr>
<th>Property</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>reg</td>
<td>Physical addresses installed in the system.</td>
</tr>
<tr>
<td>available</td>
<td>Regions of physical addresses that are currently unallocated by OpenBoot.</td>
</tr>
</tbody>
</table>

General Properties For Parent Nodes

<table>
<thead>
<tr>
<th>Property</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>#address-cells</td>
<td>Device node’s address format.</td>
</tr>
<tr>
<td>#size-cells</td>
<td>Number of cells that are used to encode the size field of a child’s reg property.</td>
</tr>
<tr>
<td>ranges</td>
<td>Relationship between the physical address spaces of the parent and child nodes.</td>
</tr>
</tbody>
</table>

Detailed Descriptions of Standard Properties

#address-cells

Applies only to bus nodes. It specifies the number of cells that are used to represent a physical address with a bus’ address space.

#size-cells

Applies only to bus nodes. It specifies the number of cells used to represent the length of a physical address range (i.e. the size field of a child’s reg property).
address

Declares currently-mapped device virtual addresses. It is generally used to declare large regions of existing mappings, in order to enable the operating system device driver to re-use those mappings, thus conserving system resources. This property should be created after virtual addresses have been assigned by mapping operations. Should be deleted when the corresponding virtual addresses are unmapped.

The property value is an arbitrary number of virtual addresses. The correspondence between declared addresses and the set of mappable regions of a particular device is device-dependent.

```
-l value my-buffers
-l value my-dma-addr
: map-me ( -- )
  my-address my-space 1.0000 " map-in" $call-parent ( virt1 )
  to my-buffers
  2000 " dma-alloc" $call-parent ( virt2 ) to my-dma-addr
  my-buffers encode-int my-dma-addr encode-int encode+ 
  " address" property
;
: unmap-me ( -- )
  my-dma-addr 2000 " dma-free" $call-parent
  my-buffers 1.0000 " map-out" $call-parent
  " address" delete-property
;
```

See also: free-virtual, property

address-bits

When declared in “network” devices, indicates the number of address bits needed to address this device on its network. Used as:

```
d# 48 encode-int " address-bits" property
```

See also: property and Chapter 9, “Network Devices”.

available

Defines the resources that are managed by this package (i.e. /memory or /mmu) that are currently available for use by a client program.

The property value is an arbitrary number of (phys-addr, length) pairs where:
• **phys-addr** is a `phys.lo phys.mid phys.hi` list of integers encoded with `encode-int`.

• **length** (whose format depends on the package) is one or more integers, each encoded with `encode-int`.

### character-set
When declared in "display" or "serial" devices, indicates the recognized character set for this device. The property value is a text string.
A typical value is "ISO8859-1". 8859-1 is the number of the ISO specification for that particular character set, which essentially covers the full range of western European languages. To get a list of possible values, consult the X registry for which there is an address in the X11R5 documentation.

Used as:

```
" ISO8859-1" encode-string " character-set" property
```

See also: property, Chapter 7, “Display Devices” and Chapter 10, “Serial Devices”.

### depth
Associated with display devices. Encoded with `encode-int`, the property value specifies the number of bits in each pixel of the display.

### device_type
Declares the type of this plug-in device. The type need not be declared, unless this device is intended to be used for booting. If this property is declared, using one of the following key values, the FCode program must follow the required conventions for that particular type of device, by implementing a specified set of properties and procedures (methods). Used as:

```
" display" encode-string " device_type" property
```
Defined values for this property are:

See also: device-type, property

height

Associated with display devices. Encoded with encode-int, the property value specifies the number of displayable pixels in the “y” direction of the display.

This optional property declares the interrupt level(s) for this plug-in device. The contents are one or more integers. Note that the bus-level interrupt (not the CPU-level interrupt) is specified.

For SBus devices, SBus interrupt levels 1-7 are allowed. The correct choice for your interrupt level will depend on your latency requirements. Typical usage is: video - SBus level 5, Ethernet - SBus level 4, SCSI and DMA - SBus level 3. SBus levels 6 and 7 should only be used with great care, otherwise significant system performance degradation may occur.

Because of previous usage of the intr property instead of the interrupts property in earlier systems, we recommend that both intr and interrupts be declared in FCode for SBus cards. However, cards which only declare intr should continue to work, as current systems automatically generate the interrupts property for you as required.

Table 5-1  Standard Device Types

<table>
<thead>
<tr>
<th>Device Type</th>
<th>Device Characteristics</th>
</tr>
</thead>
<tbody>
<tr>
<td>block</td>
<td>Random-access, block-oriented device, such as a disk drive, usable as a boot file source. See Chapter 6, “Block and Byte Devices” for the requirements of this type of device.</td>
</tr>
<tr>
<td>byte</td>
<td>Random-access, byte-oriented device, such as a tape drive, usable as a boot file source. See Chapter 6, “Block and Byte Devices” for the requirements of this type of device.</td>
</tr>
<tr>
<td>display</td>
<td>Frame buffer or other similar display device, usable for message display during booting. See Chapter 7, “Display Devices” for the requirements of this type of device.</td>
</tr>
<tr>
<td>memory</td>
<td>Random-access memory device. See IEEE Standard 1275-1994 for the requirements of this type of device.</td>
</tr>
<tr>
<td>network</td>
<td>Packet-oriented network device, such as Ethernet, can be used as a boot file source. See Chapter 9, “Network Devices” for the requirements of this type of device.</td>
</tr>
<tr>
<td>serial</td>
<td>Byte-oriented device, such as a serial port, usable for console input and/or console output. See Chapter 10, “Serial Devices” for the requirements of this type of device.</td>
</tr>
</tbody>
</table>
To declare a single interrupt (level 5), used as:

```plaintext
5 encode-int "interrupts" property
5 0 intr
```

To declare two interrupts (levels 3 and 5), used as:

```plaintext
5 encode-int 3 encode-int encode+ "interrupts" property
3 sbus-intr>cpu encode-int \ Interrupt#1
 0 encode-int encode+ \ Null vector#1
5 sbus-intr>cpu encode-int encode+ \ Interrupt#2
 0 encode-int encode+ \ Null vector#2
"intr" property
```

See also: intr, interrupts, property

### linebytes

Associated with display devices. Encoded with `encode-int`, the property value specifies the number of pixels between consecutive scan lines of the display.

### local-mac-address

Used with devices whose `device_type` is `network`, this should be present only if the device has a built-in, 48-bit, IEEE 802.3-style Media Access Control (MAC) address. The system may or may not use this address in order to access this device. Encoded with `encode-bytes`.

See also: `mac-address`, "mac-address", `property`, and Chapter 9, “Network Devices”.

---

**Properties**
mac-address

Must be created by the open method of “network” devices to indicate the Media Access Control (MAC) address that this device is currently using. This value may or may not be the same as any local-mac-address property. This property is typically used by client programs that determine which network address was used by the network interface from which the client program was loaded.

The property value is the six-byte MAC address encoded with encode-byte. Here’s how it is made up:

1. If a plug-in device has an assigned MAC address from the factory, this address is published as the value for local-mac-address.

2. The system (based on various factors such as presence or absence of local-mac-address and/or the value of the NVRAM parameter local-mac-address?) determines the address for the plug-in device to use. The value returned by the mac-address FCode is set to this address.

3. The plug-in device then reports the address it is using by publishing the mac-address property.

For example:
For a well-behaved plug-in “network” device (which has a factory-unique MAC address but can use another system-supplied MAC address if desired by the system), the FCode would appear as:

```
create mac-address 8 c, 0 c, 20 c, 0 c, 14 c, 5e c,
mac-address              encode-bytes  " mac-address"        property
(plus code to "assign" the correct mac-address value into registers)
```

See also: mac-address, "local-mac-address", property and Chapter 9, “Network Devices”.
max-frame-size

When declared in “network” devices, indicates the maximum packet size (in bytes) that the physical layer of the device can transmit. This property can be used by client programs to allocate buffers of the appropriate length.

Usage:

```
4000 encode-int " max-frame-size" property
```

See also: property and Chapter 9, “Network Devices”.

model

Identifies the model name and number (including revision) for a device, for manufacturing and field-service purposes.

The `model` property is useful to identify the specific piece of hardware (the plug-in card), as opposed to the `name` property (since several different but functionally-equivalent cards would have the same `name` property, thus calling the same device driver). Although the `model` property is good to have in general, it generally does not have any other specific purpose.

The property value format is arbitrary, but conventional usage is to begin the string with the manufacturer’s name (as with the `name` property) and to end it with the revision level.

Usage:

```
" SUNW,501-1415-1" encode-string "model" property
```

See also: "name", model, property
name

Specifies the manufacturer’s name and device name. All device nodes must publish this property. The name property can be used to match a particular operating system device driver with the device.

The property value is an arbitrary string. Any combination of one to 31 printable characters is allowed, except for "@", "::" or "//". The string may contain one comma, at most. Embedded spaces are not allowed.

IEEE Standard 1275-1994 specifies three different formats for the manufacturer’s name portion of the property value where two of those formats are strongly preferred.

For United States companies that have publicly listed stock, the most practical form of name is to use the company’s stock symbol (e.g. SUNW for Sun Microsystems, Inc.). This option is also available to any company in the world provided that there is no duplication of the company’s stock symbol on either the New York Stock Exchange or the NASDAQ exchange. If a non-U.S. company’s stock is traded as an American Depository Receipt (ADR), the ADR symbol satisfies this requirement. A prime advantage of this form of manufacturer’s name is that such stock symbols are very human-readable.

Alternatively, a company may obtain an organizationally unique identifier (OUI) from the IEEE Registration Authority Committee. This is a 24-bit number that is guaranteed to be unique world-wide. Companies that have obtained an OUI would use a sequence of hexadecimal digits of the form “0NNNNNNN” for the manufacturer’s name portion of the property. This form of name has the disadvantage that the manufacturer is not easily recognizable.

Each manufacturer may devise its own format for the device name portion of the property value.

Here is an example usage:

```
" SUNW,bison-printer" encode-string " name" property
```

You may also use the name command to create this property.

See also: name, property, device-name.

ranges

The ranges property is a list of child-to-parent physical address correspondences required for most hierarchical devices.
ranges is a property for bus devices, particularly those buses whose children can be accessed with CPU load and store operations (as opposed to buses like SCSI, whose children are accessed with a command protocol).

The ranges property value describes the correspondence between the part of the physical address space of the bus node’s parent available for use by the bus node (the parent address space), and the physical address space defined by the bus node for its children (the child address space).

The ranges property value is a sequence of

```
child-phys parent-phys size
```

specifications.

- `child-phys` is a starting address in the child physical address space defined by the bus node
- `parent-phys` is a starting address in the physical address space of the parent of the bus node
- `size` is the length in bytes of the address range.

The specification means that there is a one-to-one correspondence between the child addresses and the parent addresses in that range. The parent addresses given are always relative to the parent’s address space.

Each starting address is represented using the physical address representation as two 32-bit numbers (one for `space` and one for `offset`). `size` is encoded as an unsigned integer.

The total size of each such specification is five 32-bit numbers (two for each of the two addresses, plus one for the size). Successive specifications are encoded sequentially. A space with length $2^n$ (number of bits in a machine word) is represented with a size of 0.

The specifications should be sorted in ascending order of the child address. The address ranges thus described need not be contiguous in either the child space or the parent space. Also, the entire child space must be described in terms of parent addresses, but not all of the parent address space available to the bus device need be used for child addresses (the bus device might reserve some addresses for its own purposes, for instance).

For example, suppose that a 4-slot 25-bit SBus is attached to a machine whose physical address space consists of a 32-bit “memory” space (space=0) and a 32-bit “io” space (space=1). The SBus slots appear in “io” space at address
0xf800.0000, 0xfa00.0000, 0xfc00.0000, and 0xfe00.0000. In terms of the SBus’s parent address space, the SBus device has available for its purposes the offsets from 0xf800.0000 through 0xffff.ffff in space 1 of its parent.

The SBus device defines for its children the spaces 0, 1, 2, and 3, all starting at offset 0 and running for 0x200.0000 bytes. In this case the SBus device uses all the address space given to it by its parent for the SBus children, and reserves none of the addresses for itself. The ranges property for the SBus device would contain the encoded form of the following sequence of numbers:

<table>
<thead>
<tr>
<th>Child Address</th>
<th>Parent Address</th>
<th>Size</th>
</tr>
</thead>
<tbody>
<tr>
<td>Space, Offset</td>
<td>Space, Offset</td>
<td></td>
</tr>
<tr>
<td>0, 0</td>
<td>1, f800.0000</td>
<td>200.0000</td>
</tr>
<tr>
<td>1, 0</td>
<td>1, fa00.0000</td>
<td>200.0000</td>
</tr>
<tr>
<td>2, 0</td>
<td>1, fc00.0000</td>
<td>200.0000</td>
</tr>
<tr>
<td>3, 0</td>
<td>1, fe00.0000</td>
<td>200.0000</td>
</tr>
</tbody>
</table>

Table 5-2 Child-Parent Address Relationships

Here the high components of the child address represent the SBus slot numbers, and the high component of the parent address represents “io space.”

If ranges exists but its value is of 0 length, the bus’s child address space is identical to its parent address space.

If the ranges property for a particular bus device node is nonexistent, code using that device should use an appropriate default interpretation. Some examples include the following:

- SBus node: Missing ranges means that the version of OpenBoot was created before the ranges property came into existence. Code should supply the correct ranges based on the machine type, from the finite set of machines that existed before ranges came into existence.
- Machine node: The machine node has no parent. Therefore the correspondence between its child and parent address spaces is meaningless, and there is no need for ranges.
- SCSI host adapter node: The child address space is not directly addressable, thus ranges would be meaningless.

The distinction between reg and ranges is as follows:
• **reg** is supposed to represent the actual device registers in the parent address space. For a bus adapter, this would be such as configuration/mode-initialization registers.
• **ranges** represents the correspondence between a bus adapter’s child and parent address spaces.

Most packages do not need to be concerned with **ranges**. These properties are mainly to communicate with stand-alone programs. One exception could be a bus extender or adaptor.

See also: Chapter 8, “Memory-Mapped Buses”.

---

**reg**

This property declares the location and size of onboard registers for its device. The FCode program for every plug-in SBus device **must** declare this property.

The contents are one or more (phys, size) pairs. Each pair specifies an addressable region of the device. An FCode PROM at location 0 of the device is generally **not** declared, except in the case where there are no other regions to declare.

For example, to declare two register fields at 10.0000-10.00ff and 20.0000-20.037f, use the following:

```plaintext
my-address 10.0000 +  my-space  encode-phys  \ Offset#1
  100 encode-int          encode+  \ Merge size#1
my-address 20.0000 +  my-space  encode-phys  encode+  \ Merge
offset#2
  380 encode-int          encode+  \ Merge size#2
" reg" property
```

In some cases, the **reg** command may also be used to create this property.

See also: **reg, property**.
status

Optional. Indicates the operational status of the device.
Absence of this property means that the operational status of the device is unknown or acceptable.
The property value is a string indicating the device status, as follows:

Table 5-3  status Property Values

<table>
<thead>
<tr>
<th>Status Value</th>
<th>Meaning</th>
</tr>
</thead>
<tbody>
<tr>
<td>okay</td>
<td>The device is believed to be operational.</td>
</tr>
<tr>
<td>disabled</td>
<td>The device represented by this node is not operational, but it might become operational in the future (e.g. an external switch is turned off, or something isn’t plugged in).</td>
</tr>
<tr>
<td>fail</td>
<td>The device represented by this node is not operational because a fault has been detected, and it is unlikely that the device will become operational without repair. No additional failure details are available.</td>
</tr>
<tr>
<td>fail-xxx</td>
<td>The device represented by this node is not operational because a fault has been detected, and it is unlikely that the device will become operational without repair. “xxx” is additional human-readable information about the detected fault condition.</td>
</tr>
</tbody>
</table>

Usage:

```
" disabled" encode-string " status" property
```

See also: property.

width

Associated with display devices. Encoded with encode-int, the property value specifies the number of displayable pixels in the “x” direction of the display.

Manipulating Properties

Property Creation and Modification

Use the FCode Function property to create new properties or modify values of existing properties.

There are some special property-publishing FCodes, designed for use in common situations:
• `reg` is a quick way to create a `reg` property that describes the location of the package’s physical resources.
• `model` is a quick way to create the `model` property.
• `device-name` is a quick way to create the `name` property.
• `delete-property` completely removes a property.

**Property Values**

Various kinds of information can be stored in a property value byte array by using property encoding and decoding methods. The encoding format is machine-independent. Property value representation is independent of the byte organization and word alignment characteristics of a processor.

A property’s data type must be recognized by any software that uses it. In other words, property value data types are not self-identifying. Furthermore, the presence or absence of a property with a particular name can encode a true/false flag; such a property may have a zero-length property value.

**Property Encoding**

There are three FCodes for encoding a basic piece of data into a property value and one for concatenating the basic pieces of data for a property with multiple values.

<table>
<thead>
<tr>
<th>encode-int</th>
<th>encodes a number</th>
</tr>
</thead>
<tbody>
<tr>
<td>encode-string</td>
<td>encodes a string</td>
</tr>
<tr>
<td>encode-bytes</td>
<td>encodes a sequence of bytes.</td>
</tr>
<tr>
<td>encode+</td>
<td>is used to concatenate two previously encoded, basic pieces of data.</td>
</tr>
<tr>
<td>encode-phys</td>
<td>is an FCode that encodes a physical address (hiding all the relative addressing information). <code>encode-phys</code> is derived from <code>encode-int</code> and <code>encode+</code>.</td>
</tr>
</tbody>
</table>

**Property Retrieval**

There are three property value retrieving words, `get-my-property`, `get-inherited-property`, and `get-package-property`.
• Use `get-my-property` if the property desired exists for the package being defined.
• Use `get-package-property` if the property exists in some other package. In this case, you must first find the phandle of the other package, perhaps by using `find-package`.

• Use `get-inherited-property` if the property exists somewhere in the chain of parent instances between the package being defined and the root node of the machine.

**Note** – Using `get-inherited-property` can be a bad idea because you don’t know who supplied the data.

FCode programs often do not retrieve property values. Such programs usually know the values of their own properties implicitly, and often interact with their parents by calling well-known parent methods.

For example, suppose a particular SBus FCode package calls DVMA to transfer data between a device and memory.

It could use `my-parent ihandle>phandle get-package-property` to find the value of a property named `slave-only`. `slave-only` will be a property of the parent node of the package being defined, if it exists.

The value of the property is a bit mask of the SBus slots that do *not* support DVMA. Then the package would look at `my-unit` or `my-space` to get its slot number. The two pieces of information will tell the package whether or not it can use DVMA.

**Property Decoding**

Once a package has found a property’s value, it must decode the value to forms it can recognize. If the value is the representation of an integer, use `decode-int` to generate the number as a binary number on the stack. If the value is the representation of a string, use `decode-string`. Both of these FCodes act as parsers — they will also return the unused portion of the value for further decoding.

Other kinds of values can be decoded by `left-parse-string` or package-specific decoders. Note that the package must be able to decode the value of a property.
There is no decode-bytes function, but it is easy to synthesize if you need it.

```
: decode-bytes ( addr1 len1 #bytes -- addr1 len2 addr1 #bytes )
tuck -       ( addr1 #bytes len2 )
>r 2dup +    ( addr1 #bytes addr2 ) ( R: len2 )
r> 2swap
;
```

**Property-Specific FCodes**

Following is a summary of property-specific FCodes. See the individual dictionary entries in Chapter 11, “FCode Dictionary”, for more information.

<table>
<thead>
<tr>
<th>Name</th>
<th>Stack Comment</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Property Creation</strong></td>
<td></td>
<td></td>
</tr>
<tr>
<td>property</td>
<td>( prop-addr prop-len name-addr name-len -- )</td>
<td>Create a property named name-addr name-len with the value prop-addr prop-len.</td>
</tr>
<tr>
<td>device-type</td>
<td>( addr len -- )</td>
<td>Shorthand word to create the device_type property with the value addr len.</td>
</tr>
<tr>
<td>model</td>
<td>( addr len -- )</td>
<td>Shorthand word to create the model property with the value addr len.</td>
</tr>
<tr>
<td>name</td>
<td>( addr len -- )</td>
<td>Shorthand macro to create the name property with the value addr len.</td>
</tr>
<tr>
<td>reg</td>
<td>( phys.lo ... phys.hi size -- )</td>
<td>Shorthand word to create the reg property.</td>
</tr>
<tr>
<td>device-name</td>
<td>( addr len -- )</td>
<td>Shorthand word to create the name property with the value addr len. Similar to name, but uses only one FCode instead of creating a macro.</td>
</tr>
<tr>
<td>delete-property</td>
<td>( name-addr name-len -- )</td>
<td>Delete the desired property.</td>
</tr>
<tr>
<td><strong>Property Encoding</strong></td>
<td></td>
<td></td>
</tr>
<tr>
<td>encode-int</td>
<td>( n -- prop-addr prop-len )</td>
<td>Converts an integer to a prop-encoded-array.</td>
</tr>
<tr>
<td>encode-phys</td>
<td>( phys.lo ... phys.hi -- prop-addr prop-len )</td>
<td>Converts a physical unit pair to a prop-encoded-array.</td>
</tr>
</tbody>
</table>
Table 5-4  Property-specific FCodes (Continued)

<table>
<thead>
<tr>
<th>Name</th>
<th>Stack Comment</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>encode-string</td>
<td>( addr len -- prop-addr prop-len )</td>
<td>Converts a text string to a prop-encoded-array.</td>
</tr>
<tr>
<td>encode+</td>
<td>( prop-addr1 prop-len1 prop-addr2 prop-len2 -- prop-addr prop-len1+2 )</td>
<td>Concatenate two prop-encoded-array structures. They must have been created sequentially.</td>
</tr>
<tr>
<td>encode-bytes</td>
<td>( addr len -- prop-addr prop-len )</td>
<td>Converts a byte array to a prop-encoded-array. Similar to encode-string, except no trailing null is appended.</td>
</tr>
<tr>
<td>Property Decoding</td>
<td></td>
<td></td>
</tr>
<tr>
<td>decode-int</td>
<td>( prop-addr prop-len -- prop-addr2 prop-len2 n )</td>
<td>Converts a prop-encoded-array string to an integer.</td>
</tr>
<tr>
<td>decode-string</td>
<td>( prop-addr prop-len -- prop-addr2 prop-len2 addr len )</td>
<td>Converts a prop-encoded-array string to a normal string.</td>
</tr>
<tr>
<td>Property Retrieval</td>
<td></td>
<td></td>
</tr>
<tr>
<td>get-my-property</td>
<td>( name-addr name-len -- true</td>
<td>prop-addr prop-len false )</td>
</tr>
<tr>
<td>get-package-property</td>
<td>( addr len phandle -- true</td>
<td>prop-addr prop-len false )</td>
</tr>
<tr>
<td>get-inherited-property</td>
<td>( addr len -- true</td>
<td>prop-addr prop-len false )</td>
</tr>
</tbody>
</table>
Block Devices

Block devices are nonvolatile mass storage devices whose information can be accessed in any order. Hard disks, floppy disks, and CD-ROMs are examples of block devices. OpenBoot typically uses block devices for booting.

This device type generally applies to disk devices, but as far as OpenBoot is concerned, it simply means that the device “looks like a disk” at the OpenBoot software interface level.

The block device’s FCode must declare the block device type, and implement the methods open and close, as well as the methods described in “Required Methods” on page 84.

Although packages of the block device type present a byte-oriented interface to the rest of the system, the associated hardware devices are usually block-oriented i.e. the device reads and writes data in blocks (groups of, for example, 512 or 2048 bytes). The standard /deblocker support package assists in the presentation of a byte-oriented interface overlaying a block-oriented interface, implementing a buffering layer that hides the underlying block length.

Block devices are often subdivided into several logical partitions, as defined by a disk label - a special block, usually the first one on the device, which contains information about the device. The driver is responsible for appropriately interpreting a disk label. The driver may use the standard /disk-label support package if the device does not implement a specialized label. The /disk-label support package interprets a system-dependent label format.
Since the disk booting protocol usually depends on the label format, the standard /disk-label support package also implements a load method for the corresponding boot protocol.

**Byte Devices**

Byte devices are sequential-access mass storage devices, for example tape devices. OpenBoot typically uses byte devices for booting.

The byte device’s FCode program must declare the byte device type, and implement the open and close methods in addition to those described in “Required Methods”.

Although packages of the byte device type present a byte-oriented interface to the rest of the system, the associated hardware devices are usually record-oriented; the device reads and writes data in records containing more than one byte. The records may be fixed or variable length. The standard /deblocker support package assists in presenting a byte-oriented interface overlaying a record-oriented interface, implementing a buffering layer that hides the underlying record structure.

**Required Methods**

**block-size** ( -- block-len )

Returns the record size block-len (in bytes) of all data transfers to or from the device. The most common value for block-len is 512.

This method is only required if the /deblocker support package is used.

**load** ( addr -- size )

load works differently for block and byte devices:

With block devices, it loads a stand-alone program from the device into memory at addr. size is the size in bytes of the program loaded. If the device can contain several such programs, the instance arguments returned by my-args can be used to select the program desired. open is executed before load is invoked.

With byte devices, load reads a stand-alone program from the tape file specified by the value of the argument string given by my-args. That value is the string representation of a decimal integer. If the argument string is null, tape file 0 is used. load places the program in memory at addr, returning the size of the read-in program in bytes.
**max-transfer** ( -- max-len )

Returns the size in bytes of the largest single transfer that the device can perform. `max-transfer` is expected to be a multiple of `block-size`. This method is only required if the /deblocker support package is used.

**read** ( addr len -- actual )

Read at most `len` bytes from the device into memory at `addr`. `actual` is the number of bytes read. If the number of bytes read is 0 or negative, the read failed. Note that `len` need not be a multiple of the device’s normal block size.

**read-blocks** ( addr block# #blocks -- #read )

Read `#blocks` records of length `block-size` bytes each from the device, starting at device block `block#`, into memory at address `addr`. `#read` is the number of blocks actually read.

This method is only required if the /deblocker support package is used.

**seek** ( poslow poshigh -- status ) for block; ( offset file# -- error? ) for byte

`seek` works differently depending on whether it’s being used with a block or byte device.

For block devices, `seek` sets the device position for the next read or write. The position is the byte offset from the beginning of the device specified by the 64-bit number which is the concatenation of `poshigh` and `poslow`. `status` is -1 if the seek fails, and 0 or 1 if it succeeds.

For byte devices, it seeks to the byte `offset` in file `file#`. If `offset` and `file#` are both 0, rewind the tape. `error?` is -1 if seek fails, and 0 if seek succeeds.

**write** ( addr len -- actual )

Write `len` bytes from memory at `addr` to the device. `actual` is the number of bytes written. If `actual` is less than `len`, the write did not succeed. `len` need not be a multiple of the device’s normal block size.

**write-blocks** ( addr block# #blocks -- #written )

Write `#blocks` records of length `block-size` bytes each to the device, starting at block `block#`, from memory at `addr`. `#written` is the number of blocks actually written.

If the device is not capable of random access (e.g. a sequential access tape device), `block#` is ignored.

This method is only required if the /deblocker support package is used.
Required Properties

<table>
<thead>
<tr>
<th>Property Name</th>
<th>Sample Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>name</td>
<td>&quot;SUNW,my-scsi&quot;</td>
</tr>
<tr>
<td>reg</td>
<td>list of registers (device-dependent)</td>
</tr>
<tr>
<td>device_type</td>
<td>block or byte</td>
</tr>
</tbody>
</table>

Device Driver Examples

The structure of the device tree for the sample card supported by the sample device drivers in this chapter is as follows:

```
  sbus
     ...
     ...
     SUNW,my-scsi
         sd
         st
```

Figure 6-1  Sample Device Tree
Simple Block Device Driver

**Code Example 1** Simple Block Device Driver

```plaintext
\ This is at a stage where each leaf node can be used only as a non-bootable device.
\ It only creates nodes and publishes necessary properties to identify the device.

hex
    " SUNW,my-scsi"  encode-string " name" property
    h# 20.0000  constant scsi-offset
    h# 40  constant /scsi
    my-address scsi-offset + my-space /scsi  reg
new-device \ missing "reg" indicates a SCSI "wild-card" node
    " sd"  encode-string " name" property
    finish-device
new-device \ missing "reg" indicates a SCSI "wild-card" node
    " st"  encode-string " name" property
    finish-device
end0
```

Extended Block Device Driver

**Code Example 2** Sample Driver for my-scsi Device

```plaintext
\ sample driver for "my-scsi" device.
\ It is still a non-bootable device. The purpose is to show how an intermediate stage
\ of driver can be used to debug board during development. In addition to publishing
\ the properties, this sample driver shows methods to access, test and control
\ "SUNW,my-scsi" device.

\ The following main methods are provided for "SUNW,my-scsi" device.
\ open   ( -- okay? )
\ close  ( -- )
\ reset  ( -- )
\ selftest ( -- error? )

hex
headers

h# 20.0000  constant scsi-offset
h# 40  constant /scsi
d# 25.000.000 constant clock-frequency
```
\ sample driver for "my-scsi" device.

: identify-me ( -- )
  " SUNW,my-scsi" encode-string " name" property
  " scsi" device-type

  my-address scsi-offset + my-space /scsi reg
;
identify-me

h# 10.0000 constant dma-offset
h# 10 constant /dma
-1 instance value dma-chip

\ methods to access/control dma registers
: dmaaddress ( -- addr ) dma-chip 4 + ;
: dmacount ( -- addr ) dma-chip 8 + ;
: dmaaddr@ ( -- n ) dmaaddress rl0 ;
: dmaadr! ( n -- ) dmaaddress rl! ;
: dmacount@ ( -- n ) dmacount rl0 ;
: dmacount! ( n -- ) dmacount rl! ;
: dma-chip@ ( -- n ) dma-chip rl0 ;
: dma-chip! ( n -- ) dma-chip rl! ;
: dma-btest ( mask -- flag ) dma-chip@ and ;
: dma-bset ( mask -- ) dma-chip@ or dma-chip! ;
: dma-breset ( mask -- ) not dma-btest dma-chip! ;

external

\ methods to allocate, map, unmap, free dma buffers
: decode-unit ( addr len -- low high ) decode-2int ;
: dma-alloc ( size -- vaddr ) " dma-alloc" $call-parent ;
: dma-free ( vaddr size -- ) " dma-free" $call-parent ;
: dma-map-in ( vaddr size cache? -- devaddr ) " dma-map-in" $call-parent ;
: dma-map-out ( vaddr devaddr size -- ) " dma-map-out" $call-parent ;

\ DMA-sync could be a dummy routine if the parent device doesn't support.
: dma-sync ( virt-addr dev-addr size -- )
  " dma-sync" my-parent ['] $call-method catch if
  2drop 2drop 2drop
  then
;
: map-in ( addr space size -- virt ) " map-in" $call-parent ;
```plaintext
: map-out ( virt size -- ) "map-out" $call-parent;

headers
: dma-open ( -- )
  my-address dma-offset + my-space /dma map-in to dma-chip;
: dma-close ( -- ) dma-chip /dma map-out -1 to dma-chip;

-1 instance value scsi-init-id
-1 instance value scsi-chip
h# 20 constant /mbuf
-1 instance value mbuf
-1 instance value mbuf-dma
d# 6 constant /sense
-1 instance value sense-command
-1 instance value sense-cmd-dma
d# 8 constant #sense-bytes
-1 instance value sense-buf
-1 instance value sense-buf-dma
-1 instance value mbuf0
d# 12 constant /cmdbuf
-1 instance value cmdbuf
-1 instance value cmdbuf-dma
-1 instance value scsi-statbuf

\ mapping and allocation routines for scsi
: map-scsi-chip ( -- )
  my-address scsi-offset + my-space /scsi map-in to scsi-chip;
: unmap-scsi-chip ( -- ) scsi-chip /scsi map-out -1 to scsi-chip;

\ After any changes to sense-command by CPU or any changes to sense-cmd-dma by
\ device, synchronize changes by issuing "sense-command sense-cmd-dma /sense
\ dma-sync" Similarly after any changes to sense-buf, sense-buf-dma, mbuf,
\ mbuf-dma, cmdbuf or cmdbuf-dma, synchronize changes by appropriately issuing
\ dma-sync map scsi chip and allocate buffers for "sense" command and status
: map-scsi ( -- )
  map-scsi-chip
  /sense dma-alloc to sense-command
  sense-command /sense false dma-map-in to sense-cmd-dma
  #sense-bytes dma-alloc to sense-buf
  sense-buf #sense-bytes false dma-map-in to sense-buf-dma
```
\ sample driver for "my-scsi" device.
\  2 alloc-mem to scsi-statbuf
;

\ free buffers for "sense" command and status and unmap scsi chip
: unmmap-scsi ( -- )
  scsi-statbuf 2 free-mem
  sense-buf sense-buf-dma #sense-bytes dma-sync \ redundant
  sense-buf sense-buf-dma #sense-bytes dma-map-out
  sense-buf #sense-bytes dma-free
  sense-command sense-cmd-dma /sense dma-sync \ redundant
  sense-command sense-cmd-dma /sense dma-map-out
  sense-command /sense dma-free
-1 to sense-command
-1 to sense-cmd-dma
-1 to sense-buf
-1 to scsi-statbuf
-1 to sense-buf-dma
  unmmap-scsi-chip
;

\ constants related to scsi commands
h#  0 constant nop
h#  1 constant flush-fifo
h#  2 constant reset-chip
h#  3 constant reset-scsi
h#  80 constant dma-nop

\ words to get scsi register addresses.
\ Each chip register is one byte, aligned on a 4-byte boundary.
: scsi+ ( offset -- addr ) scsi-chip + ;
: transfer-count-lo ( -- addr ) h#  0 scsi+ ;
: transfer-count-hi ( -- addr ) h#  4 scsi+ ;
: fifo ( -- addr ) h#  8 scsi+ ;
: command ( -- addr ) h# c scsi+ ;
: configuration ( -- addr ) h# 20 scsi+ ;
: scsi-test-reg ( -- addr ) h# 28 scsi+ ;

\ Read only registers:
: scsi-status ( -- addr ) h# 10 scsi+ ;
: interrupt-status ( -- addr ) h# 14 scsi+ ;
: sequence-step ( -- addr ) h# 18 scsi+ ;
Code Example 2  Sample Driver for my-scsi Device (Continued)

\ sample driver for "my-scsi" device.
: fifo-flags  ( -- addr ) h# 1c scsi+ ;

\ Write only registers:
: select/reconnect-bus-id  ( -- addr ) h# 10 scsi+ ;
: select/reconnect-timeout ( -- addr ) h# 14 scsi+ ;
: sync-period            ( -- addr ) h# 18 scsi+ ;
: sync-offset            ( -- addr ) h# 1c scsi+ ;
: clock-conversion-factor ( -- addr ) h# 24 scsi+ ;

\ words to read from/store to scsi registers.
: cnt@      ( -- w )  transfer-count-lo rb@  transfer-count-hi rb@  bwjoin ;
: fifo@     ( -- c )  fifo rb@ ;
: cmd@      ( -- c )  command rb@ ;
: stat@     ( -- c )  scsi-status rb@ ;
: istat@    ( -- c )  interrupt-status rb@ ;
: fifo-cnt  ( -- c )  fifo-flags rb@  h# 1f and ;
: data@     ( -- c )  begin fifo-cnt until fifo@ ;
: seq@      ( -- c )  sequence-step rb@  h# 7 and ;

: fifo!  ( c -- )  fifo rb! ;
: cmd!   ( c -- )  command rb! ;
: cnt!   ( w -- )  wbsplit transfer-count-hi rb! transfer-count-lo rb! ;
: targ!  ( c -- )  select/reconnect-bus-id rb! ;
: data!  ( c -- )  begin fifo-cnt d# 16 <> until fifo! ;

\ scsi chip noop and initialization
: scsi-nop   ( -- )  nop cmd! ;
: init-scsi  ( -- )  reset-chip cmd!  scsi-nop ;

: wait-istat-clear ( -- error? )
d# 1000
begin
  1 ms 1-  ( count )
  dup 0=   ( count expired? )
  istat@  ( count expired? istat )
  0= or    ( count clear? )
  until    ( count )
  0= if
  istat@ 0<> if
  cr ." Can't clear ESP interrupts: "
  ." Check SCSI Term. Power Fuse." cr
  true  exit

Block and Byte Devices
\ sample driver for "my-scsi" device.
  
  then
  then
  false
  
  ;

  : clk-conv-factor ( -- n ) clock-frequency d# 5.000.000 / 7 and ;

\ initialize scsi chip, tune time amount, set async operation mode, and set scsi
\ bus id
  : reset-my-scsi ( -- error? )
    init-scsi
    h# 93 select/reconnect-timeout rb!
    0 sync-offset rb!
    clk-conv-factor clock-conversion-factor rb!
    h# 4 scsi-init-id 7 and or configuration rb!
    wait-istat-clear
  ;

  : reset-bus ( -- error? )
    reset-scsi cmd! wait-istat-clear
  ;

  : init-n-test ( -- ok? ) reset-my-scsi 0= ;

  : get-buffers ( -- )
    h# 8000 dma-alloc to mbuf0
    /cmdbuf dma-alloc to cmdbuf
    cmdbuf /cmdbuf false dma-map-in to cmdbuf-dma
  ;

  : give-buffers ( -- )
    mbuf0 h# 8000 dma-free -1 to mbuf0
    cmdbuf cmdbuf-dma /cmdbuf dma-sync \ redundant
    cmdbuf cmdbuf-dma /cmdbuf dma-map-out
    cmdbuf /cmdbuf dma-free
    -1 to cmdbuf -1 to cmdbuf-dma
  ;

  : scsi-selftest ( -- fail? ) reset-my-scsi ;

\ dma-alloc and dma-map-in mbuf-dma
Sample Driver for my-scsi Device (Continued)

\ sample driver for "my-scsi" device.
: mbuf-alloc ( -- )
  /mbuf dma-alloc to mbuf
  mbuf /mbuf false dma-map-in to mbuf-dma
;

\ dma-map-out and dma-free mbuf-dma
: mbuf-free ( -- )
  mbuf mbuf-dma /mbuf dma-sync \ redundant
  mbuf mbuf-dma /mbuf dma-map-out
  mbuf /mbuf dma-free
  -1 to mbuf
  -1 to mbuf-dma
;

external
\ If any routine was using buffers allocated by dma-alloc, and was using dma mapped
\ by dma-map-in, it would have to dma-sync those buffers after making any changes to
\ them.
: open ( -- success? )
  dma-open
  " scsi-initiator-id" get-inherited-property 0= if
    decode-int to scsi-init-id
    2drop
    map-scsi
    init-n-test ( ok? )
    dup if ( true )
      get-buffers ( true )
      map-scsi
    else
      unmap-scsi dma-close ( false )
      then ( success? )
      else
        ." Missing initiator id" cr false
dma-close
      then ( success? )
    else
      ;
Code Example 2  Sample Driver for my-scsi Device (Continued)

\ sample driver for "my-scsi" device.

: close   \ --
   give-buffers unmap-scsi dma-close
;

: reset   \ --
   dma-open map-scsi
   h# 80 dma-breset
   reset-my-scsi drop reset-bus drop
   unmap-scsi dma-close
;
\ if scsi-selftest was actually using buffers allocated by mbuf-alloc, it would
\ have to do dma-sync after any changes to mbuf or mbuf-dma.

: selftest   \ -- fail?
   map-scsi
   mbuf-alloc
   scsi-selftest
   mbuf-free
   unmap-scsi
;

new-device \ missing "reg" indicates a SCSI "wild-card" node
     " sd" encode-string " name" property
finish-device

new-device \ missing "reg" indicates a SCSI "wild-card" node
     " st" encode-string " name" property
finish-device

end0
Complete Block and Byte Device Driver

Sample Driver for Bootable Devices

\ sample fcode driver for bootable devices.
\ This driver supports "block" and "byte" type bootable devices, by using standard
\ "deblocker"and "disk-label" packages.

hex
headers
: copyright ( -- )
  " Copyright 1992 - 1995 Sun Microsystems. All Rights Reserved" cr
;
h# 20.0000 constant scsi-offset
h# 40 constant /scsi
d# 25.000.000 constant clock-frequency

: identify-me ( -- )
  " SUNW,my-scsi" encode-string " name" property
  " scsi" device-type
  my-address scsi-offset + my-space /scsi reg

;identify-me

h# 10.0000 constant dma-offset
h# 10 constant /dma
-1 instance value dma-chip

external
: decode-unit ( addr len -- low high ) decode-2int ;
: dma-alloc ( size -- vaddr ) " dma-alloc" $call-parent ;
: dma-free ( vaddr size -- ) " dma-free" $call-parent ;
: dma-map-in ( vaddr size cache? -- devaddr ) " dma-map-in" $call-parent ;
: dma-map-out ( vaddr devaddr size -- ) " dma-map-out" $call-parent ;

\ Dma-sync could be dummy routine if parent device doesn't support.
: dma-sync ( virt-addr dev-addr size -- )
  " dma-sync" my-parent ['] $call-method catch if
    2drop 2drop 2drop
  then
;

: map-in ( addr space size -- virt ) " map-in" $call-parent ;
: map-out ( virt size -- ) " map-out" $call-parent ;
headers
\ variables/values for sending commands, mapping etc.
-1 instance value scsi-init-id
-1 instance value scsi-chip
-1 instance value mbuf
-1 instance value mbuf-dma
h# 20 constant /mbuf
...

\ mapping and allocation routines for scsi
: map-scsi-chip ( -- )
  my-address scsi-offset + my-space /scsi map-in to scsi-chip

: unmap-scsi-chip ( -- ) scsi-chip /scsi map-out -1 to scsi-chip ;

: map-scsi ( -- )
  map-scsi-chip
  \ allocate buffers etc. for "sense" command and status
  ...

: unmap-scsi ( -- )
  \ free buffers etc. for "sense" command and status
  ...
  unmap-scsi-chip

\ words related to scsi commands and register access.
...

: reset-my-scsi ( -- error? ) ... ;
: reset-bus ( -- error? ) ... ;

: init-n-test ( -- ok? ) ... ;
: get-buffers ( -- ) ... ;
: give-buffers ( -- ) ... ;
: scsi-selftest ( -- fail? ) ... ;


\ sample fcode driver for bootable devices.

\ d# 512 constant ublock
\ 0 instance value /block
\ 0 instance value /tapeblock
\ instance variable fixed-len?
\ ...
\ external
\ : set-timeout ( n -- ) ... ;
\ : send-diagnostic ( -- error? )
\ \ run diagnostics and return any error.
\ ... ;
\ : device-present? ( lun target -- present? ) ... ;
\ : mode-sense ( -- true | block-size false ) ... ;
\ : read-capacity ( -- true | block-size false ) ... ;
\ \ Spin up a SCSI disk, coping with a possible wedged SCSI bus
\ : timed-spin ( target lun -- ) ... ;
\ : disk-r/w-blocks ( addr block# #blks direction? -- #xfered )
\ ... \ ( #xfered ) ;
\ \ Execute "mode-sense" command. If failed, execute read-capacity command.
\ \ If this also failed, return d# 512 as the block size.
\ : disk-block-size ( -- n )
\ \ mode-sense if read-capacity if d# 512 then then dup to /block
\ ;
\ : tape-block-size ( -- n ) ... ;
\ : fixed-or-variable ( -- max-block fixed? ) ... ;
\ : tape-r/w-some ( addr block# #blks read? -- actual# error? ) ... ;
\ headers
\ : dma-open ( -- ) my-address dma-offset + my-space /dma map-in to dma-chip ;
\ : dma-close ( -- ) dma-chip /dma map-out -1 to dma-chip ;
Code Example 3  Sample Driver for Bootable Devices  (Continued)

\ sample fcode driver for bootable devices.
\ After any changes to mbuf by cpu or any changes to mbuf-dma by device, synchronize
\ changes by issuing " mbuf mbuf-dma /mbuf dma-sync "
: mbuf-alloc ( -- )
  /mbuf dma-alloc to mbuf
  mbuf /mbuf false dma-map-in to mbuf-dma
;

\ dma-map-out and dma-free mbuf-dma
: mbuf-free ( -- )
  mbuf mbuf-dma /mbuf dma-sync \ redundant
  mbuf mbuf-dma /mbuf dma-map-out
  mbuf /mbuf free
  -1 to mbuf
  -1 to mbuf-dma
;

external

\ external methods for scsi bus ( "SUNW,my-scsi" node)
: open ( -- okay? )
  dma-open
  " scsi-initiator-id" get-inherited-property 0= if
    decode-int to scsi-init-id
    2drop
    map-scsi
    init-n-test ( ok? )
    dup if ( true )
      get-buffers ( true )
    else
      unmmap-scsi dma-close ( false )
      then ( success? )
    else
      " Missing initiator id" cr false
      dma-close
      then ( success? )
  ;
  : close ( -- ) give-buffers unmmap-scsi dma-close ;

: reset ( -- )
  dma-open map-scsi
  ...
  reset-my-scsi drop reset-bus drop
\ sample fcode driver for bootable devices.
   unmap-scsi dma-close
;

: selftest ( -- fail? )
   map-scsi
   mbuf-alloc
   scsi-selftest
   mbuf-free
   unmap-scsi
;

headers

\ start of child block device

new-device \ missing "reg" indicates SCSI "wild-card" node

   " sd" encode-string " name" property
   " block" device-type

   0 instance value offset-low
   0 instance value offset-high
   0 instance value label-package

\ The "disk-label" package interprets any partition information contained in
\ the disk label. The "load" method of the "block" device uses the load method
\ provided by "disk-label"

: init-label-package ( -- okay? )
   0 to offset-high  0 to offset-low
   my-args " disk-label" $open-package to label-package
   label-package if
      0 0 " offset" label-package $call-method
      to offset-high to offset-low
      true
   else
      " Can't open disk label package" cr false
   then

;

0 instance value deblocker

: init-deblocker ( -- okay? )
   " " deblocker" $open-package to deblocker
   deblocker if
\sample fcode driver for bootable devices.
  \true
  \else
    \"Can't open deblocker package" \or \false
  then
;

: device-present? \( lun \ target \ -- \ present? \)  
  \"device-present?" \$call-parent
;
\The following methods are needed for "block" device:\open, close, selftest, reset, read, write, load, seek, block-size, \max-transfer,read-blocks, write-blocks.\Carefully notice the relationship between the methods for the "block" device \and the methods pre-defined for "disk-label" and "deblocker"

external
\\external methods for "block" device \"sd" node

: spin-up \( \-- \) \my-unit \"timed-spin\" \$call-parent
;

: open \( \-- \ ok? \)
  \my-unit device-present? \0= \if \false \exit \then
  \spin-up \ \\Start the disk if necessary
    \init-deblocker \0= \if \false \exit \then
    \init-label-package \0= \if
      \deblocker close-package \false \exit
      \then
        \true
    \true
;

: close \( \-- \)
  \label-package close-package \0\to \label-package
  \deblocker close-package \0\to \deblocker
;

: selftest \( \-- \ fail? \)
  \my-unit device-present? \if
    \"send-diagnostic\" \$call-parent \( \fail? \)
  \else
    \true \( \error \)
  \then
;
The "deblocker" package assists in the implementation of byte-oriented read and write methods for disks and tapes. The deblocker provides a layer of buffering to implement a high level byte-oriented interface "on top of" a low-level block-oriented interface.

The "seek", "read" and "write" methods of this block device use corresponding methods provided by "deblocker"

In order to be able to use the "deblocker" package this device has to define the following four methods, which the deblocker uses as its low-level interface to the device:

1) block-size, 2) max-transfer, 3) read-blocks and 4) write-blocks

\sample fcode driver for bootable devices.
: reset ( -- ) ... ;

\ The "deblocker" package assists in the implementation of byte-oriented read and write methods for disks and tapes. The deblocker provides a layer of buffering to implement a high level byte-oriented interface "on top of" a low-level block-oriented interface.

\ The "seek", "read" and "write" methods of this block device use corresponding methods provided by "deblocker"

\ In order to be able to use the "deblocker" package this device has to define the following four methods, which the deblocker uses as its low-level interface to the device:
\ 1) block-size, 2) max-transfer, 3) read-blocks and 4) write-blocks

: block-size ( -- n ) " disk-block-size" $call-parent ;
: max-transfer ( -- n ) block-size h# 40 * ;

: read-blocks ( addr block# #blocks -- #read )
  true " disk-r/w-blocks" $call-parent;

: write-blocks ( addr block# #blocks -- #written )
  false " disk-r/w-blocks" $call-parent;

: dma.alloc ( #bytes -- vadr ) " dma-alloc" $call-parent ;
: dma-free ( vadr #bytes -- ) " dma-free" $call-parent ;
: seek ( offset.low offset.high -- okay? )
  offset-low offset-high x+ " seek" deblocker $call-method;

: read ( addr len -- actual-len ) " read" deblocker $call-method;
: write ( addr len -- actual-len ) " write" deblocker $call-method;
: load ( addr -- size ) " load" label-package $call-method;

finish-device \ finishing "block" device "sd"

headers

\ start of child byte device

new-device \ missing "reg" indicates "wild-card" node
" st" encode-string " name" property
The following methods are needed for "byte" devices: open, close, selftest, reset, read, write, load, seek, block-size, max-transfer, read-blocks, write-blocks. Carefully notice the relationship between the methods for "byte" devices and the methods pre-defined for the standard deblocker package.

external
\ external methods for "byte" device ( "st" node)
The "deblocker" package assists in the implementation of byte-oriented read and write methods for disks and tapes. The deblocker provides a layer of buffering to implement a high level byte-oriented interface "on top of" a low-level block-oriented interface.

The "read" and "write" methods of this "byte" device use the corresponding methods provided by the "deblocker".

In order to be able to use the "deblocker" package this device has to define the following four methods which the deblocker uses as its low-level interface to the device:
1) block-size, 2) max-transfer, 3) read-blocks and 4) write-blocks

```
: block-size ( -- n ) "tape-block-size" $call-parent ;

: max-transfer ( -- n )
  fixed-or-variable ( max-block fixed? )
  if
    \ Use the largest multiple of /tapeblock that to <= h# fe00
    h# fe00 over */
  then
;

: read-blocks ( addr block# #blocks -- #read )
  file-mark? @ 0= if
    true "tape-r/w-some" $call-parent file-mark? ! ( #read )
  else
    3drop 0
  then
;

: write-blocks ( addr block# #blocks -- #written )
  false "tape-r/w-some" $call-parent file-mark? !
;

: dma-alloc ( #bytes -- vaddr ) "dma-alloc" $call-parent ;

: dma-free ( vaddr #bytes -- ) "dma-free" $call-parent ;

: open ( -- okay? ) \ open for tape
  my-unit device-present? 0= if false exit then
  scsi-tape-first-install if
  scsi-tape-rewind if
```

---

\ sample fcode driver for bootable devices.

\ The "deblocker" package assists in the implementation of byte-oriented read and write methods for disks and tapes. The deblocker provides a layer of buffering to implement a high level byte-oriented interface "on top of" a low-level block-oriented interface.

\ The "read" and "write" methods of this "byte" device use the corresponding methods provided by the "deblocker".

\ In order to be able to use the "deblocker" package this device has to define the following four methods which the deblocker uses as its low-level interface to the device:
1) block-size, 2) max-transfer, 3) read-blocks and 4) write-blocks

```
Code Example 3  Sample Driver for Bootable Devices  (Continued)

\ sample fcode driver for bootable devices.
   ." Can't rewind tape" cr
   0= if drop then
    false exit
    then
    false to scsi-tape-first-install
   then
   \ Set fixed-len? and /tapeblock
   fixed-or-variable 2drop
   init-deblocker 0= if false exit then
   true
   
   ;

   : close ( -- )
   deblocker close-package 0 to deblocker
   write-eof-mark? if
   write-eof if
   ." Can't write EOF Marker."
   0= if drop then
   then
   then
   ;

   : reset ( -- ) ... ;
   : selftest ( -- fail? )
   my-unit device-present? if
   " send-diagnostics" $call-parent ( fail? )
   else
   true                             ( error )
   then
   ;

   : read ( addr len -- actual-len ) " read" deblocker $call-method ;
   : write ( addr len -- actual-len )
   true to write-eof-mark?
   " write" deblocker $call-method
   ;

   : load ( addr -- size )
   \ use my-args to get tape file-no
   ... ( addr file# )

   \ position at requested file
   ...
\ sample fcode driver for bootable devices.
  dup begin                  ( start-addr next-addr )
    dup max-transfer read    ( start-addr next-addr #read )
    dup 0>                   ( start-addr next-addr #read got-some? )
  while                      ( start-addr next-addr #read )
    +                       ( start-addr next-addr' )
  repeat                     ( start-addr end-addr 0 )
  drop swap -                ( size )
;

: seek ( byte# file# -- error? )
  \ position at requested file
  ...                        ( byte# )

  flush-deblocker            ( byte# )
  begin dup 0> while         ( #remaining )
    " mbuf0" $call-parent
  over ublock min read      ( #remaining #read )
  dup 0= if                 ( #remaining 0 )
    2drop true              ( error )
    exit                   ( error )
    then                   ( #remaining #read )
    -                      ( #remaining' )
  repeat                   ( 0 )
  drop false                ( no-error )
;

  finish-device \ finishing "byte" device "st"
end0
\ finishing "SUNW,my-scsi"
Display Devices

This chapter discusses writing FCode programs for display devices. The display device type applies to frame buffers and other devices appearing as memory to the processor, with associated hardware to convert the memory image to a visual display. Display devices can be used as console output devices.

Required Methods

The display device’s FCode must declare the display device type, and must implement the methods open and close.

System defer words are loaded by appropriate routines. is-install, is- remove and is-selftest are used to create the open, close and selftest routines. set-font initializes the values of frame-buffer-adr, char-height and char-width, all of which are built into the system ROM and can be used by any display device driver.

For display devices, created methods interact with OpenBoot commands in a way that is different from that of other device types. Other device types provide methods that are found by dictionary searches looking for specific names.

Some FCodes are specifically designed for display devices. See Table A-36 through Table A-42 in Appendix A, “FCode Reference”.
Required Properties

<table>
<thead>
<tr>
<th>Property Name</th>
<th>Typical Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>name</td>
<td>SUNW,cgsix</td>
</tr>
<tr>
<td>reg</td>
<td>list of registers {device dependent}</td>
</tr>
<tr>
<td>device_type</td>
<td>&quot;display&quot; {required for display devices}</td>
</tr>
</tbody>
</table>

Device Driver Examples

Simple Display Device Driver

This is a sample FCode program for a display device that does not need to be usable as a console display device during system power-up.

Code Example 4  Basic Display Device Driver Example

```plaintext
\ Basic display device driver
\ cg6 (Lego) frame buffer driver

hex

" SUNW,cgsix" name
" SUNW,501-xxxx" model

h# 20.0000 constant dac-offset h# 10 constant /dac
h# 30.0000 constant fhc-offset h# 10 constant /fhc
h# 30.1800 constant thc-offset h# 20 constant /thc
h# 70.0000 constant fbc-offset h# 10 constant /fbc
h# 70.1000 constant tec-offset h# 10 constant /tec
h# 80.0000 constant fb-offset h# 10.0000 constant /frame

: >reg-spec ( offset size -- encoded-reg )
  >r 0 my-address d+ my-space encode-phys 0 encode-int encode+ r> encode-int encode+ 
;
0 0 >reg-spec \ Configuration space registers
dac-offset /dac >reg-spec encode+
fhc-offset /fhc >reg-spec encode+
thc-offset /thc >reg-spec encode+
```
Extended Display Device Driver

This sample FCode program has additional functionality to initialize and test the device, but still is not usable as a console display device during system power-up.
Code Example 5  Extended Display Device Driver Example

\ Extended Display device driver
\ cg6 (Lego) frame buffer driver

hex

" SUNW,cgsix" name
" SUNW,501-xxxx" model

h# 20.0000 constant dac-offset h# 10 constant /dac
h# 30.0000 constant fhc-offset h# 10 constant /fhc
h# 30.1800 constant thc-offset h# 20 constant /thc
h# 70.0000 constant fbc-offset h# 10 constant /fbc
h# 70.1000 constant tec-offset h# 10 constant /tec
h# 80.0000 constant fb-offset h# 10.0000 constant /frame

: >reg-spec ( offset size -- encoded-reg )
  >r 0 my-address d+ my-space encode-phys 0 encode-int encode+ r> encode-int encode+

0 0 >reg-spec \ Configuration space registers
dac-offset /dac >reg-spec encode+
fhc-offset /fhc >reg-spec encode+
thc-offset /thc >reg-spec encode+
fbc-offset /fbc >reg-spec encode+
tec-offset /tec >reg-spec encode+
fb-offset /frame >reg-spec encode+
" reg" property

-1 value dac-addr
-1 value fhc-addr
-1 value thc-addr
-1 value fbc-addr
-1 value tec-addr
-1 value fb-addr

: copyright ( -- addr len ) "Copyright (c) 1992 - 1996 Sun Microsystems, Inc. ";
: do-map-in ( offset size -- )
  >r ( offset ) ( R: size ) \ Move size to return stack
  0 ( offset 0 ) ( R: size ) \ Convert offset to double number
  my-address ( offset 0 phys.lo phys.mid ) ( R: size )
  d+ ( phys.lo' phys.mid ) ( R: size )
  my-space r> ( phys.lo' phys.mid phys.hi size ) ( R: )
  " map-in" $call-parent

;
Display Devices

Brooktree DAC interface section

The Brooktree DAC has an internal address register which helps to select the internal register which is to be accessed. First, the address is written to register 0, then the data is written to one of the other registers. Ibis has 3 separate DAC chips which appear as the three least-significant bytes of a longword. All three chips may be simultaneously updated with a single longword write.

Code Example 5  Extended Display Device Driver Example  (Continued)

\ Extended Display device driver
: do-map-out ( vaddr size -- ) " map-out" $call-parent ;

: dac-map ( -- ) dac-offset /dac do-map-in to dac-addr ;
: dac-unmap ( -- ) dac-addr /dac do-map-out -1 to dac-addr ;

: fhc-map ( -- ) fhc-offset /fhc do-map-in to fhc-addr ;
: fhc-unmap ( -- ) fhc-addr /fhc do-map-out -1 to fhc-addr ;

: thc-map ( -- ) thc-offset /thc do-map-in to thc-addr ;
: thc-unmap ( -- ) thc-addr /thc do-map-out -1 to thc-addr ;

: fbc-map ( -- ) fbc-offset /fbc do-map-in to fbc-addr ;
: fbc-unmap ( -- ) fbc-addr /fbc do-map-out -1 to fbc-addr ;

: tec-map ( -- ) tec-offset /tec do-map-in to tec-addr ;
: tec-unmap ( -- ) tec-addr /tec do-map-out -1 to tec-addr ;

: fb-map ( -- ) fb-offset /frame do-map-in to fb-addr ;
: fb-unmap ( -- ) fb-addr /frame do-map-out -1 to fb-addr ;

: map-regs ( -- ) dac-map fhc-map thc-map fbc-map tec-map ;
: unmap-regs ( -- ) tec-unmap fbc-unmap thc-unmap fhc-unmap dac-unmap ;

\ Brooktree DAC interface section

\ The Brooktree DAC has an internal address register which helps to select the internal register which is to be accessed. First, the address is written to register 0, then the data is written to one of the other registers. Ibis has 3 separate DAC chips which appear as the three least-significant bytes of a longword. All three chips may be simultaneously updated with a single longword write.

: dac! ( data reg# -- ) >r dup 2dup bjoin r> dac-addr + l! ;
: dac-ctl! ( data int.addr reg# -- ) swap 0 dac! dac! ;

\ color! sets an overlay color register.
\ In order to be able to use either the Brooktree 457 or 458 dacs, we set the address once, then store the color 3 times. The chip internally cycles each time the color register is written, selecting in turn the red color, the green color, and the blue color. The chip is used in "RGB mode".

: color! ( r g b c# -- )
  0 dac! ( r g b )
\ Extended Display device driver
  swap rot ( b g r )
  4 dac! ( b g )
  4 dac! ( b )
  4 dac! ( )
;

: lego-init-dac ( -- )
  40 06 8 dac-ctl! \ Control reg: enable off, overlay off, RGB on
  0 05 8 dac-ctl! \ Blinking off
  ff 04 8 dac-ctl! \ Read mask set to all ones
  ff ff ff 0 color! \ White in overlay background color register
  0 0 0 ff color! \ Black in overlay foreground color register
  64 41 b4 1 color! \ SUN-blue for logo
;
\ End of Brooktree DAC code

\ Lego Selftest section

: fbc! ( value offset -- ) fbc-addr + l! ;
: fbc@ ( offset -- value ) fbc-addr + l@ ;
: tec! ( value offset -- ) tec-addr + l! ;

: lego-selftest ( -- failed? ) false ;

\ Hardware configuration register section

: fhc! ( value offset -- ) fhc-addr + l! ;
: thc! ( value offset -- ) thc-addr + l! ;

: set-res-params ( hcvd hcvs hchd hchsdvb hchs fhc-conf -- )
  0 fhc! 0 thc! 4 thc! 8 thc! c thc! 10 thc! ;

\ Resolution params:

: r1024x768 ( -- params ) 2c032c 32c0005 110051 490000 510007 3bb ;
: r1152x900 ( -- params ) 2403a8 10005 15005d 570000 10009 bbb ;
: r1024x1024 ( -- params ) 200426 10005 180054 520000 10009 3bb ;
: r1152x870 ( -- params ) 2c0392 20005 120054 540000 10009 bbb ;
: r1600x1280 ( -- params ) 340534 534009 130045 3d0000 450007 1bbb ;

0 value lego-rez-width
\ Extended Display device driver
0 value lego-rez-height

0 value sense-code

: set-resolution ( sense-code -- )
case
  0 of d# 1152 d# 900 endof
  12 of d# 1024 d# 1024 endof
  13 of d# 1600 d# 1280 endof
  drop d# 1152 d# 900 0 endcase
  to lego-rez-height to lego-rez-width
;

8f value thc-misc
: lego-video-on ( -- ) thc-misc 400 or 18 thc! ;
: lego-video-off ( -- ) thc-misc 18 thc! ;

: lego-init-hc ( -- )
sense-code case
  0 of r1152x900 endof
  12 of r1024x1024 endof
  13 of r1600x1280 endof
  drop r1152x900 0 endcase
  ( resolution-params )
  set-res-params
  016b 14 thc! \ THC_HCREFRESH
  148f 18 thc! \ THC_HCMISS
  \ 48f 18 thc! \ THC_HCMISS
  lego-video-off \ Turn video on at install time
;
\ End of hardware configuration register section
end0
Complete Display Device Drive

This sample FCode program is for a device that would be usable as a system console device.

Code Example 6  Complete Display Device Driver Example

```fcode
\ Complete Display device driver
\ cg6 (Lego) frame buffer driver

hex

" SUNW,cgsix" name
" SUNW,501-xxxx" model
" display" device-type

h# 20.0000 constant dac-offset h# 10 constant /dac
h# 30.0000 constant fhc-offset h# 10 constant /fhc
h# 30.1800 constant thc-offset h# 20 constant /thc
h# 70.0000 constant fbc-offset h# 10 constant /fbc
h# 70.1000 constant tec-offset h# 10 constant /tec
h# 80.0000 constant fb-offset h# 10.0000 constant /frame

: >reg-spec ( offset size -- encoded-reg )
  >r 0 my-address d+ my-space encode-phys 0 encode-int encode+ r> encode-int encode+

0 0 >reg-spec              \ Configuration space registers
  dac-offset /dac >reg-spec encode+
  fhc-offset /fhc >reg-spec encode+
  thc-offset /thc >reg-spec encode+
  fbc-offset /fbc >reg-spec encode+
  tec-offset /tec >reg-spec encode+
  fb-offset /frame >reg-spec encode+
" reg" property

-1 value dac-addr
-1 value fhc-addr
-1 value thc-addr
-1 value fbc-addr
-1 value tec-addr
-1 value fb-addr

: copyright ( -- addr len ) "Copyright (c) 1992 - 1996 Sun Microsystems, Inc. ";

: do-map-in ( offset size -- )
```

Brooktree DAC Interface Section

The Brooktree DAC has an internal address register which helps to select the internal register which is to be accessed. First, the address is written to register 0, then the data is written to one of the other registers.

Ibis has 3 separate DAC chips which appear as the three least-significant bytes of a longword. All three chips may be simultaneously updated with a single longword write.

Code Example 6

Complete Display Device Driver Example (continued)
In order to be able to use either the Brooktree 457 or 458 dacs, we set the address once, then store the color 3 times. The chip internally cycles each time the color register is written, selecting in turn the red color, the green color, and the blue color. The chip is used in "RGB mode".

: color! ( r g b c# -- )
  0 dac! ( r g b )
  swap rot ( b g r )
  4 dac! ( b g )
  4 dac! ( b )
  4 dac! ( )
;

: lego-init-dac ( -- )
  40 06 8 dac-ctl! \ Control reg: enable off, overlay off, RGB on
  0 05 8 dac-ctl! \ Blinking off
  ff 04 8 dac-ctl! \ Read mask set to all ones
  ff ff ff 0 color! \ White in overlay background color register
  0 0 0 ff color! \ Black in overlay foreground color register
  64 41 b4 1 color! \ SUN-blue for logo
;

\ End of Brooktree DAC code
\ Lego Selftest section

: fbc! ( value offset -- ) fbc-addr + l! ;
: fbc@ ( offset -- value ) fbc-addr + l@ ;
: tec! ( value offset -- ) tec-addr + l! ;

: lego-selftest ( -- failed? ) false ;

\ Hardware configuration register section

: fhc! ( value offset -- ) fhc-addr + l! ;
: thc! ( value offset -- ) thc-addr + l! ;

: set-res-params ( hcvd hcvs hchd hchs hchsdbv hchs fhc-conf -- )
  0 fhc! 0 thc! 4 thc! 8 thc! c thc! 10 thc! ;

\ Resolution params:
  hcvd hcvs hchd hchs hchsdbv hchs fhc-conf
Code Example 6  Complete Display Device Driver Example  (Continued)

\ Complete Display device driver
: r1024x768 ( -- params )  2c032c  32c0005  110051  490000  510007  3bb ;
: r1152x900 ( -- params )  2403a8  10005  15005d  570000  10009  bbb ;
: r1024x1024 ( -- params )  200426  10005  180054  520000  10009  3bb ;
: r1152x870 ( -- params )  2c0392  20005  120054  540000  10009  bbb ;
: r1600x1280 ( -- params )  340534  534009  130045  3d0000  450007 1bbb ;

0 value lego-rez-width
0 value lego-rez-height

0 value sense-code

: set-resolution ( sense-code -- )
case
  0 of d# 1152 d# 900 endof
12 of d# 1024 d# 1024 endof
13 of d# 1600 d# 1280 endof
drop d# 1152 d# 900 0
endcase
to lego-rez-height to lego-rez-width

8f value thc-misc
: lego-video-on ( -- ) thc-misc 400 or  18 thc! ;
: lego-video-off ( -- ) thc-misc 18 thc! ;
: lego-blink ( -- ) lego-video-off 20 ms lego-video-on ;
: lego-init-hc ( -- )
sense-code case
  0 of r1152x900 endof
12 of r1024x1024 endof
13 of r1600x1280 endof
drop r1152x900 0
endcase ( resolution-params )
set-res-params
016b 14 thc! \ THC_HCREFRESH
148f 18 thc! \ THC_HCMISC
lego-video-off \ Turn video on at install time

\ End of hardware configuration register section

\ Lego graphics section
: lego-install ( -- )
  map-regs fb-map fb-addr to frame-buffer-adr

Display Devices
\ Complete Display device driver

  default-font ( param ... ) set-font

  frame-buffer-adr encode-int " address" property

  lego-rez-width lego-rez-height over char-width / over char-height /
  fb8-install
  [' ] lego-blink to blink-screen
  lego-video-on

;

: lego-remove ( -- )
  lego-video-off
  unmap-regs
  fb-unmap -1 to frame-buffer-adr

;

\ End of Lego graphics section

: lego-probe ( -- )

  map-regs

  sense-code set-resolution

  lego-init-dac
  lego-init-hc

  unmap-regs

  lego-rez-width encode-int " width" property
  lego-rez-height encode-int " height" property
  d# 8 encode-int " depth" property
  lego-rez-width encode-int " linebytes" property

  [' ] lego-install is-install
  [' ] lego-remove is-remove
  [' ] lego-selftest is-selftest

;

lego-probe
**Code Example 6  Complete Display Device Driver Example  (Continued)**

```
\ Complete Display device driver
end0
```
This chapter discusses addressing and required properties for memory-mapped buses.

A memory-mapped bus logically extends the processor’s memory address space to include the devices on that bus. This enables the children of the bus device to be mapped into the CPU address space and accessed like memory using processor load and store cycles to address those children directly.

SBus and VMEbus are examples of memory-mapped buses.

Not all bus devices fall into this category. For example, SCSI is not a memory-mapped bus; SCSI targets are not accessed with load or store instructions.

**Required Methods**

A memory-mapped bus package code must implement the `open`, `close`, `reset`, and `selftest` methods, as well as the following:

```
decode-unit ( addr len -- phys.lo ... phys.hi )
```

Convert `addr len`, a text string representation, to `phys.lo ... phys.hi`, a numerical representation of a physical address in the address space defined by this package. The format of `phys.lo ... phys.hi` varies from bus to bus.
dma-alloc ( size -- virt )

Allocate a virtual address range of length size bytes that is suitable for direct memory access by a bus master device. The memory is allocated according to the most stringent alignment requirements for the bus. virt is a 32-bit address that the OpenBoot-based system can use to access the memory.

Note that dma-map-in must also be called to generate a suitable DMA address.

A child of a memory-mapped device calls dma-alloc using

```
" dma-alloc" $call-parent
```

For example:

```
-1 value my-vaddr
: my-dma-alloc ( size -- )
  " dma-alloc" $call-parent to my-vaddr
;
```

dma-free ( virt size -- )

Free size bytes of memory previously allocated by dma-alloc at the virtual address virt.

A child of a memory-mapped device calls dma-free by using

```
" dma-free" $call-parent
```

For example:

```
2000 value my-size
: my-dma-free ( -- )
  my-vaddr my-size " dma-free" $call-parent
  -1 to my-vaddr
;
```

dma-map-in ( virt size cacheable? -- devaddr )

Convert the virtual address range virt size, previously allocated by dma-alloc, into an address devaddr suitable for DMA on the bus. dma-map-in can also be used to map application-supplied data buffers for DMA use if the
bus allows. If cacheable? is true, the calling child desires to use any available fast caches for the DMA buffer. If access to the buffer is required before the buffer is mapped out, the child must call dma-sync or dma-map-out to ensure cache coherency with memory.

A child of a memory-mapped device calls dma-map-in using

```
" dma-map-in" $call-parent
```

For example:

```
: my-vaddr-dma-map ( -- )
  my-vaddr my-size false " dma-map-in" $call-parent ( devaddr )
to my-vaddr-dma
;
```

dma-map-out ( virt devaddr size -- )

Remove the DMA mapping previously created with dma-map-in. Flush all caches associated with the mapping.

A child of a memory-mapped device calls dma-map-out by using

```
" dma-map-out" $call-parent
```

For example:

```
: my-vaddr-dma-free ( -- )
  my-vaddr my-vaddr-dma my-size " dma-map-out" $call-parent
  -1 to my-vaddr-dma
;
```
dma-sync ( virt devaddr size -- )
Synchronize (flush) any memory caches associated with the DMA mapping previously established by dma-map-in. You must interleave calls to this method (or dma-map-out) between DMA and CPU accesses to the memory region, or you may not obtain the most recent data written into the cache.

For example, a child of a hierarchical device calls dma-sync by using $call-parent. This method is valid for FCode version 2.1 or later. Some early version 2 systems do not define this method in the /sbus node. Those systems automatically synchronize DMA and CPU access. The following example will give correct results in all cases.

```
: my-dma-sync ( virt devadr size -- )
  " dma-sync" $call-parent
;
```

probe-self ( arg-addr arg-len reg-addr reg-len fcode-addr fcode-len -- )
Probe for a child of this node. fcode-addr fcode-len is a unit-address text string that identifies the location of the FCode program for the child. reg-addr reg-len is a probe-address text string that identifies the address of the child itself. arg-addr arg-len is an instance-arguments text string for any device arguments for the child (which can be retrieved in the child’s FCode program with the my-args FCode). probe-self checks whether there is indeed FCode at the indicated location, perhaps by mapping the device and using cpeek to ensure that the device is present and that the first byte is a valid FCode start byte.
If the FCode exists, probe-self creates a new child device node and interprets the FCode. If the interpretation of the FCode fails in some way, the new device node may be empty, containing no properties or methods.
For example, to probe FCode for SBus slot #1:

```
" /sbus" open-dev
0 0 " 1,0" 2dup probe-self
device-end
```
map-in ( phys.lo ... phys.hi size -- virt )

Create a mapping associating the range of physical addresses beginning at phys.lo ... phys.hi extending for size bytes in the package’s physical address space with a processor virtual address virt.

The number of cells in the list phys.lo ... phys.hi is determined by the value of the "#address-cells" property of the node containing map-in.

For example, a child of a memory-mapped device calls map-in with "map-in" $call-parent. (The following example assumes that the value of the parent’s "#address-cells" property is 3):

```
: map-reg ( -- )
   my-address xx-offset 0 d+ my-space ( phys.lo phys.mid phys.hi )
   xx-size "map-in" $call-parent ( virt )
   to xx-vaddr ( )
;
```

map-out ( virt size -- )

Destroy the mapping set by map-in at virtual address virt of length size bytes.

For example, a child of a memory-mapped device calls map-out with "map-out" $call-parent:

```
: unmap-reg ( -- )
   xx-vaddr xx-size ( virt size )
   "map-out" $call-parent ( )
   -1 to xx-vaddr
;
```

**SBus Addressing**

The SBus uses geographical addressing with numbered slots.

An SBus physical address is represented numerically by the SBus slot number as the high number and the offset from the base of that slot as the low number.

The text string representation is slot#, offset where both slot# and offset are the ASCII representations of hexadecimal numbers.
SBus Required Properties

Table 8-1 Required SBus Properties

<table>
<thead>
<tr>
<th>Property Name</th>
<th>Sample Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>name</td>
<td>&quot;SUNW,fas&quot;</td>
</tr>
<tr>
<td>burst-sizes</td>
<td></td>
</tr>
<tr>
<td>device_type</td>
<td>&quot;sbus&quot;</td>
</tr>
<tr>
<td>ranges</td>
<td></td>
</tr>
<tr>
<td>slot-address-bits</td>
<td></td>
</tr>
</tbody>
</table>

Device Driver Examples

The following examples of a hierarchical FCCode driver are based on Sun's SBus expansion hardware, XBox. XBox increases the number of SBus slots available in a system by providing a bus-bridge between the platform's onboard SBus and an SBus in the XBox hardware. XBox includes an SBus card called the XAdaptor card which plugs into the host platform’s SBus and includes an expansion chassis called the XBox Expansion Box. Therefore XBox is an example of a hierarchical device which implements an SBus interface to child plug-in devices.

The example is divided into three parts: the basic device driver, the extended device driver, and the complete device driver. In the case of a hierarchical device, in practice, one would only want to develop and ship a driver with the complete functionality. Otherwise, plug-in cards which rely on a full set of parent services generally would not be able to function. The three stage presentation of the driver simply shows how a driver might grow through the development cycle.
Basic Hierarchical Device Driver

The basic driver simply declares most of the important properties of the device, particularly the addresses of the various registers. A driver in this state might be used to support the development of the OS driver which would attach to the device name and configure itself based on the device properties published by the FCode driver.

Code Example 8-1 Basic Hierarchical Device Driver

```plaintext
hex
fcode-version2
" SUNW,xbox" name
" 501-1840" model

\ XBox Registers
\ XAdaptor card registers
h#       0 constant write0-offset h# 4 constant /write0
h#  2.0000 constant xac-err-offset h# c constant /xac-err
h# 10.0000 constant xac-ctl0-offset h# 4 constant /xac-ctl0
h# 11.0000 constant xac-ctl1-offset h# 4 constant /xac-ctl1
h# 12.0000 constant xac-elua-offset h# 4 constant /xac-elua
h# 13.0000 constant xac-ella-offset h# 4 constant /xac-ella
h# 14.0000 constant xac-ele-offset h# 4 constant /xac-ele

\ XBox Expansion box registers
h#  42.0000 constant xbc-err-offset h# c constant /xbc-err
h#  50.0000 constant xbc-ctl0-offset h# 4 constant /xbc-ctl0
h#  51.0000 constant xbc-ctl1-offset h# 4 constant /xbc-ctl1
h#  52.0000 constant xbc-elua-offset h# 4 constant /xbc-elua
h#  53.0000 constant xbc-ella-offset h# 4 constant /xbc-ella
h#  54.0000 constant xbc-ele-offset h# 4 constant /xbc-ele

: >reg-spec ( offset size -- xdrreg )
  >r my-address + my-space encode-phys r> encode-int encode+
; }
write0-offset /write0 >reg-spec
xac-err-offset /xac-err >reg-spec encode+
xac-ctl0-offset /xac-ctl0 >reg-spec encode+
xac-ctl1-offset /xac-ctl1 >reg-spec encode+
xac-elua-offset /xac-elua >reg-spec encode+
xac-ella-offset /xac-ella >reg-spec encode+
```
Code Example 8-1  Basic Hierarchical Device Driver (Continued)

xac-ele-offset /xac-ele >reg-spec encode+
xbc-err-offset /xbc-err >reg-spec encode+
xbc-ctl0-offset /xbc-ctl0 >reg-spec encode+
xbc-ctl1-offset /xbc-ctl1 >reg-spec encode+
xbc-elua-offset /xbc-elua >reg-spec encode+
xbc-ella-offset /xbc-ella >reg-spec encode+
xbc-ele-offset /xbc-ele >reg-spec encode+
" reg" property

\ XBox can interrupt on any SBus level
1 encode-int 2 encode-int encode+ 3 encode-int encode+ 4 encode-int encode+
5 encode-int encode+ 6 encode-int encode+ 7 encode-int encode+
" interrupts" property

1 sbus-intr>cpu encode-int 0 encode-int encode+
2 sbus-intr>cpu encode-int encode+ 0 encode-int encode+
3 sbus-intr>cpu encode-int encode+ 0 encode-int encode+
4 sbus-intr>cpu encode-int encode+ 0 encode-int encode+
5 sbus-intr>cpu encode-int encode+ 0 encode-int encode+
6 sbus-intr>cpu encode-int encode+ 0 encode-int encode+
7 sbus-intr>cpu encode-int encode+ 0 encode-int encode+
" intr" property

\ XBox bus clock speed
d# 25.000.000 encode-int " clock-frequency" property

\ Burst sizes 64,32,16,8,4,2,1 bursts.
h# 7f encode-int " burst-sizes" property

\ XBox has no slave-only slots
0 encode-int " slave-only" property

\ Get the number of address bits for this SBus slot from the parent SBus
\ node without inheritance . OpenBoot 2.5 doesn't publish slot-address-bits.
\ However 2.5 is only on 4m machines, which are all 28 bits per slot.

: $= ( addr1 len1 addr2 len2 -- equal? ) \ string compare
   rot over - if
      drop 2drop false \ different lengths
   else comp 0=
      then
; 4mhack ( -- n )
   " compatible" get-inherited-property if
   d# 25 \ no "compatible" prop; assume 4c
   else decodestring " sun4m" $= if
      d# 28
   else
      d# 25 \ not sun4m
   then
   nip nip
   then
;
; #bits ( -- n )
   " slot-address-bits" my-parent ihandle>phandle
   get-package-property if
      4mhack
   else
      decode-int nip nip
   then
;
#bits constant host-slot-size
host-slot-size encode-int " slot-address-bits" property
end0
Extended Hierarchical Device Driver

The extended driver adds methods allowing access to various device registers in addition to the functions of the basic driver. It provides methods to:

- Map in the registers
- Fetch from and store to the registers
- Program one of the registers which control the allocation of address space across the various SBus slots.

Such an extended driver provides methods that a developer can use to read and write registers and verify correct hardware responses. Note that the complete driver does not use all of the device registers; read/write access methods were included for all of them to allow easy testing during development.

Code Example 8-2  Extended Hierarchical Device Driver

```fcode
\ extended hierarchical device driver sample

hex
fcode-version2

" SUNW,xbox"  name
" 501-1840"   model

\ XBox Registers

h#  0 constant write0-offset    h# 4 constant /write0
h#  2.0000 constant xac-err-offset   h# c constant /xac-err
h# 10.0000 constant xac-ctl10-offset h# 4 constant /xac-ctl10
h# 11.0000 constant xac-ctl11-offset h# 4 constant /xac-ctl11
h# 12.0000 constant xac-elua-offset  h# 4 constant /xac-elua
h# 13.0000 constant xac-ella-offset  h# 4 constant /xac-ella
h# 14.0000 constant xac-ele-offset   h# 4 constant /xac-ele
h# 42.0000 constant xbc-err-offset   h# c constant /xbc-err
h# 50.0000 constant xbc-ctl10-offset h# 4 constant /xbc-ctl10
h# 51.0000 constant xbc-ctl11-offset h# 4 constant /xbc-ctl11
h# 52.0000 constant xbc-elua-offset  h# 4 constant /xbc-elua
h# 53.0000 constant xbc-ella-offset  h# 4 constant /xbc-ella
h# 54.0000 constant xbc-ele-offset   h# 4 constant /xbc-ele

: >reg-spec ( offset size -- xdrreg )
  >r my-address + my-space encode-phys r> encode-int encode+
```
Memory-Mapped Buses

Code Example 8-2  Extended Hierarchical Device Driver (Continued)

\ extended hierarchical device driver sample
;

write0-offset    /write0    >reg-spec
xac-err-offset   /xac-err    >reg-spec encode+
xac-ctl0-offset  /xac-ctl0   >reg-spec encode+
xac-ctl1-offset  /xac-ctl1   >reg-spec encode+
xac-elua-offset  /xac-elua   >reg-spec encode+
xac-ella-offset  /xac-ella   >reg-spec encode+
xac-ele-offset   /xac-ele    >reg-spec encode+
xbc-err-offset   /xbc-err     >reg-spec encode+
xbc-ctl0-offset  /xbc-ctl0    >reg-spec encode+
xbc-ctl1-offset  /xbc-ctl1    >reg-spec encode+
xbc-elua-offset  /xbc-elua    >reg-spec encode+
xbc-ella-offset  /xbc-ella    >reg-spec encode+
xbc-ele-offset   /xbc-ele     >reg-spec encode+
" reg" property

\ Xbox can interrupt on any SBus level

1 encode-int       2 encode-int encode+  3 encode-int encode+  4 encode-int encode+
5 encode-int encode+  6 encode-int encode+  7 encode-int encode+  8 encode-int encode+
" interrupts" property

1 sbus-intr>cpu encode-int       0 encode-int encode+
2 sbus-intr>cpu encode-int encode+ 0 encode-int encode+
3 sbus-intr>cpu encode-int encode+ 0 encode-int encode+
4 sbus-intr>cpu encode-int encode+ 0 encode-int encode+
5 sbus-intr>cpu encode-int encode+ 0 encode-int encode+
6 sbus-intr>cpu encode-int encode+ 0 encode-int encode+
7 sbus-intr>cpu encode-int encode+ 0 encode-int encode+
" intr" property

\ XBox bus clock speed
d# 25.000.000 encode-int " clock-frequency" property

\ Burst sizes 64,32,16,8,4,2,1 bursts.
h# 7f encode-int " burst-sizes" property

\ XBox has no slave-only slots
0 encode-int " slave-only" property

\ Get the number of address bits for this SBus slot from the parent SBus
node without inheritance. OpenBoot 2.5 doesn't publish slot-address-bits. However 2.5 is only on 4m machines, which are all 28 bits per slot.

\$= ( addr1 len1 addr2 len2 -- equal? ) \ string compare
   rot over - if
      drop 2drop false \ different lengths
   else  comp 0=
      then

; 4mhack ( -- n )
   " compatible" get-inherited-property if
      d# 25 \ no "compatible" prop; assume 4c
   else decodestring " sun4m" $= if
      d# 28
   else
      d# 25 \ not sun4m
      then
         nip nip
      then

; #bits ( -- n )
   " slot-address-bits" my-parent ihandle>phandle
   get-package-property if
      4mhack
   else
      decode-int nip nip
   then

; #bits constant host-slot-size
host-slot-size encode-int " slot-address-bits" property

\ Utility display string
: .me ( -- ) ." SBus " my-space .d ." XBox " ;

\ The XBox device has two modes opaque and transparent.
\ Upon reset the device is set to opaque mode. In this mode all
\ accesses to address space of the device are directed to the XBox H/W
\ (ie. XAdaptor Card or the XBox Expansion Box) itself.
\ In the transparent mode all accesses are mapped to the SBus cards
\ which are plugged into the XBox. In transparent mode the XBox H/W is
Memory-Mapped Buses

Memory-Mapped Buses

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Code Example 8-2  Extended Hierarchical Device Driver (Continued)

\ extended hierarchical device driver sample
\ accessible only via the "write-0" register. To allow another bus
\ bridge to be plugged into the XBox all writes to the write-0 register
\ must contain a "key" which is programmed into the XBox H/W at boot
\ time. If the key field of a write to write-0 matches that programmed
\ at boot time the H/W intercepts the write. Otherwise the H/W passes
\ the write along.

\ The XBox has two sets of registers. Those of the XAdaptor card and
\ and those of the XBox Expansion Box.

\ Opaque mode host adapter registers
-1 value xac-err-regs
-1 value xac-ctl0  -1 value xac-ctl1
-1 value xac-elua  -1 value xac-ella
-1 value xac-ele
\ Opaque mode expansion box registers
-1 value xbc-err-regs
-1 value xbc-ctl0  -1 value xbc-ctl1
-1 value xbc-elua  -1 value xbc-ella
-1 value xbc-ele
\ Transparent mode register
-1 value write0-reg

: xbox-map-in ( offset space size -- virt ) " map-in" $call-parent ;
: xbox-map-out ( virt size -- )              " map-out" $call-parent ;
: map-regs ( -- )
  write0-offset   my-address + my-space /write0   xbox-map-in  to write0-reg
  xac-err-offset  my-address + my-space /xac-err  xbox-map-in  to xac-err-regs
  xac-ctl0-offset my-address + my-space /xac-ctl0 xbox-map-in  to xac-ctl0
  xac-ctl1-offset my-address + my-space /xac-ctl1 xbox-map-in  to xac-ctl1
  xac-elua-offset my-address + my-space /xac-elua xbox-map-in  to xac-elua
  xac-ella-offset my-address + my-space /xac-ella xbox-map-in  to xac-ella
  xac-ele-offset  my-address + my-space /xac-ele  xbox-map-in  to xac-ele
  xbc-err-offset  my-address + my-space /xbc-err  xbox-map-in  to xbc-err-regs
  xbc-ctl0-offset my-address + my-space /xbc-ctl0 xbox-map-in  to xbc-ctl0
  xbc-ctl1-offset my-address + my-space /xbc-ctl1 xbox-map-in  to xbc-ctl1
  xbc-elua-offset my-address + my-space /xbc-elua xbox-map-in  to xbc-elua
  xbc-ella-offset my-address + my-space /xbc-ella xbox-map-in  to xbc-ella
  xbc-ele-offset  my-address + my-space /xbc-ele  xbox-map-in  to xbc-ele
;
: unmap-regs ( -- )
  write0-reg   /write0   xbox-map-out  -1 to write0-reg
  xac-err-regs /xac-err  xbox-map-out  -1 to xac-err-regs
Code Example 8-2  Extended Hierarchical Device Driver (Continued)

\ extended hierarchical device driver sample
\ xac-ctl0 /xac-ctl0 xbox-map-out -1 to xac-ctl0
\ xac-ctl1 /xac-ctl1 xbox-map-out -1 to xac-ctl1
\ xac-elua /xac-elua xbox-map-out -1 to xac-elua
\ xac-ella /xac-ella xbox-map-out -1 to xac-ella
\ xac-ele /xac-ele xbox-map-out -1 to xac-ele
\ xbc-err-regs /xbc-err xbox-map-out -1 to xbc-err-regs
\ xbc-ctl0 /xbc-ctl0 xbox-map-out -1 to xbc-ctl0
\ xbc-ctl1 /xbc-ctl1 xbox-map-out -1 to xbc-ctl1
\ xbc-elua /xbc-elua xbox-map-out -1 to xbc-elua
\ xbc-ella /xbc-ella xbox-map-out -1 to xbc-ella
\ xbc-ele /xbc-ele xbox-map-out -1 to xbc-ele

;  
\ Opaque mode register access words
: xac-errd@ ( -- 1 ) xac-err-regs rl@ ;
: xac-erra@ ( -- 1 ) xac-err-regs 4 + rl@ ;
: xac-errs@ ( -- 1 ) xac-err-regs 8 + rl@ ;
: xac-ctl0@ ( -- w ) xac-ctl0 rl@ ;
: xac-ctl0! ( w -- ) xac-ctl0 rl! ;
: xac-ctl1@ ( -- w ) xac-ctl1 rl@ ;
: xac-ctl1! ( w -- ) xac-ctl1 rl! ;
: xac-elua@ ( -- l ) xac-elua rl@ ;
: xac-elua! ( l -- ) xac-elua rl! ;
: xac-ella@ ( -- w ) xac-ella rl@ ;
: xac-ella! ( w -- ) xac-ella rl! ;
: xbc-errd@ ( -- 1 ) xbc-err-regs rl@ ;
: xbc-erra@ ( -- 1 ) xbc-err-regs 4 + rl@ ;
: xbc-errs@ ( -- 1 ) xbc-err-regs 8 + rl@ ;
: xbc-ctl0@ ( -- w ) xbc-ctl0 rl@ ;
: xbc-ctl0! ( w -- ) xbc-ctl0 rl! ;
: xbc-ctl1@ ( -- w ) xbc-ctl1 rl@ ;
: xbc-ctl1! ( w -- ) xbc-ctl1 rl! ;
: xbc-elua@ ( -- l ) xbc-elua rl@ ;
: xbc-elua! ( l -- ) xbc-elua rl! ;
: xbc-ella@ ( -- w ) xbc-ella rl@ ;
: xbc-ella! ( w -- ) xbc-ella rl! ;  
\ Transparent Mode register access words
external
: unique-key ( -- n ) " unique-key" $call-parent ;
headers
unique-key constant my-key
my-key encode-int " write0-key" property

: xbox! ( w offset -- ) my-key h# 18 << or or write0-reg rl! ;

: write-xac-ctl0 ( w -- ) xac-ctl0-offset xbox! ;
: write-xac-ctl1 ( w -- ) xac-ctl1-offset xbox! ;
: write-xbc-ctl0 ( w -- ) xbc-ctl0-offset xbox! ;
: write-xbc-ctl1 ( w -- ) xbc-ctl1-offset xbox! ;

\ Some functionally oriented words

: set-key ( -- ) my-key 8 << xac-ctl0! ;
: transparent ( -- ) 1 xac-ctl1! ;
: opaque ( -- ) 0 write-xac-ctl1 ;
: enable-slaves ( -- ) h# 38 write-xbc-ctl1 ;

: xbox-errors ( -- xbc-err xac-err )
  opaque xbc-errd@ xac-errd@ transparent
;

: ?.errors ( xbc-err xac-err -- )
  dup h# 8000.0000 and if
  cr .me." xac-error " .h cr
  else drop
  then
  dup h# 8000.0000 and if
  cr .me." xbc-error " .h cr
  else drop
  then
;

\ The address space of the XBox in transparent mode may be dynamically
\ allocated across its plug-in slots. This is called the
\ upper-address-decode-map (uadm). Below is a table which relates the
\ slot configuration code which is programmed in hardware to the
\ allocation of address space for each slot. The number in each cell is
\ the number of address bits needed for the slot.

decimal
create slot-sizes-array
\ slot0 slot1 slot2 slot3 slot-config
Code Example 8-2  Extended Hierarchical Device Driver  (Continued)

```fcode
\ extended hierarchical device driver sample
23 c, 23 c, 23 c, 23 c, \ 00
23 c, 23 c, 23 c, 23 c, \ 01
23 c, 23 c, 23 c, 23 c, \ 02
23 c, 23 c, 23 c, 23 c, \ 03
25 c, 0 c, 0 c, 0 c, \ 04
 0 c, 25 c, 0 c, 0 c, \ 05
 0 c, 0 c, 25 c, 0 c, \ 06
 0 c, 0 c, 0 c, 25 c, \ 07
24 c, 24 c, 0 c, 0 c, \ 08
24 c, 0 c, 24 c, 0 c, \ 09
 0 c, 24 c, 24 c, 0 c, \ 0a
 0 c, 0 c, 0 c, 0 c, \ 0b
24 c, 23 c, 23 c, 0 c, \ 0c
23 c, 24 c, 23 c, 0 c, \ Od \ Overridden in code
23 c, 23 c, 24 c, 0 c, \ 0e \ Overridden in code
25 c, 0 c, 0 c, 0 c, \ 0f
26 c, 26 c, 26 c, 26 c, \ 10
26 c, 26 c, 26 c, 26 c, \ 11
26 c, 26 c, 26 c, 26 c, \ 12
26 c, 26 c, 26 c, 26 c, \ 13
28 c, 0 c, 0 c, 0 c, \ 14
 0 c, 28 c, 0 c, 0 c, \ 15
 0 c, 0 c, 28 c, 0 c, \ 16
 0 c, 0 c, 0 c, 28 c, \ 17
28 c, 28 c, 28 c, 28 c, \ 18
28 c, 28 c, 28 c, 28 c, \ 19
28 c, 28 c, 28 c, 28 c, \ 1a
28 c, 28 c, 28 c, 28 c, \ 1b
 0 c, 0 c, 0 c, 0 c, \ 1c
 0 c, 0 c, 0 c, 0 c, \ 1d
 0 c, 0 c, 0 c, 0 c, \ 1e
 0 c, 0 c, 0 c, 0 c, \ 1f
\ hex

20 constant /slot-sizes-array
-1 value slot-config

: >slot-size ( slot# -- size )
  slot-sizes-array slot-config la+ swap ca+ c@ 1 swap <<
  1 not and \ Could have slot size of 0.
;
\ This array is to be filled with offsets for each slot.
```
Code Example 8-2  Extended Hierarchical Device Driver (Continued)

\ extended hierarchical device driver sample  
\ Eg. 0, 100.0000, 180.0000, 200.0000
create host-offsets 0, 0, 0, 0,

: >host-offset ( child-slot# -- adr ) host-offsets swap na+ @ ;

create config-d-offsets h# 100.0000, 0, h# 180.0000, 0,  
create config-e-offsets h# 100.0000, h# 180.0000, 0, 0,

: set-host-offsets ( -- )  
slot-config case  
  h# d of config-d-offsets host-offsets 4/n* move exit endof  
  h# e of config-e-offsets host-offsets 4/n* move exit endof  
endcase  
0 ( initial-offset )  
4 0 do ( offset )  
  dup host-offsets i na+ ! ( offset )  
  i >slot-size + ( offset' )  
loop ( final-offset )  
drop  
;

: set-configuration ( config-code -- )  
is slot-config  
set-host-offsets  
slot-config 3 << my-key 8 << or  
dup write-xac-ctl0 \ set XAC  
  write-xbc-ctl0 \ set XBC  
slot-config encode-int " uadm" property \ publish slot configuration  
;

end0
Complete Hierarchical Device Driver

The complete driver includes all the required device node methods. It also includes code to initialize the hardware at system reset. In particular, it configures the allocation of address space across slots. It does this by either performing an autoconfiguration or by accepting a manual override via a property in its parent. During the configuration process, the driver interprets the FCode of any SBus card plugged into the XBox. This results in devices being added to the device tree.

Code Example 8-3  Complete Hierarchical Device Driver

```plaintext
\ complete hierarchical device driver sample
hex
fcode-version2

" SUNW,xbox"   name
" 501-1840"    model
" sbus"        device-type

\ XBox Registers

h# 0 constant write0-offset h# 4 constant /write0
h# 2.0000 constant xac-err-offset h# c constant /xac-err
h# 10.0000 constant xac-ctl0-offset h# 4 constant /xac-ctl0
h# 11.0000 constant xac-ctl1-offset h# 4 constant /xac-ctl1
h# 12.0000 constant xac-elua-offset h# 4 constant /xac-elua
h# 13.0000 constant xac-ella-offset h# 4 constant /xac-ella
h# 14.0000 constant xac-ele-offset h# 4 constant /xac-ele
h# 42.0000 constant xbc-err-offset h# c constant /xbc-err
h# 50.0000 constant xbc-ctl0-offset h# 4 constant /xbc-ctl0
h# 51.0000 constant xbc-ctl1-offset h# 4 constant /xbc-ctl1
h# 52.0000 constant xbc-elua-offset h# 4 constant /xbc-elua
h# 53.0000 constant xbc-ella-offset h# 4 constant /xbc-ella
h# 54.0000 constant xbc-ele-offset h# 4 constant /xbc-ele

: >reg-spec ( offset size -- xdrreg )
  >r my-address + my-space encode-phys r> encode-int encode+
;
write0-offset    /write0    >reg-spec
xac-err-offset   /xac-err   >reg-spec encode+
xac-ctl0-offset  /xac-ctl0  >reg-spec encode+
xac-ctl1-offset  /xac-ctl1  >reg-spec encode+
```
Memory-Mapped Buses

Code Example 8-3  Complete Hierarchical Device Driver (Continued)

\ complete hierarchical device driver sample
xac-elua-offset /xac-elua >reg-spec encode+
xac-ella-offset /xac-ella >reg-spec encode+
xac-ele-offset /xac-ele >reg-spec encode+
xbc-err-offset /xbc-err >reg-spec encode+
xbc-ctl0-offset /xbc-ctl0 >reg-spec encode+
xbc-ctl1-offset /xbc-ctl1 >reg-spec encode+
xbc-elua-offset /xbc-elua >reg-spec encode+
xbc-ella-offset /xbc-ella >reg-spec encode+
xbc-ele-offset /xbc-ele >reg-spec encode+
" reg" property

\ XBox can interrupt on any SBus level
1 encode-int  2 encode-int encode+  3 encode-int encode+  4 encode-int encode+
5 encode-int encode+  6 encode-int encode+  7 encode-int encode+
" interrupts" property

1 sbus-intr>cpu encode-int  0 encode-int encode+
2 sbus-intr>cpu encode-int encode+  0 encode-int encode+
3 sbus-intr>cpu encode-int encode+  0 encode-int encode+
4 sbus-intr>cpu encode-int encode+  0 encode-int encode+
5 sbus-intr>cpu encode-int encode+  0 encode-int encode+
6 sbus-intr>cpu encode-int encode+  0 encode-int encode+
7 sbus-intr>cpu encode-int encode+  0 encode-int encode+
" intr" property

\ XBox bus clock speed
d# 25.000.000 encode-int " clock-frequency" property

\ Burst sizes 64,32,16,8,4,2,1 bursts.
h# 7f encode-int " burst-sizes" property

\ XBox has no slave-only slots
0 encode-int " slave-only" property

\ Get the number of address bits for this SBus slot from the parent SBus
\ node without inheritance.  OpenBoot 2.5 doesn't publish slot-address-bits.
\ However 2.5 is only on 4m machines, which are all 28 bits per slot.

: $= ( addr1 len1 addr2 len2 -- equal? ) \ string compare
  rot over - if
  drop 2drop false \ different lengths


Code Example 8-3  Complete Hierarchical Device Driver (Continued)

\ complete hierarchical device driver sample
	else comp 0=
	then
;

: 4mhack ( -- n )	
	" compatible" get-inherited-property if
	
d# 25                                \ no "compatible" prop; assume 4c
	else decode-string " sun4m" $=  if
	
d# 28
	else
	
d# 25                             \ not sun4m
	then
	nip nip
	then
;

: #bits ( -- n )	
	" slot-address-bits" my-parent ihandle>phandle
	get-package-property if
	4mhack
	else
decode-int nip nip
	then
;

#bits constant host-slot-size
host-slot-size encode-int " slot-address-bits" property

\ Utility display string
: .me ( -- ) ." SBus " my-space .d ." XBox " ;

\ The XBox device has two modes opaque and transparent.

\ Upon reset the device is set to opaque mode. In this mode all
\ accesses to address space of the device are directed to the XBox H/W
\ (ie. XAdaptor Card or the XBox Expansion Box) itself.

\ In the transparent mode all accesses are mapped to the SBus cards
\ which are plugged into the XBox. In transparent mode the XBox H/W is
\ accessible only via the "write-0" register. To allow another bus
\ bridge to be plugged into the XBox all writes to the write-0 register
\ must contain a "key" which is programmed into the XBox H/W at boot
\ time. If the key field of a write to write-0 matches that programmed
\ at boot time the H/W intercepts the write. Otherwise the H/W passes
\ the write along.
The XBox has two sets of registers. Those of the XAdaptor card and those of the XBox Expansion Box.

Opaque mode host adapter registers
-1 value xac-err-regs
-1 value xac-ctl0  -1 value xac-ctl1
-1 value xac-elua
-1 value xac-ella
-1 value xac-ele

Opaque mode expansion box registers
-1 value xbc-err-regs
-1 value xbc-ctl0  -1 value xbc-ctl1
-1 value xbc-elua
-1 value xbc-ella
-1 value xbc-ele

Transparent mode register
-1 value write0-reg

\map-in ( offset space size -- virt") " map-in" $call-parent ;
\map-out ( virt size -- )              " map-out" $call-parent ;
\map-regs ( -- )
write0-offset  my-address + my-space /write0   xbox-map-in  to write0-reg
xac-err-offset my-address + my-space /xac-err   xbox-map-in  to xac-err-regs
xac-ctl0-offset my-address + my-space /xac-ctl0  xbox-map-in  to xac-ctl1
xac-ctl1-offset my-address + my-space /xac-ctl1  xbox-map-in  to xac-ctl1
xac-elua-offset my-address + my-space /xac-elua   xbox-map-in  to xac-elua
xac-ella-offset my-address + my-space /xac-ella   xbox-map-in  to xac-ella
xac-ele-offset  my-address + my-space /xac-ele    xbox-map-in  to xac-ele
xbc-err-offset my-address + my-space /xbc-err    xbox-map-in  to xbc-err-regs
xbc-ctl0-offset my-address + my-space /xbc-ctl0   xbox-map-in  to xbc-ctl1
xbc-ctl1-offset my-address + my-space /xbc-ctl1   xbox-map-in  to xbc-ctl1
xbc-elua-offset my-address + my-space /xbc-elua   xbox-map-in  to xbc-elua
xBC-ella-offset my-address + my-space /xbc-ella   xbox-map-in  to xbc-ella
xbc-ele-offset  my-address + my-space /xbc-ele    xbox-map-in  to xbc-ele

\unmap-regs ( -- )
write0-reg   /write0    xbox-map-out   -1 to write0-reg
xac-err-regs /xac-err   xbox-map-out   -1 to xac-err-regs
xac-ctl10  /xac-ctl10  xbox-map-out   -1 to xac-ctl10
xac-ctl11  /xac-ctl11  xbox-map-out   -1 to xac-ctl11
xac-elua  /xac-elua   xbox-map-out   -1 to xac-elua
xac-ella  /xac-ella   xbox-map-out   -1 to xac-ella
xac-ele  /xac-ele    xbox-map-out   -1 to xac-ele
xbc-err-regs /xbc-err   xbox-map-out   -1 to xbc-err-regs

Complete Hierarchical Device Driver (Continued)
Opaque mode register access words:

```plaintext
: xac-errd@ ( -- l )  xac-err-regs  rl@  ;
: xac-erra@ ( -- l )  xac-err-regs 4 + rl@  ;
: xac-errs@ ( -- l )  xac-err-regs 8 + rl@  ;
: xac-ctl0@ ( -- w )  xac-ctl0  rl@  ;
: xac-ctl0! ( w -- )  xac-ctl0  rl!  ;
: xac-ctl1@ ( -- w )  xac-ctl1  rl@  ;
: xac-ctl1! ( w -- )  xac-ctl1  rl!  ;
: xac-elua@ ( -- l )  xac-elua  rl@  ;
: xac-elua! ( l -- )  xac-elua  rl!  ;
: xac-ella@ ( -- w )  xac-ella  rl@  ;
: xac-ella! ( w -- )  xac-ella  rl!  ;
```

Transparent mode register access words:

```plaintext
\ \ Transparent mode register access words

external
: unique-key ( -- n ) " unique-key" $call-parent  ;
headers
unique-key constant my-key
my-key encode-int " write0-key" property

: xbox! ( w offset -- )  my-key  h# 18 << or or  write0-reg rl!  ;
```
Codec Example 8-3  Complete Hierarchical Device Driver (Continued)

\ complete hierarchical device driver sample
: write-xac-ctl0  ( w -- )  xac-ctl0-offset xbox! ;
: write-xac-ctl1  ( w -- )  xac-ctl1-offset xbox! ;
: write-xbc-ctl0  ( w -- )  xbc-ctl0-offset xbox! ;
: write-xbc-ctl1  ( w -- )  xbc-ctl1-offset xbox! ;

\ Some functionally oriented words
: set-key        ( -- )  my-key 8 <<  xac-ctl0!  ;
: transparent    ( -- )             1 xac-ctl1!  ;
: opaque         ( -- )        0 write-xac-ctl1 ;
: enable-slaves  ( -- )    h# 38 write-xbc-ctl1 ;

: xbox-errors  ( -- xbc-err xac-err )
  opaque xbc-errd@ xac-errd@  transparent  
  ;

: ?.errors  ( xbc-err xac-err -- )
  dup h# 8000.0000 and  if
    cr .me ." xac-error " .h cr
  else drop
  then
  dup h# 8000.0000 and  if
    cr .me ." xbc-error " .h cr
  else drop
  then

\ The address space of the XBox in transparent mode may be dynamically
\ allocated across its plug-in slots.  This is called the
\ upper-address-decode-map (uadm).  Below is a table which relates the
\ slot configuration code which is programmed in hardware to the
\ allocation of address space for each slot.  The number in each cell is
\ the number of address bits needed for the slot.

decimal
create slot-sizes-array
\ slot0 slot1 slot2 slot3  slot-config
 23 c, 23 c, 23 c, 23 c, \ 00
 23 c, 23 c, 23 c, 23 c, \ 01
 23 c, 23 c, 23 c, 23 c, \ 02
 23 c, 23 c, 23 c, 23 c, \ 03
 25 c,  0 c,  0 c,  0 c, \ 04
  0 c, 25 c,  0 c,  0 c, \ 05

Memory-Mapped Buses  143
\ complete hierarchical device driver sample
  0 c, 0 c, 25 c, 0 c, \ 06
  0 c, 0 c, 0 c, 25 c, \ 07
 24 c, 24 c, 0 c, 0 c, \ 08
 24 c, 0 c, 24 c, 0 c, \ 09
  0 c, 24 c, 24 c, 0 c, \ 0a
  0 c, 0 c, 0 c, 0 c, \ 0b
 24 c, 23 c, 23 c, 0 c, \ 0c \ Overridden in code
 23 c, 24 c, 23 c, 0 c, \ 0d \ Overridden in code
 23 c, 23 c, 24 c, 0 c, \ 0e \ Overridden in code
 25 c, 0 c, 0 c, 0 c, \ 0f
 26 c, 26 c, 26 c, 26 c, \ 10
 26 c, 26 c, 26 c, 26 c, \ 11
 26 c, 26 c, 26 c, 26 c, \ 12
 26 c, 26 c, 26 c, 26 c, \ 13
 28 c, 0 c, 0 c, 0 c, \ 14
  0 c, 28 c, 0 c, 0 c, \ 15
  0 c, 0 c, 28 c, 0 c, \ 16
  0 c, 0 c, 0 c, 28 c, \ 17
 28 c, 28 c, 28 c, 28 c, \ 18
 28 c, 28 c, 28 c, 28 c, \ 19
 28 c, 28 c, 28 c, 28 c, \ 1a
 28 c, 28 c, 28 c, 28 c, \ 1b
  0 c, 0 c, 0 c, 0 c, \ 1c
  0 c, 0 c, 0 c, 0 c, \ 1d
  0 c, 0 c, 0 c, 0 c, \ 1e
  0 c, 0 c, 0 c, 0 c, \ 1f

hex

20 constant /slot-sizes-array
-1 value slot-config

: >slot-size ( slot# -- size )
  slot-sizes-array slot-config la+ swap ca+ c@ 1 swap <<
  1 not and \ Could have slot size of 0.

;"
Code Example 8-3  Complete Hierarchical Device Driver (Continued)

\ complete hierarchical device driver sample
create config-e-offsets h# 100.0000 , h# 180.0000 , 0           , 0 , 0 ,

: set-host-offsets ( -- )
  slot-config case
  h# d of config-d-offsets host-offsets 4 /n* move exit endof
  h# e of config-e-offsets host-offsets 4 /n* move exit endof
endcase
0                               ( initial-offset )
4 0 do                         ( offset )
  dup host-offsets i na+ !     ( offset )
  i >slot-size +               ( offset' )
loop                            ( final-offset )
drop
;

: set-configuration ( config-code -- )
  is slot-config
  set-host-offsets
  slot-config 3 << my-key 8 << or
  dup write-xac-ctl0 \ set XAC
  write-xbc-ctl0 \ set XBC
  slot-config encode-int " uadm" property \ publish slot configuration
;
\ Required package methods
external

dma-alloc ( #bytes -- )  " dma-alloc" $call-parent ;
dma-free ( #bytes -- )   " dma-free" $call-parent ;
dma-map-in ( vaddr #bytes cache? -- devaddr ) " dma-map-in" $call-parent ;
dma-map-out ( vaddr devaddr #bytes -- )    " dma-map-out" $call-parent ;
dma-sync ( virt devaddr #bytes -- )         " dma-sync" $call-parent ;

map-in ( offset slot# size -- virtual )
  >r ( offset xbox-slot# )
  >host-offset + my-space ( parent-offset parent-slot# )
  r> " map-in" $call-parent ( virtual )
;

map-out ( virt size -- ) " map-out" $call-parent ;

decode-unit ( adr len -- address space )
Code Example 8-3  Complete Hierarchical Device Driver  (Continued)

\ complete hierarchical device driver sample
  \ dup 0 3 between 0= if
  \ " Invalid XBox slot number " .d cr
  \ 1 abort
  \ then ( offset slot# )
  \ ;

\ Hack because set-args and byte-load are not FCodes
: byte-load ( adr len -- ) " byte-load" $find drop execute ;
: set-args ( adr len adr len -- ) " set-args" $find drop execute ;

  ['] decode-unit catch if
  2drop 2drop 2drop 2drop exit
  then ( arg-str reg-str fcode-offs,space )

  h# 10000 map-in
  \ dup cpeek if
  \ dup h# f0 = swap h# fd = or if
  \ new-device
  \ >r set-args r>
  \ dup 1 byte-load
  \ finish-device
  \ then
  \ else
  \ nip nip nip nip
  \ " Invalid FCode start byte in " .me cr
  \ then
  \ else
  \ nip nip nip nip
  \ then

  h# 10000 map-out
  ;

: open ( -- ok? ) true ;
: close ( -- ) ;

headers

\ The XBox slot configuration may be forced by the user. The mechanism...
This string is processed into the config bits array. Then the slot-sizes-array is searched for a configuration which matches or exceeds the requested number for each slot. If the request is unreasonable the default-slot-config is used. Then the configuration is set in the XBox hardware. Finally each slot is probed based on the config.

: default-slot-config ( -- n )
  host-slot-size d# 25 = if
    h# c \ 1x24 bits, 2x23 bits
  else h# 10 \ 4x26 bits
  then

This array to be filled with bit sizes for each slot. Eg. 24, 23, 23, 0 create config-bits 0 c, 0 c, 0 c, 0 c,

: config-ok? ( config -- ok? )
  true
  slot-sizes-array rot 4 * ca+ ( ok? slot-adr )
  4 0 do
    config-bits i ca+ c@
    over i ca+ c@ ( ok? slot-adr conf-bits slot-bits )
    > if
      nip false swap leave
    then
  loop
  drop

: fit-config ( -- config )
  default-slot-config
  /slot-sizes-array 0 do
    i config-ok? if
      drop i leave
    then
  loop

: megs>bits ( megs -- bits ) \ Convert requested megs to # of address bits
Code Example 8-3  Complete Hierarchical Device Driver (Continued)

\ complete hierarchical device driver sample
?dup 0= if 0 exit then
dup 9 < if drop d# 23 exit then
dup d# 17 < if drop d# 24 exit then
dup d# 33 < if drop d# 25 exit then
dup d# 65 < if drop d# 26 exit then
dup d# 129 < if drop d# 27 exit then
d# 257 < if d# 28 exit then
    d# 29 \ d#29 is too many bits => error

; : request-megs ( adr len -- ) \ Fill config-bits table
    base @ \r decimal
    4 0 do
        ascii , left-parse-string
        $number 0= if
            megs>bits config-bits i ca+ c!
        then
        loop
    2drop
    r> base !
;
: find-config ( adr len -- config )
    request-megs fit-config
;
create slot-string ascii # c, ascii , c, ascii 0 c,

: probe-slot ( slot# -- )
    dup >slot-size 0= if drop exit then ( slot# )
    ascii 0 + slot-string c!
    " " slot-string 3 ( arg-str reg-str )
    2dup ( arg-str reg-str fcode-str )
    probe-self
;
: probe-children ( -- )
    4 0 do
        config-bits i ca+ c@ if
            i probe-slot
        then
        loop
    ;
The Xbox slot configuration may be autoconfigured by the driver. The autoconfiguration mechanism uses the following state transition table. The table basically loops through each Xbox slot with a current guess at the slot config. With each slot the code then probes the slot's FCode and uses the reg property information of the slot's new device node to determine the amount of address space required by the slot. The slot config guess is updated and a state transition is made.

This is the state transition table. Each entry in the table consists of 16 bits. The most significant 8 bits is the Xbox configuration code for the next state, and the least 8 bits is the next state.

create states

\| Empty | min  | mid  | for 25 bit host SBus slot
---|------|------|-------------------------
0501 w, 0d04 w, 0803 w, \ 0 testing slot 0
0602 w, 0a05 w, 0a0f w, \ 1 Slot 0 empty, testing slot 1
0706 w, 000f w, 060e w, \ 2 Slots 0,1 empty, testing slot 2
090f w, 0c0f w, 080e w, \ 3 Slot 0 is 24 bit, testing slot 1
0e05 w, 0e05 w, 0d0f w, \ 4 Slot 0 23 bit, testing slot 1
000f w, 000f w, 0e0e w, \ 5 Slot 0 empty and Slot1 23 bit, or Slot 0,1 are 23 bit testing slot 2
0c0e w, 070e w, 070e w, \ 6 Slots 0,1,2 empty, testing slot 3
Empty notused 26 for 28 bit host SBus slot
1508 w, 100e w, 100b w, \ 7 testing slot 0
1609 w, 100e w, 100c w, \ 8 Slot 0 empty, testing slot 1
170a w, 100e w, 100d w, \ 9 Slots 0,1 empty, testing slot 2
100e w, 100e w, 170e w, \ a Slots 0,1,2 empty, testing slot 3
100c w, 100e w, 100c w, \ b Slot 0 is 26 bit, testing slot 1
100d w, 100e w, 100d w, \ c Slots 0,1 are 26 bit, testing slot 2
100e w, 100e w, 100e w, \ d Slots 0,1,2 are 26 bit, testing slot 3
\ e
\ f
0 value slot#
0 value start-state \ for auto-config state machine
\ complete hierarchical device driver sample
4 value start-config
h# 100.0000 value max-card \ 25 bit default
h# 080.0000 value mid-card \ 25 bit default

: configure25 ( -- ) \ 25 bit host SBus slots
  0 is start-state
  4 is start-config
h# 100.0000 is max-card \ 25 bits for one Xbox slot
h# 080.0000 is mid-card \ 24 bits per Xbox slot
;
: configure28 ( -- ) \ 28 bit host SBus slots
  7 is start-state
h# 14 is start-config
h# 800.0000 is max-card \ 28 bits for one Xbox slot
h# 0 is mid-card \ 26 bits per Xbox slot
;
0 value child-node

\ Since child and peer do not appear until 2.3,
\ we include the following workarounds.
: next-peer ( phandle -- phandle' )
  fcode-version 2.0003 >= if
      peer
  else
      " romvec" $find drop execute 1c + @ 0 + @
      " call" $find drop execute nip
  then
;
: first-child ( phandle -- phandle' )
  fcode-version 2.0003 >= if
      child
  else
      " romvec" $find drop execute 1c + @ 4 + @
      " call" $find drop execute nip
  then
;
0 value extent \ 1 if card exists, but no reg prop or 0 reg

: bump-extent ( n -- ) extent max is extent ;
\ complete hierarchical device driver sample
: max-reg-extent ( adr len -- )
  begin dup while
    decode-int drop decode-int >r decode-int r> + ( adr' len' extent)
    bump-extent
  repeat
  2drop
  extent 0= if \ reg prop is 0 -- fake it
    1 bump-extent
  then
  ;

: find-extent ( -- )
  0 is extent
  begin
    child-node if
    child-node next-peer
    else
      my-self ihandle>phandle first-child
      then ( next-child )
    ?dup while
      is child-node
      " reg" child-node get-package-property 0= if ( adr len )
      max-reg-extent
    else \ card has no reg prop -- fake it
      1 bump-extent
    then
    repeat
  ;

: evaluate-size ( -- size-code )
  find-extent
  extent slot# >slot-size > if
    ." The card in slot " slot# .
    ." of " .me
    ." uses too much address space." cr
    abort
  then
  extent ( max-extent )
  dup max-card > if drop 3 exit then ( max-extent ) \ max-size card
  dup mid-card > if drop 2 exit then ( max-extent ) \ mid-size card?
  0 > if 1 exit then ( ) \ 25-small card?
  0 \ null for 28
  ;

Code Example 8-3   Complete Hierarchical Device Driver (Continued)
\ complete hierarchical device driver sample

: test-slot ( xbox-config -- size-code )
  set-configuration ( )
  slot# probe-slot ( )
  evaluate-size ( size-code )

;  

: autoconfigure ( -- )
  0 is child-node
  -1 is slot#
  host-slot-size d# 25 = if configure25 else configure28 then

  start-state start-config ( state# xbox-config )
  begin ( state# xbox-config )
    slot# 1+ is slot# test-slot ( state# size-code )
    dup 3 = if 2drop exit then ( state# size-code )
    over h# f = if 2drop exit then ( state# size-code )
    states rot 3 * wa+ swap wa+ w@ wbsplit ( state#' xbox-config' )
    over h# e = until ( state#' xbox-config' )
  2drop

;  

\ Initialize the XBox H/W. If the XAdaptor H/W detects that XBox
\ Expansion H/W is connected and powered-up it puts the H/W into
\ transparent mode and sets the XBox slot configuraton based on either a
\ forced configuration or the autoconfiguration algorithm.

: configuration ( -- )
  " xbox-slot-config" get-inherited-property 0= if
    decodestring ( adr len adr len )
    find-config forced-configuration
    2drop
  else
    2drop
    autoconfigure
    then

;  

: null-xdr ( -- adr len )
  fcode-version 2.0001 >= if
    0 0 encodebytes
  else
    0 verb

" Code Example 8-3  Complete Hierarchical Device Driver (Continued)
\ complete hierarchical device driver sample
  else
    here 0
  then

: make-ranges  ( -- )
  null-xdr  ( adr len )
  4 0 do
    i >slot-size if  ( adr len )
    0 i encode-phys encode+  ( adr len )
    i >host-offset my-space encode-phys encode+  ( adr len )
    i >slot-size encode-int encode+  ( adr len )
    then
  loop
  " ranges" property

;  

\ Because we go transparent in the middle and therefore the fcode prom
\ disappears the following must be in a definition.

: init-pkg  ( -- )
  map-reg
  set-key \ opaque already
  xac-errors h# 40 and if \ Child ready?
    transparent \ Go transparent, then enable-slaves
    enable-slaves
    configuration
    make-ranges
    xbox-errors
    ?.errors
    " true"
  else
    cr .me
    " child not ready --" cr
    " perhaps the cable is not plugged in" cr
    " or the expansion box is not turned on." cr
    " false"
  then
  encodestring " child-present" property
  unmap-reg
  ['] end0 execute

;
Code Example 8.3  Complete Hierarchical Device Driver (Continued)

```c
\ complete hierarchical device driver sample
init-pkg
end0
```
This chapter describes how to implement network device drivers.

Network devices are packet-oriented devices capable of sending and receiving packets addressed according to IEEE 802.2 (Ethernet). OpenBoot firmware typically uses network devices for diskless booting. The standard `/obp-tftp` support package assists in the implementation of the load method for this device type.

Normally the network device driver would have a one level tree or a two level tree. The user can create a multi-level tree by applying `new-device` and `finish-device`.

A one level tree could have several nodes, depending on how many net channels the plug-in card can support, each node corresponds to one net channel.

This chapter shows three sample network device drivers for the Quad Ethernet device card. The device tree structure for the examples is as follows:
Each QED SBus card defines two levels:

- *one qec device node*
- *four qe device nodes*

The general pathname (after *sbus* or *sbi* on *sun4d* such as *SS2000* and *SC1000*)

\[
\text{sbus} \text{ (or } \text{sbi on sun4d such as SS2000 and SC1000) }
\]

\[
\text{qec@S,20000/qe@C,0}
\]

where *S* is the SBus slot number and *C* is the network channel number.

**Required Methods**

The network device FCode must declare the network device-type, and must implement the methods `open` and `close`, as well as the following methods:

\[
\text{load (addr -- len)}
\]

Read the default stand-alone program into memory starting at *addr* using the default network booting protocol. *len* is the size in bytes of the program read in.
read ( addr len -- actual )

Receive a network packet, placing at most the first len bytes in memory at addr. Return the actual number of bytes received (not the number copied), or -2 if no packet is currently available. Packets with hardware-detected errors are discarded as though they were not received. Do not wait for a packet (non-blocking).

write ( addr len -- actual )

Transmit the network packet of size len bytes starting at memory address addr. Return the number of bytes actually transmitted. The packet must be complete with all addressing information, including source hardware address.

Required Device Properties

The required properties for a network device are:

<table>
<thead>
<tr>
<th>Name</th>
<th>Typical Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>name</td>
<td>&quot; SUNW,my-net&quot;</td>
</tr>
<tr>
<td>reg</td>
<td>list of registers [device-dependent]</td>
</tr>
<tr>
<td>device_type</td>
<td>&quot; network&quot;</td>
</tr>
<tr>
<td>mac-address</td>
<td>8 0 0x20 0x0c 0xea 0x41 [the MAC address currently being used.]</td>
</tr>
</tbody>
</table>

Optional Device Properties

Several other properties may be declared for network devices:

<table>
<thead>
<tr>
<th>Property Name</th>
<th>Typical Property Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>max-frame-size</td>
<td>0x4000</td>
</tr>
<tr>
<td>address-bits</td>
<td>48</td>
</tr>
<tr>
<td>slave-burst-sizes</td>
<td>0x7f [depends on the number of entries in the reg property]</td>
</tr>
<tr>
<td>local-mac-address</td>
<td>8 0 0x20 0x0c 0xea 0x41 [the built-in Media Access Control address.]</td>
</tr>
</tbody>
</table>
Device Driver Examples

Simple Network Device Example

At minimum, a network device driver need only provide the desired tree structure and to publish all the necessary properties to identify the devices.

Code Example 7   QED Identification ROM Sample

\ QED Identification ROM
\ qed-idrom.fth
fcode-version 1

fload board.fth
headers
: copyright ( -- )
  ." Two-level QED-IDROM 1.1 " cr
  ." Copyright 1992 - 1995 Sun Microsystems, Inc.. All Rights Reserved" cr
;

: identify-qed ( -- )
create-qec-properties
  4 0  do
    new-device
    i create-qec-properties
    finish-device
  loop
;
identify-qed
end0

\ -----------------------------------------------------------------
\ board.fth
\ To define required properties for QED devices.
headers
my-address  constant my-sbus-addr
my-space    constant my-sbus-space
headerless
\ QED identification ROM
\ Define the address map.
\ MED Address Map PA[18:0] (totally 512KB address space).
\ h# 00.0000 constant eprom-pa
\ h# 00.8000 constant /eprom \ 32KB used, 64KB total
    h# 01.0000 constant mace-regs-offset
    h# 01.0000 constant mace0-base
    h# 01.4000 constant mace1-base
    h# 01.8000 constant mace2-base
    h# 01.c000 constant mace3-base
    h# 00.4000 constant /mace-regs \ 16KB per channel, 64KB total
    h# 02.0000 constant /global-regs \ 64KB total
    h# 01.0000 constant global-regs-offset
    h# 01.0000 constant mace0-base
    h# 03.0000 constant mace1-base
    h# 03.8000 constant mace2-base
    h# 03.c000 constant mace3-base
    h# 00.4000 constant /channel-regs \ 16KB per channel, 64KB total
    h# 04.0000 constant locmem-pa
    h# 01.0000 constant /locmem \ 64KB used, 256KB total
\ Real size of mace/qec-global/qec-channel registers.
  20 constant /qec-mace-regs
  14 constant /qec-global-regs
  34 constant /qec-channel-regs
\ Miscellaneous constant definitions.
  1 constant #channels
    h# 4000 constant max-frame-size \ ( d# 1536 for le )
    d# 48 constant address-bits
\ Hardwired SBus interrupt level for MED.
  4 constant sbus-que-intr

: encode-reg ( addr space size -- addr len ) >r encode-phys r> encode-int encode+ ;

: encode-ranges ( offs bustype phys offset size -- addr len )
    >r >r >r encode-phys r> r> r> encode-reg encode+ ;

: offset>physical-addr ( offset -- paddr.lo paddr.hi )
    my-sbus-addr + my-sbus-space

headers
Code Example 7  QED Identification ROM Sample  (Continued)

\ QED identification ROM
: create-qec-properties  ( -- )
  " qec"  name
  " SUNW,595-3198" encode-string " model" property  \ 595-3198-01
  global-regs-offset offset>physical-addr /global-regs encode-reg
  locmem-pa offset>physical-addr /locmem encode-reg encode+
  " reg" property

 0 0 channel0-base offset>physical-addr /channel-regs encode-ranges
0 1 channel1-base offset>physical-addr /channel-regs encode-ranges encode+  0 2 channel2-base offset>physical-addr /channel-regs encode-ranges encode+  0 3 channel3-base offset>physical-addr /channel-regs encode-ranges encode+  0 h# 10 mace0-base offset>physical-addr /mace-regs encode-ranges encode+  0 h# 11 mace1-base offset>physical-addr /mace-regs encode-ranges encode+  0 h# 12 mace2-base offset>physical-addr /mace-regs encode-ranges encode+  0 h# 13 mace3-base offset>physical-addr /mace-regs encode-ranges encode+  " ranges" property

#channels encode-int " #channels" property
\ One interrupt per qec, not one interrupt per channel.
  sbus-qe-intr encode-int " interrupts" property

; : create-qe-properties  ( chan# -- )
  >r
  " qe" encode-string " name" property
  r@ encode-int " channel#" property
  max-frame-size encode-int " max-frame-size" property
  address-bits encode-int " address-bits" property
  0 r@ /channel-regs encode-reg
  0 r@ h# 10 + /mace-regs encode-reg encode+  " reg" property
  r> drop

;
Sample Driver With Test and Debugging Methods

This version of a network device driver is still non-bootable, but it shows how an intermediate step of driver can be used to debug and test the device during or after development.

The coding techniques shown in this and the following examples are:

- Each qe node has the same set of instance variables as each of the other qe nodes.
- All the qe nodes use the same qe driver source code defined in the first qe node (qe0).

Code Example 8  QED Test ROM Sample

```fth
def cc job

fcode-version2
headers
fload board.fth
: copyright ( -- )
  " QED-TEST 1.1 " cr
  " Copyright 1992 - 1995 Sun Microsystems, Inc.. All Rights Reserved" cr
;
: instance ( -- ) fcode-revision 20001 >= if instance then;
\ Create qec device node.
  create-qec-properties
  fload qec-test.fth \ qec test code.
\ Create qe0 device node.
  new-device
    0 create-qe-properties
    : dma-sync ( virt-addr dev-addr size -- ) " dma-sync" $call-parent ;

\ ***** qe0 instance variables *****
0 instance value mace \ virtual address of Mace registers base
0 instance value qecc \ virtual address of Qec channel registers base
instance variable my-channel# \ qe channel#
  my-channel# off
  fload qe-test.fth \ qe test code.
\ ***** qe0 external methods *****
```
Code Example 8  QED Test ROM Sample (Continued)

```
\ QED test ROM.
  external
    : selftest ( -- fail? )
      qe0-selftest
    ;
    : open ( -- okay? )
      qe0-open
    ;
    : close ( -- )
      qe0-close
    ;
    : reset ( -- )
      qe0-reset
    ;
    headers
  finish-device

\ Create qe1 device node.
  new-device
    1 create-qe-properties

\ ***** qe1 instance variables *****
  0 instance value mace \ virtual address of Mace registers base
  0 instance value qecc \ virtual address of Qec channel registers base
  instance variable my-channel# \ qe channel#
  my-channel# off

\ ***** qe1 external methods *****
  external
    : selftest ( -- fail? )
      qe0-selftest
    ;
    : open ( -- okay? )
      qe0-open
    ;
    : close ( -- )
      qe0-close
    ;
    : reset ( -- )
      qe0-reset
    ;
    headers
  finish-device
```
\ QED test ROM.
\ Create qe2 device node.
   new-device
   2 create-qe-properties

\ ***** qe2 instance variables *****
0 instance value mace \ virtual address of Mace registers base
0 instance value qecc \ virtual address of Qec channel registers base
instance variable my-channel# \ qe channel#
my-channel# off

\ ***** qe2 external methods *****
external
: selftest  ( -- fail? )
  qe0-selftest
;
: open  ( -- okay? )
  qe0-open
;
: close  ( -- )
  qe0-close
;
: reset  ( -- )
  qe0-reset
;
headers
finish-device

\ Create qe3 device node.
   new-device
   3 create-qe-properties

\ ***** qe3 instance variables *****
0 instance value mace \ virtual address of Mace registers base
0 instance value qecc \ virtual address of Qec channel registers base
instance variable my-channel# \ qe channel#
my-channel# off

\ ***** qe3 external methods *****
external
: selftest  ( -- fail? )
  qe0-selftest
;
: open  ( -- okay? )

Code Example 8  QED Test ROM Sample  (Continued)

\ QED test ROM.
  qe0-open
  ;
  : close  ( -- )
    qe0-close
  ;
  : reset  ( -- )
    qe0-reset
  ;
  headers
  finish-device

end0

\ ---------------------------------------------------------------------
\ qec-test.fth
\ Test code for the qec node.

/locmem #channels / value chmem
chmem 2/ value rxbufsize

\ ***** qed utility (from qed-util.fth) *****

: lwrt-rd-cmp ( mask data addr -- success? )
  2dup rl! rl@ rot and =
  ;
: cwrt-rd-cmp ( mask data addr -- success? )
  2dup rb! rb@ rot and =
  ;
instance defer wrt-rd-cmp
' lwrt-rd-cmp to wrt-rd-cmp
d# 32 instance value #bits

external
: wlk-test ( mask addr #bits -- success? )
  dup to #bits
  d# 32 = if ['] lwrt-rd-cmp else ['] cwrt-rd-cmp then to wrt-rd-cmp
  true -rot    ( true mask addr )
  #bits 0
  do
    ( flag0 mask addr )
    over l i lshift and ?dup if
      ( flag0 mask addr data )
      >r 2dup r> swap wrt-rd-cmp false =
      ( flag0 mask addr flag )
    if
    rot drop false -rot leave then
  then
```plaintext
\ QED test ROM.
   loop
   2drop
;

headers
instance variable ms-timeout

external
  : set-ms-timeout ( #ms -- ) ms-timeout !
  : ms-timeout? ( -- flag )
    ms-timeout @ dup if
    1- ms-timeout ! 1 ms false
    else
    drop true
    then
;
headers

\ ***** qec global register (from global.h.fth) *****
\ QEC Global register set.
\ Virtual addresses of QEC global registers.
\ The actual addresses will be assigned later.
0 instance value qecg

hex
\ global control register (RW)
  : qecg-control ( -- vaddr ) qecg ;
  : qecg-control@ ( -- data ) qecg-control rll ;
  : qecg-control! ( data -- ) qecg-control rl! ;

headers
\ For Global Control Register.
f000.0000 constant gcr-mode \ Mode mask
4000.0000 constant gcr-mace \ Mace mode
1 constant gcr-reset \ Reset bit (0), 1 to enable reset.

headers

\ ***** qec map (from qecmap.fth ) *****
```
\ QED test ROM.
0 instance value locmem-base
false value dma-sync?
0 value dma-sync-addr

: find-dma-sync ( -- )
  "dma-sync" my-parent ihandle>phandle find-method if
  true to dma-sync?
  to dma-sync-addr
  then
;
find-dma-sync

external
: decode-unit ( addr len -- address space ) decode-2int ;
: map-in ( offset slot# #bytes -- virtual ) " map-in" $call-parent ;
: map-out ( addr len -- ) " map-out" $call-parent ;
: dma-map-in ( vaddr n cache? -- devaddr ) " dma-map-in" $call-parent ;
: dma-map-out ( vaddr devaddr n -- ) " dma-map-out" $call-parent ;
: dma-alloc ( size -- addr ) " dma-alloc" $call-parent ;
: dma-free ( addr size -- ) " dma-free" $call-parent ;

\ Dma-sync could be dummy routine if parent device doesn’t support.
\ sun4c ROMs may not support it.
: dma-sync ( virt-addr dev-addr size -- )
  dma-sync? if
    dma-sync-addr my-parent call-package
  else
    3drop
  then
;
headers

: map-qec-regs ( -- )
  global-regs-offset my-sbus-addr + my-sbus-space /qec-global-regs
  " map-in" $call-parent to qecg
;
: unmap-qec-regs ( -- )
  qecg /qec-global-regs " map-out" $call-parent
  0 to qecg
;
: map-locmem ( -- )
\ QED test ROM.
locmem-pa my-sbus-addr + my-sbus-space /locmem
" map-in" $call-parent to locmem-base
;
: unmap-locmem ( -- )
locmem-base /locmem " map-out" $call-parent
0 to locmem-base
;

\ ***** qec test (from qectest.fth) *****
hex
headerless
\ 18 constant /qec-global-regs
\ Define the mask bits that can be tested for each global register.
create gl-reg-masks
0000.001e , 0000.0000 , 0000.0000 , 0001.e000 ,
0000.f000 , 0000.f000 ,
\ Test Qec global registers.
: gl-reg-test ( -- success? )
true
/qec-global-regs 0 do                       ( flag0 )
   gl-reg-masks i + @
   qecq i + d# 32 wlk-test                   ( flag0 flag )
false = if drop false leave then ( flag0 )
//n +loop
;
\ Perform register test for the qec node.
: qec-reg-test ( -- success? )
diagnostic-mode? if
   ." Qec register test -- "
   then
   gl-reg-test
diagnostic-mode? if
      dup if ." succeeded." else ." failed." then cr
   then
; headers

\ ***** qec package *****
\ QED test ROM.

: reset-qec-global ( -- fail? )
  gcr-reset qecg-control! \ Issue global reset.
  d# 100 set-ms-timeout
  begin
    qecg-control@ gcr-reset and
  while
    ms-timeout? if ." Global reset failed" cr true exit then
    repeat
    false
  ;

: identify-chip ( -- okay? )
  qecg-control@ gcr-mode and gcr-mace =
  ;

external

: open ( -- true )
  map-qec-regs
  identify-chip dup 0= if
    unmap-qec-regs
  then
  ;

: close ( -- )
  qecg if unmap-qec-regs then
  ;

: selftest ( -- fail? )
  qecg          ( qecg )
  map-qec-regs
  qec-reg-test   ( qecg success? )
  unmap-qec-regs
  swap to qecg    ( success? )
  0=              ( fail? )
  ;

: reset ( -- )
  qecg
  map-qec-regs
  reset-qec-global drop
  unmap-qec-regs
  to qecg
  ;
\ QED test ROM.
headers

\ ---------------------------------------------------------------
\ qe-test.fth
\ Test code for the qe node.

: wlk-test ( mask addr #bits -- success? ) " wlk-test" $call-parent ;
: set-ms-timeout ( #ms -- ) " set-ms-timeout" $call-parent ;
: ms-timeout? ( -- flag ) " ms-timeout?" $call-parent ;

\ ***** qe map (from qemap.fth) *****
headers
\ instance variable my-channel# my-channel# off
: my-channel#! ( channel# -- ) my-channel# ! ;

: my-chan# ( -- channel# )
  my-channel# @ ;

: mace-regs ( -- devaddr space size )
  my-sbus-addr mace-regs-offset + /mace-regs my-chan# * +
  my-sbus-space /qec-mace-regs ;

: map-mace ( -- )
  mace-regs " map-in" my-parent $call-method to mace ;

: unmap-mace ( -- )
  mace /qec-mace-regs " map-out" my-parent $call-method
  0 to mace ;

: channel-regs ( -- devaddr space size )
  my-sbus-addr channel-regs-offset + /channel-regs my-chan# * +
  my-sbus-space /qec-channel-regs ;

: map-channel ( -- )
  channel-regs " map-in" my-parent $call-method to qecc ;

: unmap-channel ( -- )
  qecc /qec-channel-regs " map-out" my-parent $call-method
  0 to qecc ;
\ QED test ROM.
;
: map-chips ( -- )
  mace 0= if \ Do mapping if it is unmapped.
    map-mace
    map-channel
  then
;
: unmap-chips ( -- )
  mace if \ Do unmapping if it is mapped.
    unmap-channel
    unmap-mace
  then
;
\ ***** qe test (from qeregtest.fth) *****

hex
\ Define the mask bits that can be tested for each register.
create ch-reg-masks
  0000.0004 , 0000.0000 , ffff.f800 , ffff.f800 ,
  0000.0001 , 0000.0001 , 001f.001f , 001f.001f ,
  0000.fffe , 0000.fffe , 0000.fffe , 0000.fffe ,
  0000.00ff ,
create mace-reg-masks
  00 c, 00 c, 89 c, 00 c, 00 c, 0d c, 00 c, 00 c,
  00 c, 67 c, 00 c, 70 c, f3 c, ef c, 04 c, 05 f c,
  00 c, 00 c, 00 c, 00 c, 00 c, 00 c, 00 c, 00 c,
  00 c, 00 c, 00 c, 00 c, 00 c, 00 c, 00 c,

\ Test Qec per channel registers.
: ch-reg-test ( -- flag )
  true
  /qec-channel-regs 0 do                       ( flag0 )
    ch-reg-masks i + @
    qecc i + d# 32 wlk-test                   ( flag0 flag )
  false = if drop false leave then       ( flag0 )
/n +loop
;\ Test Mace registers.
: mace-reg-test ( -- flag )
  true
\ QED test ROM.
/qec-mace-regs 0 do ( flag0 )
  mace-reg-masks i + c@ ( flag0 flag )
  mace i + 8 wlk-test
false = if drop false leave then ( flag0 )
loop
;
\ Perform register test for the qe node.
: qe-reg-test ( -- success? )
diagnostic-mode? if
  " Qe register test -- "
then
  ch-reg-test
  mace-reg-test and
  diagnostic-mode? if
    dup if ." succeeded." else ." failed." then cr
  then
;
\ ***** qe0 package *****

headerless
\ For MACE BIU Configuration Control (R11). (RW)
01 constant m-swrst \ software reset
: mace-biucc ( -- vaddr ) h# 0b mace + ;
: mace-biucc@ ( -- data ) mace-biucc rb@ ;
: mace-biucc! ( data -- ) mace-biucc rb! ;
\ For QEC per channel control reg. (RW)
02 constant c-rst
: qecc-control ( -- vaddr ) qecc ;
: qecc-control@ ( -- data ) qecc-control rl@ ;
: qecc-control! ( data -- ) qecc-control rl! ;

headers
: set-my-channel# ( -- )
\ If don't find the channel property, use 0.
  " channel#" get-my-property if 0 else decode-int nip nip then
  my-channel#!
;
\ Reset (or stop) the qec channel.
\ Issue a soft reset to the desired Mace.
\ Then issue a soft reset to the desired channel in QEC.
\ Chip reset algorithm:
\ QED test ROM.
\ Set the reset bit then wait until the reset bit cleared.
\ Timeout in 0.1 sec if fail.
\ : channel-reset ( -- fail? )
  m-swrs mace-biucc! \ Issue Mace reset.
  d# 100 set-ms-timeout
  begin
    mace-biucc@ m-swrs and
  while
    ms-timeout? if ." Cannot reset Mace" cr true exit then
  repeat
  c-rst qecc-control! \ Reset QEC channel registers.
  d# 100 set-ms-timeout
  begin
    qecc-control@ c-rst and
  while
    ms-timeout? if ." Cannot reset QEC channel" cr true exit then
  repeat
  false

external
: qe0-selftest ( -- flag ) \ Flag 0 if passes test.
  set-my-channel#
  map-chips
  qe-reg-test ( success? )
  unmap-chips
  0= ( fail? )

: qe0-open ( -- okay? )
  set-my-channel#
  mac-address drop 6 encode-string " mac-address" property
  true

: qe0-close ( -- )

: qe0-reset ( -- )
  set-my-channel#
  map-chips channel-reset drop unmap-chips

headers
Bootable Network Device Driver Example

The example below shows a complete version of a bootable network driver. It implements the selftest method callable by OpenBoot test commands and the watch-net method callable by OpenBoot watch-net and watch-net-all commands.

Code Example 9  QED Bootable Driver Sample

```plaintext
\ QED bootable driver
 \ qed.fth

fcode-version 1
headers
fload board.fth
: copyright ( -- )
  ." QED 1.1 " cr
  ." Copyright 1992-1995 Sun Microsystems, Inc. All Rights Reserved" cr
;
: instance ( -- ) fcode-revision 20001 >=
  if instance then
  ;;
  \ Create qec device node.
  create-qec-properties
  fload qec.fth \ qec driver.

\ Create qe0 device node.
new-device
  0 create-qe-properties
  " network" device-type
  fload qeinstance.fth \ qe instance variables.
  : dma-sync ( virt-addr dev-addr size -- ) " dma-sync" $call-parent ;
  fload qe.fth \ qe driver.
  fload qe-package.fth \ qe external methods.
  finish-device

\ Create qe1 device node.
new-device
  1 create-qe-properties
  " network" device-type
  fload qeinstance.fth \ qe instance variables.
  fload qe-package.fth \ qe external methods.
  finish-device
```
Create qe2 device node.
new-device
  2 create-qe-properties
    "network" device-type
  fload qeinstance.fth \ qe instance variables.
  fload qe-package.fth \ qe external methods.
finish-device

Create qe3 device node.
new-device
  3 create-qe-properties
    "network" device-type
  fload qeinstance.fth \ qe instance variables.
  fload qe-package.fth \ qe external methods.
finish-device
end0

---

Code Example 9  QED Bootable Driver Sample (Continued)

\ QED bootable driver
\ Create qe2 device node.
new-device
  2 create-qe-properties
    "network" device-type
  fload qeinstance.fth \ qe instance variables.
  fload qe-package.fth \ qe external methods.
finish-device

\ Create qe3 device node.
new-device
  3 create-qe-properties
    "network" device-type
  fload qeinstance.fth \ qe instance variables.
  fload qe-package.fth \ qe external methods.
finish-device
end0

\ qec.fth

/locmem #channels / value chmem
chmem 2/ value rxbufsize

fload qed-util.fth \ Not included, refer to example 2.
fload global.h.fth \ Not included.
fload qecmap.fth \ Not included, refer to example 2.
fload qectest.fth \ Not included, refer to example 2.

: reset-qec-global ( -- fail? )
gcr-reset qecg-control! \ Issue global reset.
d# 100 set-ms-timeout
begin
  qecg-control@ gcr-reset and
while
  ms-timeout? if ." Global reset failed" cr true exit then
repeat
false
false
;
: qec-init ( -- )
Code Example 9  QED Bootable Driver Sample (Continued)

\ QED bootable driver
  chmem qecg-memsize!
  rxbufsize qecg-rxsize!
  chmem rxbufsize - qecg-txsize!
  gcr-burst16 qecg-control! \ SBus parity disabled, Rx/Tx equal priority.
  ;
  : identify-chip ( -- okay? ) qecg-control@ gcr-mode and gcr-mace = ;
  
  external
  : open ( -- true )
    map-qec-reg
    identify-chip dup if
      qec-init
    else unmap-qec-reg
    then
    ;
  : close ( -- ) qecg if unmap-qec-reg then ;

  : selftest ( -- fail? )
    qecg
    map-qec-reg
    qec-reg-test ( qecg success? )
    unmap-qec-reg
    swap to qecg ( success? )
    0= ( fail? )
  ;

  : reset ( -- )
    qecg
    map-qec-reg
    reset-qec-global drop
    unmap-qec-reg
    to qecg
  ;

headers

\ qeinstance.fth
\ Define instance words for qe driver.

\ headerless
\ mace.h.fth:
0 instance value mace \ virtual address of Mace registers base
**Code Example 9**  
QED Bootable Driver Sample (Continued)

```c
/* QED bootable driver */
channel.h.fth:
0 instance value qecc \ virtual address of Qec channel registers base
qemap.fth:
  instance variable my-channel# \ qe channel#
  my-channel# off
qedcore.fth:
  \ CPU base address of tmd, rmd, tbuf, rbuf rings.
0 instance value cpu-dma-base \ base address of dma memory object viewed by cpu
0 instance value tmd0 \ transmit message descriptor#0
0 instance value rmd0 \ receive message descriptor#0
0 instance value tbuf0 \ base address of transmit buffer
0 instance value rbuf0 \ base address of receive buffers
  \ IO (or device) base address of tmd, rmd, tbuf, rbuf rings.
0 instance value io-dma-base \ base addr of dma memory object viewed by device
0 instance value io-tmd0 \ transmit message descriptor#0
0 instance value io-rmd0 \ receive message descriptor#0
0 instance value io-tbuf0 \ base address of transmit buffer
0 instance value io-rbuf0 \ base address of receive buffers
  \ Required total Dma buffer size for all rings.
0 instance value qe-dma-size \ Amount of memory mapped
  *** Define required variables ***
instance variable status \ Accumulated channel status word.
instance variable restart? \ Restart? flag on after serious error.
instance variable nextrmd \ Point to next rmd.
instance variable nexttmd \ tmd0 nexttmd !, never changes presently
instance variable mode \ To store loopback control & promiscuous info.
6 instance buffer: this-en-addr \ Contain ethernet address
instance defer .receive-error
instance defer .error
instance defer .transmit-error
  \ timed-receive.fth:
instance variable alarmtime
instance defer handle-broadcast-packet
qetest.fth:
instance variable qe-verbose? \ Flag for displaying diagnostic message.
  qe-verbose? off
instance variable ext-lbt? \ Flag for execution of external loopback test.
  ext-lbt? off
  \ qe0-package.fth:
6 instance buffer: macbuf \ Contain mac address.
0 instance value obp-tftp \ Contain ihandle of TFTP package.
instance variable qe-nbytes \ Buffer size of higher layer receiver.
instance variable qe-buf \ Buffer address of higher layer receiver.
```
headers
\ -----------------------------------------------
\ qe.fth
: wlk-test ( mask addr #bits -- success? ) " wlk-test" $call-parent ;
: set-ms-timeout ( #ms -- ) " set-ms-timeout" $call-parent ;
: ms-timeout? ( -- flag ) " ms-timeout?" $call-parent ;

fload mace.h.fth \ Not included.
fload channel.h.fth \ Not included.
fload qemap.fth \ Not included, refer to example 2.
fload qecore.fth
fload timed-receive.fth
fload qeregtest.fth \ Not included, refer to example 2.
fload qetest.fth
fload qe0-package.fth

\ -----------------------------------------------
\ qe0-package.fth
\ Define the required methods for the network qe driver

set-my-channel#
external
: read ( buf len -- -2 | actual-len )
  qe0-read ;
: write ( buf len -- actual-len )
  qe0-write ;
: selftest ( -- flag ) \ Flag 0 if passes test.
  qe0-selftest ;
: watch-net ( -- )
  qe0-watch-net ;
: load ( addr -- len )
  qe0-load ;
: open ( -- okay? )
  qe0-open ;
: close ( -- )
Code Example 9  QED Bootable Driver Sample (Continued)

\ QED bootable driver
\  qe0-close
; : reset ( -- )
  qe0-reset
;

headers

\ ---------------
\ qecore.fth
\ Main core of QEC/MACE per channel Tx/Rx drivers.
\ SQEC has the following features:
\  - Supports four independent IEEE 802.3 10BASE-T twisted pair interfaces.
\  - Supports SBus parity checking.
\  - Supports 32 bit of DVMA addressing.
\  - Automatic rejection/discard of receive/transmit packets
\    when receive/transmit suffers from errors.
\
headerless
\ *** Rx/Tx Ring Descriptor Layout ***

struct ( Rx/Tx Descriptor )
4 field >flags \ OWN, SOP, EOP, size/length
4 field >addr \ buffer address
( total-length ) constant /md

hex
\ Definition for >flag field.
\ Bit[10:0] - Rx for W is buffer size, Rx for R is byte count, Tx for W is byte count.
8000.0000 constant own \ For both Rx & Tx.
4000.0000 constant stp \ For Tx only.
2000.0000 constant enp \ For Tx only.
07ff constant lenmask

\ Value to write to message descriptor to enable it for use
enp stp or own or \ constant ready

\ *** buffer sizes and counts ***

\ Xmit/receive buffer structure.
This structure is organized to meet the following requirements:
- starts on an QEBURSTSIZE (64) boundary.
- qebuf is an even multiple of QEBURSTSIZE.
- qebuf is large enough to contain max frame (1518) plus QEBURSTSIZE for alignment adjustments.

Similar to the 7990 ethernet controller, the QEC and the Software driver communicate via ring descriptors. There are separate Rx & Tx descriptor rings of 256 entries. Unlike 7990 the number of descriptor entries is not programmable (fixed at 256 entries).

decimal
/md constant /rmd \ rmd size = 8
/md constant /tmd \ tmd size = 8
1792 constant /rbuf \ 7*256 receive buffer size at least 1518+128=1636
1600 constant /tbuf \ transmit buffer size
256 constant #rmds
256 constant #tmds
1 constant #tbufs \ Just allocate one buffer for transmitter buffer pool.
32 constant #rbufs \ # buffers allocated for receiver buffer pool.

#rmds /rmd * value /rmds
#tmds /tmd * value /tmds

headers
: restart?-on ( -- ) restart? on ;

\ Conversion between cpu dma address and io dma address.

\ buffer# to address calculations
: rmd#>rmdaddr ( n -- addr ) /rmd * rmd0 + ;
: rbuf#>rbufaddr ( n -- addr ) #rbufs mod /rbuf * io-rbuf0 + ;
: tmd#>tmdaddr ( n -- addr ) /tmd * tmd0 + ;
\ address to buffer# calculations
: rmdaddr>rmd# ( addr -- n ) rmd0 - /rmd / ;

\ *** Qe message descriptor ring access ***
\ Get current rx/tx message descriptor ring pointer (on CPU side).
: nextrmd@ ( -- cpu-rmd-addr ) nextrmd @ ;
\ QED bootable driver
: nexttmd@ ( -- cpu-tmd-addr ) nexttmd @ ;

\ get location of buffer
: addr@ ( rmd/tmd-addr -- buff-addr ) >addr rl@ ;

: status@ ( rmd/tmd-addr -- statusflag ) >flags rl@ ;

\ gets length of incoming message, receive only
: length@ ( rmdaddr -- messagelength ) >flags rl@ lenmask and ;

\ Set current rx/tx message descriptor ring pointer (on CPU side).
: nextrmd! ( cpu-rmd-addr -- ) nextrmd ! ;
: nexttmd! ( cpu-tmd-addr -- ) nexttmd ! ;

\ Store buffer address into message descriptor
: addr! ( buff-addr rmd/tmd-addr -- ) >addr rl! ;

\ Set length of message to be sent - transmit only
: length! ( length rmd/tmd-addr -- ) >flags rl! ;

\ *** Qe synchronization ***
\ Sync the message descriptor after cpu or device writes it.
: qesyncriopb ( md -- )
  dup cpu>io-addr /md           ( cpu-addr io-addr size )
  dma-sync
;  
\ Sync the transmitting/received buffer after cpu/device writes it.
: qesynccbuf ( md -- )
  dup addr@ dup io>cpu-addr swap     ( md cpu-buf-addr io-buf-addr )
  rot length@                     ( cpu-buf-addr io-buf-addr size )
  dma-sync
;  
\ The buffer was already put back, put the descriptor in the chip’s ready list
: give-buffer ( rmd/tmd-addr -- )
  dup >flags dup rl@ ready or swap rl!       ( md )
\ Sync the descriptor so the device sees it.
  qesyncriopb                           ( )
;  
\ *** Qe error handling ***
\ QED bootable driver
: get-qe-status ( -- channel-status )
   qecc-status@ status @ or dup status !
   ;

\ get receive errors, receive only
: rerrors@ ( -- errorsflag ) get-qe-status c-rerr-mask and ;

\ get transmit errors, transmit only
: xerrors@ ( -- errorsflag ) get-qe-status c-terr-mask and ;

\ Clear transmit/receive/all error flags
: clear-terrors ( -- ) status @ c-terr-mask not and status !
   ;
: clear-rerrors ( -- ) status @ c-rerr-mask not and status !
   ;
: clear-errors ( -- ) status off restart? off
   ;
: clear-tint ( -- ) status @ c-tint not and status !
   ;

\ *** Basic initialization routines ***
\ words to set loopback control mode in UTR(R29) & promiscuous mode in MACCC(R13)
\ Bit<7> to control promiscuous mode, Bits<2:1> to control loopback mode,
\ Bit<0> to test the cable connection.
1 constant m-cable

: set-loop-mode ( -- ) mode @ m-loop-mask and m-rpa or mace-utr!
   ;
: set-prom-mode ( -- ) mode @ m-prom and mace-maccc!
   ;
: check-cable-mode? ( -- flag ) mode @ m-cable =
   ;
: external-loopback? ( -- flag ) mode @ m-loop-mask and m-loop-ext =
   ;

\ Check existence of no-tpe-test property to initialize disable-tpe-link-test bit.
\ Enable tpe-link-test if the property doesn’t exist,
\ or disable tpe-link-test if the property exists.
: init-link-test ( -- )
   \ Disable link test for external loopback mode.
   external-loopback? if m-dlnktst mace-phycc! exit then
      " no-tpe-test" get-my-property if 0
   else 2drop m-dlnktst then
      mace-phycc!
   ;

\ Enable/disable tpe-link-test
: setup-link-test ( enable-flag -- )
   " no-tpe-test" " get-property" eval if
      \ Property doesn’t exist, already enabled.
      0= if 0 0 " no-tpe-test" property then
After doing a port select of the twisted pair port, the driver needs to give ample time for the MACE to start sending pulses to the hub to mark the link state up. Loop here and check of the link state has gone into a pass state.

: link-state-fail? ( -- fail? )
  d# 1000 set-ms-timeout
  begin
  mace-phycc@ m-lnkst and
  while
  ms-timeout? if
  check-cable-mode? if
  ." failed, transceiver cable problem? or check the hub." cr
  true
  else
  m-dlnktst mace-phycc!
  false
  then
  exit
  then
  repeat
  check-cable-mode? if ." passed." cr then
  false
  
: set-physical-address ( -- )
  m-addrchg mace-iac!
  begin mace-iac@ m-addrchg and 0= until
  m-phyaddr mace-iac!
  \ Store least significant byte first.
  this-en-addr 6 bounds do i c@ mace-paddr! loop
  0 mace-iac!
  
: set-address ( en-addr len -- )
  drop this-en-addr 6 move ;
\ QED bootable driver
: set-logaddr-filter ( -- )
m-addrchg mace-iac!
begin mace-iac@ m-addrchg and 0= until
m-logaddr mace-iac!
8 0 do 0 mace-laddrf! loop
0 mace-iac!
;
\ Reset (or stop) the qec channel.
\ Issue a soft reset to the desired Mace.
\ Then issue a soft reset to the desired channel in QEC.
\ Chip reset algorithm:
\ Set the reset bit then wait until the reset bit cleared.
\ Timeout in 0.1 sec if fail.
\
: channel-reset ( -- fail? )
m-swrst mace-biucc! \ Issue Mace reset.
d# 100 set-ms-timeout
begin
mace-biucc@ m-swrst and
while
ms-timeout? if ." Cannot reset Mace" cr true exit then
repeat
c-rst qecc-control! \ Reset QEC channel registers.
d# 100 set-ms-timeout
begin
qecc-control@ c-rst and
while
ms-timeout? if ." Cannot reset QEC channel" cr true exit then
repeat
false
;
\ Initialize a single message descriptor
: rmd-init ( rbufaddr rmdaddr -- )
/rbuf over length! \ Buffer length
addr! \ Buffer address
;
\ Set up the data structures necessary to receive a packet
: init-rxring ( -- )
Initially first N=#rbufs descriptors with one-to-one association with a
buffer are made ready, the rest (256-N) not ready, then turn on receiver.
Whenever a receive buffer is processed, the information is copied out,
the buffer will be linked to the ((current+N)%256) entry then make the
entry is ready. I.e. The window of N ready descriptor/buffer pair is
moving around the ring.

: enable-rxring ( -- )
  #rbufs 0 do i rmd#>rmdaddr give-buffer loop 

transmit buffer initialize routine
: init-txring ( -- )
  tmd0 nexttmd!
  #tmds 0 do io-tbuf0 i tmd#>tmdaddr addr! loop 

*** Receive packet routines ***

Utility words used in .rerr-text & .terr-text.
: bits ( mask #right-bits -- mask' right-bits )
  >r dup /n 8 * r@ - tuck << swap >> ( mask bits ; RS: #bits )
  swap r> >> swap ( mask' bits )

: 1bit ( mask -- mask' rightest-bit-value ) 1 bits ;
: .rerr-text ( -- )
  rerrs$  
  lbit if ." SBus Rx Error Ack " restart?-on then  
  lbit if ." SBus Rx Parity " restart?-on then  
  lbit if ." SBus Rx Late " restart?-on then  
  lbit if ." Data Buffer Too Small " then  
  lbit if ." Rx packet Dropped " then  
  lbit drop \ Skip drop error, happens all the time  
  lbit drop \ Skip receive interrupt bit.  
  lbit if ." CRC error " then  
  lbit if ." Framing error " then  
  lbit if ." MACE Rx Late Collision " then  
  lbit if ." MACE FIFO overflow " then
\ QED bootable driver
  lbit if ." MACE Missed Counter Overflow " then
  lbit if ." MACE Runt Counter Overflow " then
  lbit if ." MACE Rx Coll Counter Overflow " then
  lbit if ." Collision error " then
drop cr
;

: (.receive-error ( -- )
  rerrors@ if .rerr-text then
);
' (.receive-error to .receive-error
' (.receive-error to .error

: to-next-rmd ( -- )
  /rmd nextrmd +!
  nextrmd@ rmd0 - /rmds >= if  rmd0 nextrmd! then
;
\ *** Transmit packet routines ***

: to-next-tmd ( -- )
  /tmd nexttmd +!
  nexttmd@ tmd0 - /tmds >= if  tmd0 nexttmd! then
;
\ Ignores the size argument, and uses the standard buffer.
: get-buffer ( dummysize -- buffer )
drop nexttmd@ addr@     ( io-tbuf )
io>cpu-addr               ( cpu-tbuf )
;
\ Display time domain reflectometry information
\ : .tdr ( -- )

: .terr-text ( -- )
  xerrors@
  d# 16 bits drop        \ Skip the receiver bits.
  lbit if ." SBus Tx Error Ack " restart?-on then
  lbit if ." SBus Tx Parity " restart?-on then
  lbit if ." SBus Tx Late " restart?-on then
  lbit if ." QEC Chained Tx Descriptor Error " restart?-on then
  lbit if ." QEC Tx Retry Counter Overflow " then
  lbit drop              \ Skip transmit interrupt bit
\ QED bootable driver
  1bit if ." MACE >1518 Babble " then
  1bit if ." MACE Jabber " then
  1bit if ." MACE FIFO Underflow " then
  1bit if ." Tx Late Collision " then
  1bit if ." Too Many Retries " then
  1bit if ." Lost Carrier (transceiver cable problem?) " then
  1bit if ." Excessive Defer " then
  drop cr

\ print summary of any HARD errors
  : (.transmit-error ( -- )
    xerrors@ if .terr-text then
  );
  ' (.transmit-error to .transmit-error

\ Set up CPU page maps
  : map-qe-buffers ( -- )
    #rbufs /rbuf *
  \ 2KB (8*256) for tmds & 2KB (8*256) for rmds & 4KB for tbuf
  \ ie. one page for tmds & rmds, one page for tbuf, the rest for rbufs.
    h# 2000 +
    to qe-dma-size

  \ Allocate and map that space
  qe-dma-size dma-alloc ( dma-adr )

  \ Set the addresses of the various DMA regions used by the cpu.
  dup to cpu-dma-base
  dup to tmd0 h# 800 + ( next-address )
  dup to rmd0 h# 800 + ( next-address ) \ Enough for 256 entries
  dup to tbuf0 h# 1000 + ( next-address ) \ Enough for max packet
  to rbuf0   ( )
  tmd0 qe-dma-size false dma-map-in ( io-dma-addr )

  \ Set the addresses of the various DMA regions used by the qec chip.
  dup to io-dma-base
  dup to io-tmd0 h# 800 + ( next-address )
  dup to io-rmd0 h# 800 + ( next-address ) \ Enough for 256 entries
  dup to io-tbuf0 h# 1000 + ( next-address ) \ Enough for max packet
  to io-rbuf0   ( )

};
: unmap-qe-buffers ( -- )
Code Example 9    QED Bootable Driver Sample (Continued)

C QED bootable driver
  tmd0 io-tmd0 qe-dma-size dma-map-out
  tmd0 qe-dma-size dma-free
  0 to tmd0
;

\ *** Chips initialization routines ***

\ Initializes the QEC/Mace chips.
: channel-init ( -- fail? )
  \ *** Initialize QEC per channel registers.
  io-rmd0 qecc-rxring!
  io-tmd0 qecc-txring!
  c-rintmask qecc-rintmask!  \ Mask RINT.
  c-tintmask qecc-tintmask!  \ Mask XINT.
  my-chan# chmem * dup qecc-lmrxxwrite! dup qecc-mlxmread!
  rxbufsize + dup qecc-mltxwrite! qecc-mltxread!
  c-qecerrmask qecc-qecerrmask!
  c-macerrmask qecc-macerrmask!
\ *** Initialize MACE registers.
  0 mace-xmtfc!
  m-apadxmt mace-xmtfc!   \ Set auto pad transmit for transmit frame control
  0 mace-rcvfc!            \ Init. receive frame control.
\ Init. Interrupt Mask Register to mask rcvint & cerr and unmask xmtint
\ according QEC spec.
  m-cerrm m-rcvintm or mace-imr!
\ Init. Bus Interface Unit Configuration Control to transmit after 64 bytes
\ have been loaded & byte swap.
  m-xmtsp64 m-xmtspshift << m-bswp or mace-biuc!
\ Init. FIFO Conf Control to set transmit/receive fifo watermark update
  m-xmtfw16 m-rcvfw32 or m-rcvfwu or m-rccfwu or mace-fifocc!
  m-10base-t mace-plsc!
\ Select twisted pair mode.
  init-link-test   \ Init. tpe link test mode.
  set-physical-address \ Set mac address.
  set-logaddr-filter \ Set logical address filter.
  0 mace-iac!
  link-state-fail? \ Wait and check the link state marked up.
  mace-mpc@ drop \ Read to reset counter and to prevent an invalid int.
  set-loop-mode   \ Set UTR
  set-prom-mode   \ Set MACCC
  m-apadxmt not mace-xmtfc@ and mace-xmtfc!
  m-astrprcv not mace-rcvfc@ and mace-rcvfc!
Code Example 9  

\ QED bootable driver
\ Turn on the Mace, ready to tx/rx packets.
: enable-mace ( -- )
  m-enxmt m-enrcv or mace-macc@ or mace-macc!
;

\ *** Ethernet on/off routines ***
\ Initializes the QEC/Mace chips, allocating the necessary memory,
\ and enabling the transmitter and receiver.
: net-on ( -- flag ) \ true if net-on succeeds
  clear-errors
  mac-address set-address
  channel-reset 0= if
    init-txring
    init-rxring
    channel-init 0= dup if
      enable-rxring
      enable-mace
    then
  else  false
  then
;
\ Stop the activity of this net channel.
: net-off ( -- )  channel-reset drop init-link-test  ;

\ *** Main receive routines ***
\ Whenever a receive buffer is processed, the information is copied out,
\ the buffer will be linked to the ((current+N)%256)th entry then make the
\ entry is ready ie.the window of N ready descriptor/buffer pair is
\ moving around the ring.
\ If 256 (#rmds) is multiples of N (#rbufs=32), we don’t need to link the
\ next-ready-rmd with the current processed rx buffer dynamically. They can
\ be set at the initialization time statically. For run time, we just need
\ to make the ((current+N)%256)th rmd ready.
\ : return-buffer ( buf-handle -- )
  rmdaddr>rmd#  ( [io-rbuf] rmd# )
  #rbufs + #rmds mod  ( [io-rbuf] next-ready-rmd# )
  rmd#>rmdaddr  ( [io-rbuf] next-ready-rmd )
\ QED bootable driver
  dup addr@ over rmd-init \ ( next-ready-rmd ; Set length )
give-buffer \ ; Make it ready 
to-next-rmd \ \ Bump SW nextrmd to next one
;

: receive-ready? \ ( -- packet-waiting? )
  restart? @ if net-on drop then
  nextrmd@ \ \ ( rmd )
  \ Sync RMD before CPU looking at it.
  dup qesynciopb \ \ ( rmd )
  status@ own and 0= \ \ ( flag )
;

: receive \ ( -- buf-handle buffer len ) \ \ len non-zero if packet ok
  nextrmd@ dup addr@ \ \ ( rmd io-rbuf-addr )
io>cpu-addr \ \ ( rmd cpu-rbuf-addr )
over length@ \ \ ( rmd cpu-rbuf-addr len )
reerrors@ if
  .receive-error clear-reerrors
then
  dup if \ \ ( rmd cpu-rbuf-addr len )
  \ Sync the received buffer before CPU looking at it.
  nextrmd@ qesyncrebuf \ \ ( rmd cpu-rbuf-addr len )
then
;

\ *** Main transmit routines ***

: set-timeout \ ( interval -- ) get-msecs + alarmitime ! ;

: timeout? \ ( -- flag ) get-msecs alarmitime @ >= ;

: wait 10us\wait \ ( -- ) d# 10 begin 1- dup 0= until drop ;

\ Wait until transmission completed
: send-wait \ ( -- )
  \ Wait the packet to get to the local memory, ready for MACE to xmit.
  d# 2000 set-timeout \ \ 2 second timeout.
Code Example 9

QED Bootable Driver Sample (Continued)

\ QED bootable driver
   begin
      get-qe-status
      c-tint and \ Transmit interrupt bit set?
      timeout? or \ Or timeout?
   until
   timeout? if
      ." TINT was not set!" cr true exit
   then
   \ Transmit completion, sync TMD before looking at it.
   nexttmd@ dup qesynciopb \ tmd
   status@ own and if \ flag
      ." Tx descriptor still owned by QEC!" cr
   then
   \ Wait the packet to get to net, make sure at most one xmit packet in MACE FIFO.
   d# 1000 set-timeout \ 1 second timeout.
   begin
      10us-wait
      qecc-lmtxwrite@ qecc-lmtxread@ =
   timeout? or
   until
   timeout? if
      ." Tx packet not out to net!" cr
   then
   false
   ;

\ This send routine does not enforce the minimum packet length. It is
\ used by the loopback test routines.
: short-send ( buffer length -- error? )
   clear-tint \ Erase tint status bit.
   \ discard buffer address, assumes using nexttmd
   nip nexttmd@ \ tmd
   tuck length! \ Set length
   \ Sync the transmit buffer so the device sees it.
   dup qesyncbuf \ tmd
   give-buffer \ ; Give tmd to chip
   c-tmd qecc-control! \ Bang the chip, let chip look at it right away
   send-wait \ \ wait for completion
   xerrors@ dup if \ fail? error? )
      .transmit-error clear-terrors
   then or \ ( error? )
   to-next-tmd \ ( error? )
   restart? @ if net-on drop then \ ( error? )
\ QED bootable driver
  c-hard-terr-mask and ( hard-error? )
;
\ Transmit packet routine, no S/W retry on this layer.
: net-send ( buffer length -- error? ) \ error? is contents of chan-status
  d# 64 max \ force minimum length to be 64
  short-send ( error? )
;
\ -----------------------------------------------------------------
\ timed-receive.fth
\ Implements a network receive that will timeout after a certain interval.
decimal
: multicast? ( handle data-address length -- handle data-address length flag )
  \ Check for multicast/broadcast packets
  over ( ... data-address )
  c@ h# 80 and dup if \ Look at the multicast bit
  ( handle data-address length multicast? )
  handle-broadcast-packet
  then
;
: receive-good-packet ( -- [ buffer-handle data-address length ] | 0 )
  begin
    timeout? if false exit then
    receive-ready?
    until
    receive dup 0= while
    .error 2drop return-buffer
    repeat
; 
: receive-unicast-packet ( -- [ buffer-handle data-address length ] | 0 )
  begin
    receive-good-packet dup 0= if exit then
    multicast?
    while
      2drop return-buffer
    repeat
;
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Code Example 9  QED Bootable Driver Sample (Continued)

\ QED bootable driver
\ Receive a packet, filtering out broadcast packets and timing
\ out if no packet comes in within a certain time.
: timed-receive ( timeout-msecs -- [ buffer-handle data-address length ] err?)
  set-timeout receive-unicast-packet ?dup 0= ;

\ qetest.fth
\ Define Qec/Mace loopback-test, net-init & watch-test routines.
\ This file contains Qec/Mace selftest routines.
\ It defines the following external words:
  loopback-test ( internal/external-flag -- success? )
  net-init ( -- success? )
  watch-test ( -- )
\ Also it defines the following external variable.
  ge-verbose?  - Flag to indicate if want the test messages displayed.
  ext-lbt?     - Flag to indicate if run the external loopback test.
\ The algorithme for the loopback test:
  Set internal or external loopback with no promiscuous mode.
  Turn on the Qec/Mace Ethernet port.
  If it succeeds, send out a short packet containing walking 0/1 patterns.
  If it succeeds, wait for a period, check if receive the loopback packet.
  If so, verify the length of the received packet is right.
  Also check if the data of the received packet is right.
  Return true if everything is fine, otherwise return false.

hex
headerless
create loopback-prototype
  ff c, 00 c, \ Ones and zeroes
  01 c, 02 c, 04 c, 08 c, 10 c, 20 c, 40 c, 80 c, \ Walking ones
  fe c, fd c, fb c, f7 c, ef c, od c, 0bf c, 7f c, \ Walking zeroes
  55 c, aa c,

: loopback-buffer ( -- addr len )
  d# 32 get-buffer ( addr )
  mac-address drop over 6 move \ Set source address
  mac-address drop over 6 + 6 move \ Set destination address
  loopback-prototype over d# 12 + d# 20 move \ Set buffer contents
  d# 32 ;
Code Example 9  QED Bootable Driver Sample (Continued)

\ QED bootable driver

: pdump ( addr -- )
  base @ >r hex
  dup d# 10 bounds do i c@ 3 u.r loop cr
  d# 10 + d# 10 bounds do i c@ 3 u.r loop cr
  r> base !
;

\ Print loopback control type for verbose mode.
: .loopback ( -- )
  mode @ m-loop-mask and
  ?dup if
    dup m-loop-ext = if ." External " drop
    else ." Internal " m-loop-intmen = if ." (including Mendec) " then
    then
  ." loopback test -- 
    then
  then
;

\ Print loopback control type for non-verbose mode,
\ it is used after any error occurs.
: ?.loopback ( -- )
  qe-verbose? @ 0= if .loopback then
  ;

: switch-off ( -- false ) qe-verbose? off false ;

: bad-rx-data ( buf-handle data-address -- false )
  ?.loopback
    ." Received packet contained incorrect data. Expected: " cr
    loopback-prototype pdump
    ." Observed:" cr
  d# 12 + pdump
  switch-off
;
\ Check the data of the received packet, return true if data is ok.
: check-data ( buf-handle data-address length -- ok? )
  drop ( buf-handle data-address )
  dup d# 12 + loopback-prototype d# 20 comp
  if bad-rx-data
    else drop ( buf-handle )
    return-buffer
  qe-verbose? @ 0= ." succeeded." cr then

Network Devices
\ QED bootable driver
  mode off true
  then

  \ Check the length & data of the received packet, return true if data & len ok.
  : check-len&data ( buf-handle data-address length -- ok? )
    \ The CRC is appended to the packet, thus it is 4 bytes longer than
    \ the packet we sent.
    dup d# 36 <>
    if  ?.loopback
      ." Wrong packet length; expected 36, observed " .d cr
      switch-off
    else  check-data
    then

  ;

headers
  \ Run internal or external loopback test, return true if the test passes.
  : loopback-test ( internal/external -- pass? )
    mode !
    qe-verbose? @ if ." .loopback then
      net-on if
        loopback-buffer short-send if
        ?.loopback ." send failed." cr
        switch-off
      else
        d# 2000 timed-receive if
        ?.loopback ." Did not receive expected loopback packet." cr
        switch-off
      else
        ( buf-handle data-address length )
        check-len&data
        then
        then
      else
        switch-off
      then
    net-off mode off

  ;

\ If there is a normal external loopback test, then we don’t need this.
\ MACE external loopback test requires a special cable. Don’t run external
\ loopback test for selftest & watch-net.

Code Example 9  QED Bootable Driver Sample  (Continued)
Network Devices

Code Example 9  QED Bootable Driver Sample (Continued)

\ QED bootable driver
: check-cable? ( -- ok? )
   m-cable mode ! " Link state check -- "
   net-on ( success? )
   net-off mode off
;
\ Turn on the Ethernet port after pass loopback test.
\ Return true if net-init succeeds, otherwise return false if it fails.
: net-init ( -- flag )
   mode @ \ Save requested mode because loopback changes it.
   m-loop-int loopback-test
   if ( mode-saved ; Pass internal loopback test. )
      ext-lbt? @ \ Run external loopback test if the ext-lbt? flag is set.
   \ qe internal loopback with mendec is equivalent to external loopback of le.
   if m-loop-intmen loopback-test else true then ( mode-saved )
   swap mode ! \ Restore the mode.
   if net-on else false then
   else mode ! false then
;
headerless
: wait-for-packet ( -- )
   begin key? receive-ready? or until
;
headers
\ Check for incoming Ethernet packets.
\ Use promiscuous mode to check for all incoming packets.
: watch-test ( -- )
   " Looking for Ethernet packets." cr
   ". is a good packet. 'X' is a bad packet." cr
   ". Type any key to stop." cr
   begin
      wait-for-packet
      receive-ready?
      if receive
      if ." ." else ." X" then
         drop return-buffer
      then
      key? dup if key drop then
      until
The network driver uses the standard "obp-tftp" support package for implementation. The "obp-tftp" package implements the Internet Trivial File Transfer Protocol (TFTP) for use in network booting. The "obp-tftp" package defines the following methods to be used by the network driver:

- open ( -- okay? )
- close ( -- )
- load ( addr -- size )

The "obp-tftp" package uses the read and write methods of the network driver for receiving and transmitting packets. The package assumes the size of the maximum transfer packet is 1518 bytes. If the network driver needs bigger maximum packet size, then it requires the method "max-transfer" defined, the method will be called by the obp-tftp package to define the maximum transfer packet size.

```fcode
: init-obp-tftp ( -- okay? )
  " obp-tftp" find-package if ( phandle )
  my-args rot open-package ( ihandle )
  else 0 then
  dup to obp-tftp ( ihandle | 0 )
  dup 0= if
    ." Can't open OBP standard TFTP package" cr
  then

: set-my-channel# ( -- )
  If don't find the channel property, use 0.
  " channel#" get-my-property if 0 else decode-int nip nip then
  my-channel#!

headers
: qe-xmit ( bufaddr nbytes -- #sent )
  tuck get-buffer ( nbytes bufaddr ether-buffer )
  tuck 3 pick move ( nbytes ether-buffer )
  over net-send if drop 0 then ( #sent )
```

Code Example 9   QED Bootable Driver Sample (Continued)
Code Example 9  QED Bootable Driver Sample (Continued)

\ QED bootable driver

: qe-poll ( bufaddr nbytes -- #received )
  qe-nbytes ! qe-buf ! ( )
  receive-ready? 0= if 0 exit then \ Bail out if no packet ready
  receive ?dup if ( rmd ether-buffer length )
    dup >r ( rmd ether-buffer length )
    qe-nbytes @ min ( rmd ether-buffer length' )
    qe-buf @ swap move ( rmd )
    return-buffer r> ( #received )
  else
    drop return-buffer 0 ( 0 )
  then

; : set-vectors ( -- )
  ['] (.receive-error to .error
  ['] (.transmit-error to .transmit-error
  ['] noop to handle-broadcast-packet

; : map-qe ( -- )
  mace 0= if \ Do mapping if it is unmapped.
    map-chips
    map-qe-buffers
  then

; : unmap-qe ( -- )
  mace if \ Do unmapping if it is mapped.
    unmap-qe-buffers
    unmap-chips
  then

; : qe-loopback-test ( -- flag ) \ flag true if passes test
  set-vectors
  mode off qe-verbose? on
  ext-lbt? on
  net-init
  ext-lbt? off
  dup if net-off drop check-cable? then
  qe-verbose? off

; : (watch-net) ( -- )
  map-qe
  set-vectors
\ QED bootable driver
  m-prom mode !
  qe-verbose? off
  ext-lbt? off
  net-init if watch-test net-off then
    unmap-qe

; external
  : qe0-read  ( buf len -- -2 | actual-len )
    qe-poll ?dup 0= if -2 then
  ;
  : qe0-write  ( buf len -- actual-len ) qe-xmit ;
  : qe0-selftest  ( -- flag ) \ Flag 0 if passes test.
    map-qe
    qe-reg-test ( success? )
    if
      qe-loopback-test 0= \ Alternate the return flag.
    else
      true
    then ( failure? )
    unmap-qe

; : qe0-watch-net  ( -- )
    qe0-selftest 0= if (watch-net) then

; : qe0-load  ( addr -- len ) " load" obp-tftp $call-method ;
 : qe0-open  ( -- okay? )
    map-qe
    set-vectors
    mode off qe-verbose? off
    net-init 0= if unmap-qe false exit then
    mac-address drop macbuf 6 move \ Update macbuf.
    macbuf 6 encode-string " mac-address" property
    init-obp-tftp 0= if close false exit then
    true

; qe0-close  ( -- )
    obp-tftp ?dup if close-package then
    mace if net-off then
    unmap-qe

Code Example 9  QED Bootable Driver Sample (Continued)
\ QED bootable driver
;
: qe0-reset ( -- )
  mace if net-off
  else map-chips net-off unmap-chips then
;
headers
This chapter describes programming requirements for serial devices, and gives examples of serial device drivers. Serial devices are byte-oriented, sequentially-accessed devices such as asynchronous communication lines (often attached to a dumb terminal).

**Required Methods**

The serial device driver must declare the serial device type, and must implement the methods `open` and `close`, as well as the following:

- **install-abort** stack: `( -- )

  Instruct the driver to begin periodic polling for a keyboard abort sequence. `install-abort` is executed when the device is selected as the console input device.

- **read** stack: `( addr len -- actual )

  Read `len` bytes of data from the device into memory starting at `addr`. Return the number of bytes actually read, `actual`, or -2 if no bytes are currently available from the device. -1 is returned if other errors occur.
**remove-abort** stack: ( -- )

Instruct the driver to cease periodic polling for a keyboard abort sequence. *remove-abort* is executed when the console input device is changed from this device to another.

**write** stack: ( addr len -- actual )

Write *len* bytes of data to the device from memory starting at *addr*. Return the number of bytes actually written, *actual*.

**Required Properties**

These are the standard properties of a serial driver:

<table>
<thead>
<tr>
<th>Property Name</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>name</td>
<td>&quot;SUNW,zs&quot;</td>
</tr>
<tr>
<td>reg</td>
<td>list of registers [device-dependent]</td>
</tr>
<tr>
<td>device_type</td>
<td>&quot;serial&quot;</td>
</tr>
</tbody>
</table>

**Device Driver Examples**

The following three examples are serial device drivers for the Zilog 8530 SCC (UART) chip.

- The first sample is a short driver which simply creates a device node and declare the properties for the device.
- The second sample is a more sophisticated driver that defines methods to control and access the device.
- The third sample shows the complete serial device driver.
Simple Serial FCode Program

```fcode
\ This driver creates a device node and publishes the minimum required set of
\ properties.
  fcode-version1
    hex
      " SUNW,zs" name
      my-address 10.0000 + my-space 8 reg
      7 encode-int " interrupts" property
  end0
```
Extended Serial FCode Program

Code Example 10

Extended Serial FCode Program

\ Extended Serial FCode Program
\ In addition to publishing the properties, this sample driver
\ provides methods to access and control the serial ports.
\ The following main methods are provided:
\ - usea   ( -- )
\ \ Selects serial port A. All subsequent operations will
\ \ be directed to port A
\ - useb   ( -- )
\ \ Selects serial port B. All subsequent operations will
\ \ be directed to port B
\ - uemit  ( char -- )
\ \ Emits a given character to the selected serial port.
\ - ukey   ( -- key )
\ \ Retrieves a character from the selected serial port.
\ - read   ( addr len -- #read )
\ \ Reads "len" number of characters from the selected port,
\ \ and store them at "addr".
\ - write  ( addr len -- #written )
\ \ Writes "len" number of characters from the buffer located
\ \ at "addr" to the selected serial port.

fcode-version2
hex

" SUNW,zs" name
my-address 10.0000 + my-space 8 reg
7 encode-int " interrupts" property

: >phys-addr ( offset -- phys.io phys.mid phys.hi )
  >r my-address r> 0 d+ my-space
;
: do-map-in ( offset size -- virt )
  >r >phys-addr r> " map-in" $call-parent
;
: do-map-out ( virt size -- ) " map-out" $call-parent ;

: /string ( addr len n -- addr+n len-n ) tuck - -rot + swap ;

1 constant RXREADY \ received character available
4 constant TXREADY \ transmit buffer empty
\ Extended Serial FCode Program
 0 instance value uart \ define uart as an "per-instance" value.
0 instance value uartbase
h# ff instance value mask-#data \ mask for #data bits
h# 10 instance buffer: mode-buf

\ The following line assumes that A2 selects the channel within the chip
  usea ( -- )    uartbase 4 + to uart  ;
  useb ( -- )    uartbase to uart  ;
  uctl! ( c -- ) uart rb!  ;
  uctl@ ( -- c ) uart rb@  ;

\ The following line assumes that A1 chooses the command vs. data port
  udata! ( c -- ) uart 2 + rb!  ;
  udata@ ( -- c ) uart 2 + rb@  ;

\ Test for "break" character received.
  ubreak? ( -- flag ) 10 uctl! uctl@ h# 80 and 0<>  ;

\ Clear the break flag
  clear-break ( -- )
    begin ubreak? 0= until \ Let break finish
    udata@ drop \ Eat the null character
    30 uctl! \  Reset errors
  ;

  uemit? ( -- flag )  uctl@ RXREADY and  ;
  uemit ( char -- ) begin uemit? until udata!  ;

  ukey? ( -- flag )  uctl@ RXREADY and  ;
  ukey ( -- key ) begin ukey? until udata@  ;

  uwrite ( addr len -- #written )
    tuck bounds ?do ( len )
    i c@ uemit ( len )
    loop ( len )
  ;

  uread ( addr len -- #read ) \ -2 for none available right now
    ukey? 0= if 2drop -2 exit then ( addr len )
     tuck ( len addr len )
     begin dup 0<> ukey? 0<> and while ( len addr len )
Complete Serial FCode Program

Code Example 11  Complete Serial FCode Program

\ Complete Serial driver.
\ In addition to the methods defined in the above driver sample,
\ this version defines more methods to initialize, test, and access
\ the serial ports.
\ The new main methods are:
\ - inituarts    ( -- )
\  Initializes both serial ports A and B.
\ - open        ( -- okay? )
\  Maps in the uart chip. Selects port A on default, then check
\  my-args, if port B was specified, then selects port B instead.
\ - close       ( -- )
\  Unmap the uart chip.
\ - selftest    ( -- )
\  Performs selftest on both Port A and B.
\ - install-abort ( -- )
\  Sets up alarm to do poll-tty every 10 miliseconds.
\ - remove-abort ( -- )
\  Removes the poll-tty alarm.

fcode-version2
hex

" SUNW,zs" name
my-address 10.0000 + my-space 8 reg
7 encode-int " interrupts" property
" serial" device-type
Complete Serial FCode Program (Continued)

\ Complete Serial driver.
  : >phys-addr ( offset -- phys.lo phys.mid phys.hi )
    >r my-address r> 0 d+ my-space
  ;

  : do-map-in ( offset size -- virt ) >r >phys-addr r> " map-in" $call-parent ;
  : do-map-out ( virt size -- ) " map-out" $call-parent ;

  : /string ( addr len n -- addr+n len-n ) tuck - -rot + swap ;

  fload inituarts.fth
  fload ttydriver.fth
end0

\--------------------------------------------------
\ inituarts.fth

hex
headerless
create uart-init-table
  \ 9 c, c0 c, \ Master reset channel a (80), channel b (40)
  9 c, 2 c, \ Don't respond to intack cycles (02)
  4 c, 44 c, \ No parity (00), 1 stop bit (04), x16 clock (40)
  3 c, c0 c, \ receive 8 bit characters (c0)
  5 c, 60 c, \ transmit 8 bits (60)
  e c, 82 c, \ Processor clock is baud rate source (02)

  b c, 55 c, \ TRxC = xmit clk (01), enable TRxC (04), Tx clk is baud (10),
              \ Rx clk is baud (40)
  c c, e c, \ Time constant low
  d c, 0 c, \ Time constant high

  3 c, c1 c, \ receive 8 bit characters (c0), enable (01)
  5 c, 68 c, \ transmit 8 bits (60), enable (08)
  e c, 83 c, \ Processor clock is baud rate source (02), Tx enable (01)

  0 c, 10 c, \ Reset status bit latches

  ff c, ff c, \ Mark end of data

\--------------------------------------------------
Code Example 11  Complete Serial FCode Program  (Continued)

\ Complete Serial driver.
\ ttydriver.fth - Driver for Zilog 8530 SCC (UART) chips.

hex
0 instance value uartbase

create default-mode
\ 0 1 2 3 4 5 6 7
 00 c, 00 c, 00 c, c1 c, 44 c, 68 c, 00 c, 00 c,
\ 8 9 a b c d e f
 00 c, 02 c, 00 c, 55 c, 0e c, 00 c, 83 c, 00 c,

0 instance value uart \ define uart as an "per-instance" value.
h# ff instance value mask-#data \ mask for #data bits
h# 10 instance buffer: mode-buf

create masks   1f c, 7f c, 3f c, ff c,

\ The following line assumes that A2 selects the channel within the chip
: usea   ( -- )    uartbase 4 + to uart  ;
: useb   ( -- )    uartbase to uart  ;
: uctl!  ( c -- )  uart rb!   ;
: uctl@  ( -- c )  uart rb@  ;

\ The following line assumes that A1 chooses the command vs. data port
: udata!  ( c -- )  uart 2 + rb!  ;
: udata@  ( -- c )  uart 2 + rb@  ;

\ Write all the initialization sequence to both uarts
: inituart  ( -- )
  uart-init-table
  begin   dup c@ ff <> while
    dup c@ uctl!  dup cal+ c@ uctl!
  /c 2* +
  repeat
  drop
  ;

: inituarts  ( -- ) usea inituart useb inituart usea  ;

\ Test for "break" character received.
: ubreak?  ( -- break? ) 10 uctl!  uctl@  h# 80 and 0<>  ;
\ Complete Serial driver.
\ Clear the break flag
: clear-break ( -- )
    begin 
    ubreak? 0= until \ Let break finish
    udata@ drop \ Eat the null character
    30 uctl! \ Reset errors
    ;

1 constant RXREADY \ received character available
4 constant TXREADY \ transmit buffer empty

: uemit? ( -- emit? ) uctl@ TXREADY and ;
: uemit ( char -- ) begin uemit? until udata! ;

: ukey? ( -- key? ) uctl@ RXREADY and ;
: ukey ( -- key ) begin ukey? until udata@ ;

: uwrite ( addr len -- #written )
    tuck bounds ?do ( len )
    i c@ uemit ( len )
    loop ( len )
;

: uread ( addr len -- #read ) \ -2 for none available right now
    ukey? 0= if 2drop -2 exit then ( addr len )
    dup 0<> while ukey? 0<> and ( len addr len )
    over ukey mask-#data and swap c! ( len addr len )
    1 /string ( len addr' len' )
    repeat ( len addr' len' )
    nip - ( #read )
;

: poll-tty ( -- )
    ttylock @ if exit then
    ubreak? if clear-break user-abort then
;

external
: open ( -- okay? )
    phys-addr 8 do-map-in to uartbase
    usea
    my-args ( arg-str )
    ascii , left-parse-string if ( rem addr )
    c@ ascii b = if ( rem )
\ Complete Serial driver.

  2drop ( )
  useb ( )
  then ( rem )
  else ( rem addr )
  drop 2drop ( )
  then ( )
  true

;  

: close ( -- ) uartbase 8 do-map-out ; 

headers 

: utest ( -- 0 ) h# 7f b1 ?do i uemit loop 0 ; 

external 

: selftest ( -- error? )
  open 0= if :" Can't open device" true exit then
  my-args if ( addr )
    c0 case
      ascii a of usea endof
      ascii b of useb endof
      ( default ) :" Bad zs port letter" drop false exit
    endcase
  else \ No port letter so test both ports.
    drop
    usea utest
    useb utest
    or close exit ( fail? )
  then
  utest ( fail? )
  close

;  

: read ( addr len -- #read ) uread ;
: write ( addr len -- #written ) uwrite ;
: install-abort ( -- ) ['] poll-tty d# 10 alarm ;
: remove-abort ( -- ) ['] poll-tty 0 alarm ;

\ "seek" might be implemented to select a load file name
\ Implement "load" ( optional )

headers
This dictionary describes all of the FCodes defined by IEEE Standard 1275-1994 and supported in the 3.x tokenizer. This dictionary includes the pre-defined FCode words that you can use as part of FCode source code programs. It also includes tokenizer directives and macros. Appendix A, “FCode Reference”, contains a command summary, with words grouped by function.

The words are listed alphabetically in this chapter, sorted by the first alphabetic character in the word’s name. For example, the words mod and */mod are adjacent to each other. Words having no alphabetic characters in their names are placed at the beginning of the chapter, in ASCII order.

The boot PROM and tokenizer are case-insensitive (all Forth words are converted to lowercase internally). The only exceptions are literal text, such as text inside " strings and text arguments to the ascii command, which are left in the original form. In general, you may use either uppercase or lowercase. By convention, OpenBoot drivers are written in lowercase.

Defining words create a header by calling external-token, named-token, or new-token. See the definitions of these words for more details.

All FCode byte values listed in this chapter are in hexadecimal.
stack: ( x a-addr -- )
code: 72
Stores x at a-addr. For more portable code, use 1! if you explicitly want a 32-bit access. a-addr must be aligned as given by variable.

See also: c!, w!, l!, rb!, rw!, rl!

stack: ( x a-addr -- )
code: 12 len xx xx xx ...
generates: b (") len-byte xx-byte ... xx-byte
Gathers the immediately following text string or hex data until reaching the terminator "<whitespace>.

At execution time, the address and length of the string is left on the stack. For example:

" SUNW,new-model" encode-string " model" property

You can embed control characters and 8-bit binary numbers in strings. This is similar in principle to the \n convention in C, but syntactically tuned for Forth. This feature applies to the string arguments of the words " and ."

The escape character is ‘”’. Here is the list of escape sequences:

<table>
<thead>
<tr>
<th>Syntax</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>&quot;</td>
<td>quote (&quot;)</td>
</tr>
<tr>
<td>&quot;n</td>
<td>newline</td>
</tr>
<tr>
<td>&quot;r</td>
<td>carriage return</td>
</tr>
<tr>
<td>&quot;t</td>
<td>tab</td>
</tr>
<tr>
<td>&quot;f</td>
<td>formfeed</td>
</tr>
<tr>
<td>&quot;l</td>
<td>linefeed</td>
</tr>
<tr>
<td>&quot;b</td>
<td>backspace</td>
</tr>
</tbody>
</table>
followed by any other printable character not mentioned above is equivalent to that character.

" (means to start parsing pairs of hexadecimal digits as one or more 8-bit characters in the range 0x00 through 0xFF, delimited by a trailing ) and ignoring non-hexadecimal digits between pairs of hexadecimal digits. Both uppercase and lowercase hexadecimal digits are recognized. Since non-hex characters (such as space or comma) are ignored between " (and), these characters make useful delimiters.

Any characters thus recognized are appended to any previous text in the string being assembled. After the ) is recognized, text assembly continues until a trailing "<whitespace>.

For example:

```
" This is "(01 32,8e)abc"nA test xyzzy "!"! abedefg""hijk"^bl"
  ^^^^^   ^  ^ ^    ^     ^
 3 bytes  newline  2 bells  " control b
```

Note – The use of "n for line breaks is discouraged. The preferred method is to use cr, rather than embedding the line break character inside a string. Use of cr results in more accurate display formatting, because Forth updates its internal line counter when cr is executed.

When " is used outside a colon definition, only two interpreted strings of up to 80 characters each can be assembled concurrently. This limitation does not apply in colon definitions.

See also: b ("")
# stack: ( ud1 -- ud2 )
code: C7

Converts a digit *ud1* in pictured numeric output conversion. Typically used between <# and #>. 

#> stack: ( ud -- str len )
code: C9

Ends pictured numeric output conversion. *str* is the address of the resulting output array. *len* is the number of characters in the output array. *str* and *len* together are suitable for type. See (.) and (u.) for typical usages.

' stack: ( "name ( -- xt )
code: " 11 FCode (name)
generates: b(')

Generates the execution token (*xt*) of the word immediately following ' in the input stream. ' should only be used outside of definitions. See b('), ['] for more details.

For example:

defer opt-word   ( -- ) ' noop is opt-word
( stack: ( -- )
code: none

Causes the compiler/interpreter to ignore subsequent text after the "( " up to a delimiting ")". Note that a space is required after the ( . Although either ( or \ may be used equally well for documentation, by common convention we use ( ...) for stack comments and \ ... for all other text comments and documentation.

For example:

: 4drop ( a b c d -- )
 2drop ( a b )
 2drop ( )
;

( . ) stack: ( n -- str len )

code: 47 2D 96 9A 49 98 97
generates: dup abs <# u#s swap sign u#>

Converts a number into a text string according to the value in base. This is the numeric conversion primitive, used to implement display words such as "." If n is negative, the first character in the array will be a minus (-) sign.

For example:

" CPU boot: show-version ( -- )
  .rom version is " base @ d# 16 base ! ( old-base )
  firmware-version ( old-base version )
  lwsplit (. ) type ascii . emit .h cr base ! ( )

* stack: ( nu1 nu2 -- prod )
code: 20

prod is the arithmetic product of nu1 times nu2. If the result cannot be represented in one stack entry, the least significant bits are kept.
*/ stack: ( n1 n2 n3 -- quot )
code: 30 20 31 21

Calculates n1*n2/n3.

+ stack: ( nu1 nu2 -- sum )
code: 1E

sum is the arithmetic sum of nu1 plus nu2.

+! stack: ( nu a-addr -- )
code: 6C

nu is added to the value stored at a-addr. This sum replaces the original value at a-addr. a-addr must be aligned as given by variable.

, stack: ( x -- )
code: D3

Reserves one cell of storage in data-space and stores x in the cell. The data space pointer must be aligned prior to the execution of ,.

For example, to create an array containing integers 40004000 23 45 6734:

```
create my-array 40004000 , 23 , 45 , 6734 ,
```

- stack: ( nu1 nu2 -- diff )
code: 1F

diff is the result of subtracting nu1 minus nu2.

. stack: ( nu -- )
code: 9D

Displays the absolute value of nu in a free field format with a leading minus sign if nu is negative, and a trailing space.
If the base is hexadecimal, . displays the number in unsigned format, since signed hex display is hardly ever wanted. Use s. to display signed hex numbers.

See also: s.

." stack: ([text<>] -- )
code: 12 len xx xx ... 90
generates: b("”) len text type

This word compiles a text string, delimited by "<whitespace> e.g. . " hello world".

At execution time, the string is displayed. This word is equivalent to using " text" type.

." is normally used only in a definition. The text string will be displayed later when that definition is called. You may wish to follow it with \cr to flush out the text buffer immediately. Use .( for any printing to be done immediately.

See also: ", .,( tokenizer[

.( stack: ([text<>] -- )
code: 12 len xx xx ... 90

Gathers a text string, delimited by ), to be immediately displayed. For example:

.( hello world)

This word is equivalent to: " text" type

Us ( to print out text immediately. (You may wish to follow it with a cr to flush out the text buffer immediately). .( may be called either inside or outside of definitions; the text is immediately displayed in either case.

Note that during FCode interpretation the string will typically be printed out of serial port A, since any frame buffer present may not yet be activated when SBus slots are being probed. Use ." for any printing to be done when new words are later executed.

See also .", tokenizer[
/ stack: ( n1 n2 -- quot )
code: 21

Calculates n1 divided by n2. An error condition results if the divisor (n2) is
zero. See /mod.

: stack: ( "new-name< >" -- colon-sys ) (E: ... -- ??? )
code: (header) B7
generates: new-token | named-token | external-token b(:)

Begins a new definition, terminated by ; Used in the form:

: my-newname ... ;

Later usage of my-newname is equivalent to usage of the contents of the
definition.
See named-token, new-token, and external-token for more information
on header formats.

; stack: ( colon-sys -- )
code: C2
generates: b(;)

Ends the compilation of a colon definition.
See also: :

< stack: ( n1 n2 -- less_than? )
code: 3A

less_than? is true if n1 is less than n2. n1 and n2 are signed integers.
<# stack: ( -- )
code: 96

Initializes pictured numeric output conversion. You can use the words:

<# # #s hold sign #>

to specify the conversion of a number into an ASCII character string stored in right-to-left order. See (.) and (u.) for example usages.

<< stack: ( x1 u -- x2 )
code: 27
generates: lshift

x2 is the result of logically left shifting x1 by u places. Zeroes are shifted into the least-significant bits. Synonymous with lshift.

For example:

: bljoin ( byte.low byte.lowmid byte.highmid byte.high -- 1 )
  8 << + 8 << + 8 << +
  ;

<= stack: ( n1 n2 -- less_than_or_equal? )
code: 43

less_than_or_equal? is true if n1 is less than or equal to n2. n1 and n2 are signed integers.

<> stack: ( x1 x2 -- not_equal? )
code: 3D

not_equal? is true if x1 is not equal to x2. x1 and x2 are signed integers.

= stack: ( x1 x2 -- equal? )
code: 3C

equal? is true if x1 is equal to x2. x1 and x2 are signed integers.
> stack: ( n1 n2 -- greater_than? )
code: 3B

greater_than? is true if n1 is greater than n2. n1 and n2 are signed integers.

>= stack: ( n1 n2 -- greater_than_or_equal? )
code: 42

greater_than_or_equal? is true if n1 is greater than or equal to n2. n1 and n2 are signed integers.

>> stack: ( x1 u -- x2 )
code: 28
generates: rshift

x2 is the result of logically right shifting x1 by u places. Zeroes are shifted into the most-significant bits. Use >>a for signed shifting. Synonym for rshift.

For example:

: wbsplit ( w -- b.low b.high )
dup h# ff and swap 8 >>
  h# ff and
;

? stack: ( a-addr -- )
code: 6D 9D
generates: @ .

Fetches and prints the value at the given address. A standard Forth word, primarily used interactively.

@ stack: ( a-addr -- x )
code: 6D

x is the value stored at a-addr. a-addr must be aligned as given by variable.
See also: c@, w@, 1@, rb@, rw@, rl@
[ stack: ( -- )
code: none

Enter interpretation state.

[ ' ] stack: ([old-name< >] -- xt )
code: 11 FCode
generates: b(') old-FCode#

' or ['] is used to generate the execution token (xt) of the word immediately following the ' or ['].

' should only be used outside definitions; ['] may be used either inside or outside definitions. Examples shown usually use ['], since it will always generate the intended result:

: my-probe … ['] my-install is-install ... ;

or

['] my-install is-install

In normal Forth, ' may be used in definitions for the creation of language extensions, but such usage is not applicable to FCode Programs.

\ stack: ( [rest-of-line<eol> -- )
code: none

Causes the compiler/interpreter to ignore the rest of the input line after the \ . \ can occur anywhere on an input line. Note that a space must be present after \\

For example:

0 value his-ihandle \ place to save someone’s ihandle

See also: (, (s
Enter compilation state.

Leaves the value 0 on the stack. The only numbers that are not encoded using \texttt{b(lit)} are the values -1, 0, 1, 2, or 3. Because these numbers occur so frequently, they are assigned individual FCodes to save space.

\texttt{0<} \quad \text{stack: ( \text{n} \text{ -- less\_than\_0? } )} \\
\text{code: 36} \\
\textit{less\_than\_0?} \textbf{is true if } \text{n} \textbf{is less than zero (negative).}

\texttt{0<=} \quad \text{stack: ( \text{n} \text{ -- less\_than\_or\_equal\_to\_0? } )} \\
\text{code: 37} \\
\textit{less\_than\_or\_equal\_to\_0?} \textbf{is true if } \text{n} \textbf{is less than or equal to zero.}

\texttt{0<>} \quad \text{stack: ( \text{n} \text{ -- not\_equal\_to\_0? } )} \\
\text{code: 35} \\
\textit{not\_equal\_to\_0?} \textbf{is true if } \text{n} \textbf{is not zero.}

\texttt{0=} \quad \text{stack: ( \text{n} \text{ -- equal\_to\_0? } )} \\
\text{code: 34} \\
\textit{equal\_to\_0?} \textbf{is true if } \text{n} \textbf{is zero. This word will invert any flag.}

\texttt{0>} \quad \text{stack: ( \text{n} \text{ -- greater\_than\_0? } )} \\
\text{code: 38} \\
\textit{greater\_than\_0?} \textbf{is true if } \text{n} \textbf{is greater than zero.}
0>= stack: ( n -- greater_than_or_equal_to_0? )
  code: 39

  greater_than_or_equal_to_0? is true if n is greater than or equal to zero.

1 stack: ( -- 1 )
  code: A6

  Leaves the value 1 on the stack. The only numbers that are not encoded using b(lit) are the values -1, 0, 1, 2, or 3. Because these numbers occur so frequently, these values are assigned individual FCodes to save space.

1+ stack: ( nu1 -- nu2 )
  code: A6 1E
generates: 1 +

  nu2 is the result of adding 1 to nu1.

1- stack: ( nu1 -- nu2 )
  code: A6 1F
generates: 1 -

  nu2 is the result of subtracting 1 from nu1.

-1 stack: ( -- -1 )
  code: A4

  Leaves the value -1 on the stack. The only numbers that are not encoded using b(lit) are the values -1, 0, 1, 2, or 3. Because these numbers occur so frequently, these values are assigned individual FCodes to save space.

2 stack: ( -- 2 )
  code: A7

  Leaves the value 2 on the stack. The only numbers that are not encoded using b(lit) are the values -1, 0, 1, 2, or 3. Because these numbers occur so frequently, these values are assigned individual FCodes to save space.
2! stack: ( x1 x2 a-addr -- )
code: 77

x1 and x2 are stored in consecutive locations starting at a-addr. x2 is stored at the lower address. This is equivalent to: swap over ! cell+ !.

2* stack: ( x1 -- x2 )
code: 59

x2 is the result of shifting x1 left one bit. A zero is shifted into the vacated bit position. This is equivalent to multiplying by 2.

2+ stack: ( nu1 -- nu2 )
code: A7 1E

generates: 2 +

nu2 is the result of adding 2 to nu1.

2- stack: ( nu1 -- nu2 )
code: A7 1F

generates: 2 -

nu2 is the result of subtracting 2 from nu1.

2/ stack: ( x1 -- x2 )
code: 57

x2 is the result of arithmetically shifting x1 right one bit. The sign is included in the shift and remains unchanged. This is equivalent to dividing by 2.

2@ stack: ( a-addr -- x1 x2 )
code: 76

x1 and x2 are two numbers stored in consecutive 32-bit locations starting at a-addr. x2 is the number that was stored at the lower address. This is equivalent to: dup cell+ @ swap @.
3 stack: ( -- 3 )
code: A8

Leaves the value 3 on the stack. The only numbers that are not encoded using \texttt{b(lit)} are the values -1, 0, 1, 2, or 3. Because these numbers occur so frequently, these values are assigned individual FCodes to save space.

\textbf{>>a} stack: ( x1 u -- x2 )
code: 29

\(x_2\) is the result of arithmetically right shifting \(x_1\) by \(u\) places. The sign bit of \(x_1\) is shifted into the most-significant bits (i.e. sign extend the high bit).

For example:

\begin{verbatim}
ok \texttt{ffff.0000 6 >>a .h}
fffff00
ok \texttt{ffff.0000 6 >> .h}
3ffffc00
\end{verbatim}

\textbf{abort} stack: ( ... -- ) (R: ... -- )
code: 2 16

Aborts program execution, clearing the data and return stacks. Control returns to the \texttt{ok} prompt. Called after encountering fatal errors.

For example:

\begin{verbatim}
: probe-loop ( addr -- )
  begin dup 1@ drop key? if abort then again
  \ generate a tight probe loop until any key is pressed.
; 
\end{verbatim}
abs stack: ( n -- u )
code: 2D

u is the absolute value of n. If n is the maximum negative number, u is the same value since the maximum negative number in two's complement notation has no positive equivalent.

accept stack: ( addr len1 -- len2 )
code: 88 6D 4B 8A 88 6D 49 88 72
generates: span @ -rot expect span @ swap span !

Get an edited input line, storing it at addr.

again stack: ( C: dest-sys -- ) ( -- )
code: 13

generates: bbranch - offset

Used in the form begin...again to generate an infinite loop. Use a keyboard abort, or abort or exit, to terminate such a loop. Use this word with caution!

For example:

: probe-loop  ( addr -- )
  \ generate a tight probe loop until any key is pressed.
  begin dup l@ drop key? if abort then again
;  

See also: repeat, until, while

alarm stack: ( xt n -- )
code: 2 13

Arranges to execute the package method xt at periodic intervals of n milliseconds (to the best accuracy possible). If n is 0, stop the periodic execution of xt in the current instance context (leaving unaffected any periodic execution of xt that was established in a different instance).
$xt$ is the execution token, as returned by $[']$. Each time the method is called, the current instance will be set to the same as the current instance at the time that $alarm$ was executed and the current instance will then be restored to its previous value afterwards.

$xt$ must be the execution token of a method which neither expects stack arguments nor leaves stack results i.e. whose stack diagram is $\text{( -- )}$.

A common use of $alarm$ would be to implement a console input device's polling function.

For example:

```fcode
: my-checker  ( -- )  test-dev-status if user-abort then ;
: install-abort  ( -- )  [''] my-checker d# 10 alarm ;
```

**alias**

```fcode
stack: ('new-name< >old-name< >' -- )
code: none
```

*alias* creates a new name, with the exact behavior of some other existing name. The new name can then be used interchangeably with the old name and have the same effect.

The tokenizer does not generate any FCode for an *alias* command, but instead simply updates its own lookup table of existing words. Any occurrence of *new-name* causes the assigned FCode value of *old-name* to be generated. One implication is that *new-name* will not appear in the OpenBoot dictionary after the FCode Program is compiled. If this behavior is undesirable, use a colon definition instead.

If the original FCode source text is downloaded and interpreted directly, without being tokenized or detokenized, then any new *alias* words will show up and be usable directly.

For example:

```fcode
alias pkg-prop get-package-property
```
align stack: ( -- )
code: none

Allocates dictionary bytes as necessary to leave the top of the dictionary variable aligned.

aligned stack: ( n1 -- n1 | a-addr )
code: AE

Increases \( n1 \) as necessary to yield a variable aligned address. If \( n1 \) is already aligned, returns \( n \). Otherwise, returns the next higher variable aligned address, \( a-addr \).

alloc-mem stack: ( len -- a-addr )
code: 8B

Allocates a buffer of \( len \) of physical memory that has been aligned to the most stringent requirements of the processor. If successful, returns the associated virtual address. If not successful, \( \text{throw} \) will be called with an appropriate error message as with \( \text{abort} \).

Memory allocated by \( \text{alloc-mem} \) is not suitable for DMA.

See also: \( \text{abort} \), \( \text{dma-alloc} \), \( \text{free-mem} \), \( \text{throw} \).

To detect an out-of-memory condition:

```
\h# 100 ['] alloc-mem catch ?dup if
  throw
else
  ( virt ) constant my-buff
then
```

allot stack: ( len -- )
code: none
generates: 0 max 0 ?do 0 c, loop

Allocates \( len \) bytes in the dictionary. If the operation fails, a \( \text{throw} \) will be called with an appropriate error message as with \( \text{abort} \). Error conditions can be detected and handled properly with the phrase ['] allot catch.
**and** stack: ( x1 x2 -- x3 )  
code: 23  

x3 is the bit-by-bit logical and of x1 with x2.

**ascii** stack: ([text< >] -- char )  
code: 10 00 00 00 xx  
generates: b(lit) 00 00 00 value  

Skips leading space delimiters and puts the ASCII value of the first letter in text on the stack. For example:

```
ascii C ( equals hex 43 )  
ascii c ( equals hex 63 )
```

**b("** stack: ( -- str len )  
code: 12 len xx xx xx ...  

An internal word, generated by ", " and . ( which leaves a text string on the stack. Never use the word b(") in source code.

**b('** stack: ( -- xt )  
code: 11 FCode#  

An internal word, generated by ` and [ ' which leaves the execution token of the immediately following word on the stack. The FCode for b(') should always be followed by the FCode of the desired word. Never use the word b(') in source code.

**b(:** stack: ( -- )  
code: B7  

An internal word generated by the defining word :. Never use the word b(:) in source code.
**b(;)**  
*stack: ( -- )*

*code: C2*

An internal word generated by ; to end a colon definition. Never use the word b(;) in source code.

**base**  
*stack: ( -- addr )*

*code: A0*

generate the variable that contains the current numeric conversion radix to be used when the FCode Program is executing, such as 10 for decimal, 16 for hex, 8 for octal, and so on. Like any variable, base leaves its address on the stack.

For example, to print the current value of base, use:

```
base @ .d
```

The tokenizer words decimal or hex are also available for changing the value in base as desired. However, these words behave differently depending whether they occur in a definition or outside of a definition.

If decimal or hex occur in a definition, then it will be compiled, later causing a change to the value in base when that definition is executed.

If decimal or hex occur outside of a definition, however, then it is interpreted as a command to the tokenizer program itself, thus affecting the interpretation of all subsequent numbers in the text.

Note that changes to base affect the numeric base of the User Interface, which can create much confusion for any user (the default value for base is hexadecimal). If you must change the base, it is recommended that you save and then restore the original base, as in:

```
: .o ( n -- ) \ Print n in octal
  base @ swap  ( oldbase n )
  8 base ! .( oldbase )
  base ! ;
```
In general, only numeric output will be affected by the value in base. Fixed numbers in FCode source are interpreted by the tokenizer program. Most numeric input is controlled by decimal, hex, d#, and h#, but these words only affect the tokenizer input base; they but do not affect the value in base.

For example:

<table>
<thead>
<tr>
<th>version1</th>
<th>hex</th>
<th>( tokenizer in base 16; later execution, using base, in base 16 )</th>
</tr>
</thead>
<tbody>
<tr>
<td>20 .</td>
<td>decimal</td>
<td>( tokenizer is in base 10, later execution is in base 16 )</td>
</tr>
<tr>
<td>20 .</td>
<td>: TEST ( -- )</td>
<td>( compile decimal 20, later print &quot;14&quot; since FCode executes in hex )</td>
</tr>
<tr>
<td></td>
<td></td>
<td>( assume the initial value in base is 16, i.e. User Interface is in hex )</td>
</tr>
<tr>
<td></td>
<td></td>
<td>( no assumptions should be made about the initial tokenizer base )</td>
</tr>
<tr>
<td></td>
<td>8base !</td>
<td>( still compiling in decimal, later change base when TEST executes )</td>
</tr>
<tr>
<td></td>
<td>20 .</td>
<td>( compiles decimal 20, prints &quot;24&quot; since base was just changed )</td>
</tr>
<tr>
<td></td>
<td>h# 20 .d</td>
<td>( compiles decimal 32, prints &quot;32&quot;; no permanent base changes )</td>
</tr>
<tr>
<td></td>
<td>20 .</td>
<td>( compiles decimal 20, prints &quot;24&quot; )</td>
</tr>
<tr>
<td></td>
<td>;</td>
<td></td>
</tr>
<tr>
<td></td>
<td>20 .</td>
<td>( compile decimal 20, later print &quot;14&quot; )</td>
</tr>
<tr>
<td></td>
<td>TEST</td>
<td>( prints &quot;24 32 24&quot;; has a side-effect of changing the base )</td>
</tr>
<tr>
<td></td>
<td>20 .</td>
<td>( compile decimal 20, later print 24 since TEST changed base )</td>
</tr>
<tr>
<td></td>
<td>hex</td>
<td>( tokenizer is in base 16; later execution, using base, still in base 8 )</td>
</tr>
<tr>
<td></td>
<td>20 .</td>
<td>( compile decimal 32, later print &quot;40&quot; )</td>
</tr>
</tbody>
</table>

If this all seems confusing, simply follow these guidelines:

**Good:** Initially declare hex just after fcode-version2, and make liberal use of d#, h#, .h and .d.

**Bad:** Changing base either directly or by calling decimal or hex in a definition.

branch stack: ( -- )
code: 13 offset

An internal word generated by again, repeat, and else which causes an unconditional branch. Never use the word branch in source code.
branch stack: ( flag -- )
code: 14 offset
An internal word generated by until, while, and if which causes a conditional branch. Never use the word branch in source code.

buffer:) stack: ( n -- )
code: BD
An internal word generated by the defining word buffer:) which allocates $n$ bytes of storage space. Never use the word buffer:) in source code.

because stack: ( sel-- sel )
code: C4
An internal word generated by case. Never use the word b(case) in source code.

b(constant) stack: ( n -- )
code: BA
An internal word generated by the defining word constant. Never use the word b(constant) in source code.

b(create) stack: ( -- )
code: BB
An internal word generated by the defining word create. Never use the word b(create) in source code.

b(defer) stack: ( -- )
code: BC
An internal word generated by the defining word defer. Never use the word b(defer) in source code.
**b(do)**

stack: ( end start -- )
code: 17
generates: +offset

An internal word generated by `do`. Never use the word `b(do)` in source code.

**b(?do)**

stack: ( end start -- )
code: 18 +offset

An internal word generated by `?do`. Never use the word `b(?do)` in source code.

**begin**

stack: C: -- dest-sys ) ( -- )
code: B1
generates: b(<mark)

Marks the beginning of a conditional loop, such as `begin...until`, `begin...while... repeat`, or `begin...again`. See these other words for more details.

**behavior**

stack: ( defer-xt -- contents-xt )
code: DE

This command is used to retrieve the execution contents of a defer word.

A typical use would be to fetch and save the current execution of a defer word, change the behavior temporarily and later restore the original behavior. For example:

```plaintext
defer my-func
0 value old-func
['] framus is my-func ...
['] my-func behavior is old-func
['] foo is my-func
... my-func ...
old-func is my-func```
bell stack: ( -- 0x07 )
code: AB

Leave the ASCII code for the bell character on the stack.

b(endcase) stack: ( sel | <nothing>-- )
code: C5

An internal word generated by endcase. Never use the word b(endcase) in source code.

b(endof) stack: ( -- )
code: C6 +offset

An internal word generated by endof. Never use the word b(endof) in source code.

between stack: ( n min max -- min<=n<=max? )
code: 44

min<=n<=max? is true if n is between min and max, inclusive of both endpoints.
See within for a different form of comparison.

b(field) stack: ( addr -- addr+offset )
code: BE

An internal word generated by the defining word field. Never use the word b(field) in source code.

bl stack: (-- 0x20 )
code: A9

Leaves the ASCII code for the space character on the stack.
**blank** stack: ( addr len -- )

code: A9 79

generates: bl fill

Sets len bytes of memory beginning at addr to the ASCII character value for space (hex 20). No action is taken if len is zero.

**b(leave)** stack: ( -- )

code: 1B

An internal word generated by leave. Never use the word b(leave) in source code.

**blink-screen** stack: ( -- )

code: 1 5B

A defer word, called by the terminal emulator, when it has processed a character sequence that calls for ringing the console bell, but the console input device package has no ring-bell method.

blink-screen is initially empty, but must be loaded with a system-dependent routine in order for the terminal emulator to function correctly. The routine must cause some momentary discernible effect that leaves the screen in the same state as before.

This can be done with to, or it can be loaded automatically with fbl-install or fb8-install (which loads fbl-blink-screen or fb8-blink-screen, respectively). These default routines invert the screen (twice) by xor-ing every visible pixel. This is quite slow.

A replacement routine simply disables the video for 20 milliseconds or so, i.e.

```plaintext
: my-blink-screen ( -- ) video-off 20 ms video-on ;
...
\ load default behaviors with fbx-install, then:
['] my-blink-screen to blink-screen
```

Of course, this example assumes that your display hardware is able to quickly enable and disable the video without otherwise affecting the state.
b(lit) stack: ( -- n )
code: 10 xx xx xx xx
An internal word used to save numbers. Never use the word b(lit) in source code.

The only numbers that are not encoded using b(lit) are the values -1, 0, 1, 2, or 3. Because these numbers occur so frequently, these values are assigned individual FCodes to save space.

bljoin stack: ( byte.lo byte2 byte3 byte.hi -- quad )
code: 7F
Merges four bytes into a single 32-bit word. Incorrect results may be generated unless the high bytes of each input stack item are zero.

b(loop) stack: ( -- )
code: 15 -offset
An internal word generated by loop. Never use the word b(loop) in source code.

b(+loop) stack: ( n -- )
code: 16 -offset
An internal word generated by +loop. Never use the word b(+loop) in source code.

b(<mark) stack: ( -- )
code: B1
An internal word generated by begin. Never use the word b(<mark) in source code.

body> stack: ( a-addr -- xt )
code: 85
Converts the data field address of a word to its execution token.
>body stack: ( xt -- a-addr )
code: 86
Converting the execution token of a word to its data field address.

b(of) stack: ( testval -- )
code: 1C +offset
An internal word generated by of. Never use the word b(of) in source code.

bounds stack: ( start cnt -- start+cnt start )
code: AC
Converts a starting value and count into the form required for a do or ?do loop. For example, to perform a loop 20 times, counting up from 4000 to 401f inclusive, use:

```
4000 20 bounds do...loop
```

This is equivalent to:

```
4020 4000 do...loop
```

b(>resolve) stack: ( -- )
code: B2
An internal word generated by repeat and then. Never use the word b(>resolve) in source code.

bs stack: ( -- 0x08 )
code: AA
Leaves the ASCII code for the backspace character on the stack.
b(to) stack: ( -- )
code: C3

An internal word generated by to. Never use the word b(to) in source code.

buffer: stack: ( len "new-name<>" -- ) (E: -- addr )
code: (header)
genelines: new-token|named-token|external-token b(buffer:)

Allocates len bytes of storage space and creates a name, new-name. When new-name is executed, it leaves the address of the first byte of the buffer on the stack.

For example:

```
200 buffer: my-name
my-name ( addr )
```

b(value) stack: ( n -- )
code: B8

An internal word generated by the defining word value. Never use the word b(value) in source code.

b(variable) stack: ( -- )
code: B9

An internal word generated by the defining word variable. Never use the word b(variable) in source code.

bwjoin stack: ( byte.lo byte.hi -- w )
code: B0

Merges two bytes into the low 16-bits of a stack entry whose upper bytes are zeroed. Incorrect results may be generated unless the high bytes of each input stack item are zero.
**bxjoin** stack: ( b.lo b.2 b.3 b.4 b.5 b.6 b.7 b.hi -- o )
code: 02 41

Joins 8 bytes to form an octlet. Combines the eight least-significant bits of each operand to form an octlet. Other operand bits are ignored.

**byte-load** stack: ( addr xt -- )
code: 02 3E

Interprets the FCode Program located at addr. If xt is 1, use `rb@` to read the FCode Program, otherwise use `xt` as the execution token of the definition to be used to read the FCode Program. Continue reading and interpreting the program until `end0` is encountered.

Be aware that `byte-load` does not itself create a new device node as a “container” for any properties and methods defined by the FCode Program that `byte-load` evaluates. If the FCode Program is intended to create such a node, appropriate preparation must be done before and after executing `byte-load`. For example, `new-device` and `set-args` can be executed before and `finish-device` can be executed after `byte-load` is executed.

If `byte-load` is to be executed from the User Interface, additional set up is usually necessary before executing `new-device`.

**c!** stack: ( x addr -- )
code: 75

Stores the least significant 8 bits of `x` in the byte at `addr`.

See also: `rb!`

**c,** stack: ( byte -- )
code: D0

Compiles a byte into the dictionary. `c,` can be used, in conjunction with `create`, to create an array-type structure, as:

```
create yellow 77 c, 23 c, ff c, ff c, 47 c, 22 c, ...
```
Later execution of yellow leaves the address of the first byte of the array (the address of the byte “77”) on the stack.

---

\( /c \) stack: \(( -- n )\)
code: 5A

Leaves the number of address units to a byte (i.e. 1) on the stack.

See also: \( /w, /l, /n \)

---

\( /c* \) stack: \(( nu1 -- nu2 )\)
code: 66
generates: chars

Synonym for \( \text{chars}\).

---

\( c@ \) stack: \(( addr -- byte)\)
code: 71

Fetches the byte at address \( addr \) and leaves it on top of the stack with the high order bytes filled with zeroes.

See also: \( \text{rb}@ \)

---

\( ca+ \) stack: \(( addr1 index -- addr2 )\)
code: 5E

Increments \( addr1 \) by \( index \) times the value of \( /c \). \( ca+ \) should be used in preference to + when calculating addresses because it more clearly expresses the intent of the operation and is more portable.

---

\( ca1+ \) stack: \(( addr1 -- addr2 )\)
code: 62

Synonym for \( \text{char+}\).
$call-method\n\hfill stack: ( ??? method-str method-len ihandle -- ??? )
\hfill code: 02 0E
\hfill Executes the device interface method \textit{method-str} \textit{method-len} in the open package instance \textit{ihandle}. The question marks (???) indicate that the contents of the stack before and after the method is called depend on the particular method being called.
\hfill For example:
\begin{verbatim}
: dma-alloc ( #bytes -- vaddr ) " dma-alloc" my-parent $call-method ;
\end{verbatim}
\hfill See also: open-package.
\hfill call-package\n\hfill stack: ( ??? xt ihandle -- ??? )
\hfill code: 02 08
\hfill Executes the device interface method \textit{xt} in the open package instance \textit{ihandle}. The question marks (???) indicate that the contents of the stack before and after the method is called depend on the particular method being called.
\hfill For example:
\begin{verbatim}
0 value label-ihandle \ place to save the ihandle of other package
0 value offset-method \ place to save the xt of found method
: init ( -- )
  my-args " disk-label" $open-package ( ihandle )
  to label-ihandle
  " offset" label-ihandle
  ihandle>phandle ( name-addr name-len phandle )
  find-method if
    to offset-method
  else
    ." Can't find offset method "
    then
;
init
: add-offset ( d.byte# -- d.bytes# )
  offset-method label-ihandle call-package
;
\end{verbatim}
See also: find-method, open-package

$call-parent

stack: ( ??? method-str method-len -- ??? )
code: 02 09

Calls the method named by method-str method-len in the parent instance. If the called package has no such method, an error is signaled with throw. Equivalent to:

```
my-parent $call-method
```

The question marks (???) indicate that the contents of the stack before and after the method is called depend on the particular method being called.

For example:

```
: my-dma-alloc ( -- vaddr ) h# 2000 "dma-alloc" $call-parent ;
```

carret

stack: ( -- 0x0D )
code: 10 00 00 00 0D
generates: b(lit) 00 00 00 0x0D

Leaves the ASCII code for “carriage return” on the stack.
case stack: ( sel -- sel )
code: C4
generates: b(case)

Starts a case statement that selects its action based on the value of sel.
Example of use:

```forth
: foo ( selector -- )
case
  0 of ." It was 0" endof
  5 of ." It was 5" endof
-2 of ." It was -2" endof
  ( selector ) ." It was " dup u. \ default clause
endcase
```
of tests the top of the stack against the selector at run time. If they are the
same, the selector is dropped and the following Forth code is executed. If they
are not the same, execution continues at the point just following the matching
endof.

The default clause is optional. When an of clause is executed, the selector is
not on the stack. When a default clause is executed, the selector is on the stack.
The default clause may use the selector, but must not remove it from the stack
(it will be automatically removed by endcase). If the default case adds to the
stack, the selector must be moved to top of stack from which it will be
dropped. For example:

```forth
: bar (selector -- value )
case
  3 of 21 endof
  4 of 33 endof
  27 swap \ default clause
endcase
```
case statements can be used both inside and outside of colon definitions.
catch stack: ( ??? xt -- ??? error-code | ??? false )
code: 02  17

Creates a new error handling context and executes xt in that context.

If a throw (see below) is called during the execution of xt,

1. The error handling context is removed
2. The stack depth is restored to the depth that existed prior to the execution of xt (not counting the xt stack item)
3. The error code that was passed to throw is pushed onto the stack
4. catch returns

If throw is not called during the execution of xt, the error handling context is removed and catch returns a false. The stack effect is otherwise the same as if xt were executed using execute.

For example:

: add-n-check-limit ( n1 n2 n3 -- n )
  + + dup h# 30 > if true throw then
;  
: add-me ( n1 n2 n3 -- a b c | n1+n2+n3 )
  ['] add-n-check-limit catch if
  ." Sum exceeds limit " .s
  else
  ." Sum is within limit. Sum = " .s
  then cr
;

Note that, given this definition:

1 2 3 add-me

shows:

Sum is within limit. Sum = 6
while:

```
10 20 30 add-me
```

may show something like:

```
Sum exceeds limit  50 9 12
```

**Note** – On a non-zero `throw`, only the stack depth is guaranteed to be the same as before `catch`, not the data stack contents.

---

**cell+**

stack: ( addr1 -- addr2 )
code: 65

Increments `addr1` by the value of `/n`. `cell+` should be used in preference to `wa1+` or `la1+` when the intent is to address items that are the same size as items on the stack.

---

**cells**

stack: ( nu1 -- nu2 )
code: 69

`nu2` is the result of multiplying `nu1` by `/n`, the length in bytes of a normal stack item. This is useful for converting an index into a byte offset.

---

**char-height**

stack: ( -- height )
code: 01 6C

A value, containing the standard height (in pixels) for all characters to be drawn. This number, when multiplied by `#lines`, determines the total height (in pixels) of the active text area.

This word *must* be set to the appropriate value if you wish to use any `fb1-` or `fb8-` utility routines or `>font`. This can be done with `to`, but is normally done by calling `set-font`. 
chars stack: ( nu1 -- nu2 )  
code: 66

nu2 is the result of multiplying nu1 by /c, the length in bytes of a byte. This is useful for converting an index into a byte offset.

char-width stack: ( -- width )  
code: 01 6D

A value, containing the standard width (in pixels) for all characters to be drawn. This number, when multiplied by #columns, determines the total width (in pixels) of the active text area.

This word must be set to an appropriate value if you want to use any fb1- or fb8- utility routines. This can be done with to, but is normally done by calling set-font.

child stack: ( parent-phandle -- child-phandle )  
code: 02 3B

Returns the phandle of the package that is the first child of the package parent-phandle.

child returns zero if the package parent-phandle has no children.

You will generally use child, together with peer, to enumerate (possibly recursively) the children of a particular device. One common use could be for bus adapter device drivers to use the phrase my-self ihandle>phandle to develop the parent-phandle argument.

For example:

: my-children ( -- ) \ shows phandles of all children
  my-self ihandle>phandle child ( first-child )
  begin ?dup while dup .h peer repeat
  ;
**close-package**

stack: ( ihandle -- )
code: 02 06

Closes the package instance identified by *ihandle* by calling that package’s *close* method and then destroying the instance.

For example:

```fcode
: tftp-load-avail? ( -- exist? )
  0 0 " obp-tftp" $open-package ( ihandle )
dup ihandle>phandle " load" rot
  find-method if drop true else false then
  close-package
;
```

**cmove>**

stack: ( adr1 adr2 len -- )
code: 78
generates: move

Copy *len* bytes of an array starting at *adr1* to *adr2*. This word is an alias for *move*.

**column#**

stack: ( -- column# )
code: 01 53

A value, set and controlled by the terminal emulator, that contains the current horizontal position of the text cursor. A value of 0 represents the left-most cursor position of the text window, not the left-most pixel of the frame buffer.

*column#* can (and should) be looked at as needed if your FCode Program is implementing its own set of frame buffer primitives.

For example:

```fcode
: set-column ( column# -- )
  0 max #columns 1- min to column#
;
```

See also: window-left.
#columns stack: ( -- columns )
code: 01 51

This is a value that returns the number of columns of text in the text window i.e. the number of characters in a line, to be displayed using the boot PROM’s terminal emulator.

#columns must be set to a proper value in order for the terminal emulator to function correctly. The open method of any package that uses the terminal emulator package must set #columns to the desired width of the text region. This can be done with to, or it can be handled automatically as one of the functions performed by fb1-install or fb8-install.

For example:

```
: set-column (column# -- )
  0 max #columns l- in to column#
;
```

See also: is-install, fb1-install, fb8-install

comp stack: ( addr1 addr2 len -- n )
code: 7A

Compares two strings of length len starting at addresses addr1 and addr2 and continuing for len bytes. n is 0 if the arrays are the same. n is 1 if the first differing character in the array at addr1 is numerically greater than the corresponding character in the array at addr2. n is -1 if the first differing character in the array at addr1 is numerically less than the corresponding character in the array at addr2.

For example:

```
ok " this" drop " that" comp .h
1
ok " thisismy" drop " this" comp .h
0
ok " thin" drop " this" comp .h
ffffffff
```
**compile**, stack: ( xt -- )
code: DD

Compiles the behavior of the word given by xt.

**[compile]** stack: ([old-name< >] -- )
code: none

Compiles the immediately-following command.

**constant** stack: ( x "new-name< >" -- ) (E: -- value )
code: (header) BA
  generates: new-token|named-token|external-token b(constant)

Creates a named constant. The name is initially created with:

```
108 constant purple
```

where 108 is the desired value for purple.

Later occurrences of purple will leave the 108 on the stack. If you wish to change the value of a constant in a program, you should use value instead of constant.

**control** stack: ([text< >] -- char )
code: 10 00 00 00 xx
  generates: b(lit) 00 00 00 xx-byte

Causes the compiler/interpreter to interpret the next letter as a control-code. For example:

```
control c  ( equals 03 )
```
**count** stack: ( pstr -- addr len )

code: 84

Converts a packed string into a byte-array format. *pstr* is the address of a packed string, where the byte at address *pstr* is the length of the string and the string itself starts at address *pstr*+1.

Packed strings are generally not used in FCode. Virtually all string operations are in the "addr len" format.

For example:

```plaintext
h# 100 alloc-mem constant my-buff
" This is a string" my-buff pack ( pstr ) count type
```

**cpeek** stack: ( addr -- false | byte true )

code: 02 20

Tries to read the 8-bit byte at address *addr*. Returns the data and *true* if the access was successful. A *false* return indicates that a read access error occurred.

See also: rb@

**cpoke** stack: ( byte addr -- okay? )

code: 02 23

Attempts to write the 8-bit byte at address *addr*. Returns *true* if the access was successful. A *false* return indicates that a write access error occurred.

**Note** – *cpoke* may be unreliable on bus adapters that buffer write accesses.

See also: rb!

**cr** stack: ( -- )

code: 92

A *defer* word used to terminate the line on the display and go to the next line. The default implementation transmits a carriage return and line feed to the display, clears *#out* and adds 1 to *#line*. 
Use cr whenever you want to start a new line of output, or to force the display of any previously buffered output text. This forcing is valuable for outputting error messages, to ensure that the error message is sent before any system crash.

For example:

```
: show-info ( -- )
  ." This is the first line of output " cr
  ." This is the second line of output " cr

: show-info ( -- )
  ." This is the first line of output " cr
  ." This is the second line of output " cr
```

<table>
<thead>
<tr>
<th>(cr) stack: ( -- )</th>
</tr>
</thead>
<tbody>
<tr>
<td>code: 91</td>
</tr>
<tr>
<td>Outputs only the carriage return character (carret, 0x0D). The most common use of (cr is for reporting the progress of a test that has many steps. By using (cr instead of cr, the progress report appears on a single line instead of scrolling.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>create stack: (&quot;new-name&lt; &gt;&quot; -- ) (E: -- addr)</th>
</tr>
</thead>
<tbody>
<tr>
<td>code: (header) BB</td>
</tr>
<tr>
<td>generates: new-token</td>
</tr>
<tr>
<td>Creates the name new-name. When new-name is subsequently executed, it returns the address of memory immediately following new-name in the dictionary. You can use create to build an array-type structure, as:</td>
</tr>
<tr>
<td>create white 12 c, 08 c, 36 c, 25 c, 27 c, 5 c, ...</td>
</tr>
</tbody>
</table>

Later execution of green leaves the address of the first byte of the array (here, the address of the byte “12”) on the stack. The returned address will be two-byte aligned.

create may not be used in definitions in an FCode Program. The common Forth construct create...does> is not supported.
d# stack: ( [number< >] -- n )
code: 10 value
generates: b(lit) xx-byte xx-byte xx-byte xx-byte

Causes the compiler/interpreter to interpret the next number in decimal (base 10), regardless of any previous settings of hex, decimal or octal. Only the immediately following number is affected, the default numeric base setting is unchanged. For example:

```
<table>
<thead>
<tr>
<th>hex</th>
<th>d# 100</th>
<th>100</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>( equals decimal 100 )</td>
<td>( equals decimal 256 )</td>
</tr>
</tbody>
</table>
```

See also: h#.

d+ stack: ( d1 d2 -- d.sum )
code: D8

Adds two double numbers, leaving the double sum on the stack.

For example:

```
ok 1234.0000 0056.7800 9abc 3400.009a d+ .s
1234.9abc 3456.789a
```

d- stack: ( d1 d2 -- d.diff )
code: D9

Subtracts two double numbers, leaving the double result on the stack.

For example:

```
ok 0 6 1 0 d- .s
ffff.ffff 5
ok 4444.8888 aaaa.bbbb 2222.1111 5555.2222 d- .s
2222.7777 5555.9999
```
.d stack: ( n -- )
code: A0 6D 49 10 00 00 00 0A A0 72 9D A0 72
generates: base @ swap d# 10 base ! . base !

Displays n in decimal with a trailing space. The value of base is not permanently affected.

decimal stack: ( -- )
code: 10 00 00 00 0A A0 72
generates: d# 10 base !

If used outside of a definition, commands the tokenizer program to interpret subsequent numbers in decimal (base 10).

If used in a definition, appends the phrase 10 base ! to the FCode Program that is being created thus affecting later numeric output when the FCode Program is executed.

See also: base

decode-bytes stack: ( prop-addr1 prop-len1 data-len -- prop-addr2 prop-len2 data-addr data-len )
code: none
generates: >r over r@ + swap r@ - rot r>

Decodes data-len bytes from a property value array and returns the remainder of the array and the decoded byte array.

decode-int stack: ( prop-addr1 prop-len1 -- prop-addr2 prop-len2 n )
code: 02 1B

Decodes a number from the beginning of a property value array and returns the remainder of the property value array and the number n.
For example:

```fcode
: show-clock-frequency ( -- )
" clock-frequency" get-inherited-property 0= if
  " Clock frequency: " decode-int .h cr 2drop
then
;
```

decode-phys stack: ( prop-addr1 prop-len1 -- prop-addr2 prop-len2 phys.lo ... phys.hi )
code: 01 28

Decodes a unit address from a property value array and returns the remainder of the array and the decoded list of address components. The number of cells in the list `phys.lo ... phys.hi` is determined by the value of the #address-cells property of the parent node.

```fcode
: show-model ( -- )
" model" get-my-property 0= if decode-string type 2drop then
;
```

decode-string stack: ( prop-addr1 prop-len1 -- prop-addr2 prop-len2 str len )
code: 02 1C

Decodes a string from the beginning of a property value array and returns the remainder of the property value array and the string `str len`.

For example:

```fcode
: default-font set-font
```

The actual parameters returned by `default-font` are:

```fcode
: default-font stack: ( -- fontaddr charwidth charheight #fontbytes #firstchar #chars )
code: 01 6A

Returns all the necessary information about the character font that is built into the boot PROM. This font defines the appearance of every character to be displayed. To load this font, simply pass these parameters to `set-font`, with:
```
fontaddr - The address of the beginning of the built-in font table
charwidth - The width of each character in pixels
charheight - The height of each character in pixels
#fontbytes - The separation (in bytes) between each scan line entry
#firstchar - The ASCII value for the first character actually stored in the font table.
#chars - The total number of characters stored in the font table.

defer stack: ("new-name< >" -- ) (E: -- ???)

code: (header) bc

generates: new-token|named-token|external-token b(defer)

Creates a command new-name that is a defer word i.e. a word whose behavior can be altered with to. new-name is initially created with execution behavior that indicates that it is an uninitialized defer word. For example:

```
ok defer blob
ok blob
<--deferred word not initialized
```

Later, this behavior can then be altered to be that of another existing word by placing that second word’s execution token on the stack and loading it into new-name with to. For example:

```
['] foobar to blob
```
defer words are useful for generating recursive routines. For example:

```fcode
defer hold2 \ Will execute action2
: action1
  ...
  hold2 ( really action2 )
  ...
; : action2
  ...
  action1
  ...
; ' action2 to hold2
```

defer words can also be used for creating words with different behaviors depending on your needs. For example:

```fcode
defer .special ( n -- ) \ Print a value, using special techniques
: print-em-all ( -- )
  ... .special
  ... .special
  ... .special

; (.d prints in decimal
 (.h prints in hexadecimal)
 (.sp prints in a custom format)
 : print-all-styles
 ['] .d to .special print-em-all
 ['] .h to .special print-em-all
 ['] .sp to .special print-em-all

In FCode source, defer cannot appear inside a colon definition.

See also: behavior.
```
**delete-characters** stack: ( n -- )
code: 01 5E

Deletes n characters to the right of the cursor.

delete-characters is one of the **defer** words of the display device interface. The terminal emulator package executes delete-characters when it has processed a character sequence that requires the deletion of characters to the right of the cursor. The cursor position is unchanged, the cursor character and the first n-1 characters to the right of the cursor are deleted. All remaining characters to the right of the cursor, including the highlighted character, are moved left by n places. The end of the line is filled with blanks.

This word is initially empty, but **must** be loaded with an appropriate routine in order for the terminal emulator to function correctly. This can be done with **to**, or it can be loaded automatically with fb1-install or fb8-install (which loads fb1-delete-characters or fb8-delete-characters, respectively).

See also: fb8-install.

**delete-lines** stack: ( n -- )
code: 01 60

Deletes n lines at and below the cursor line.

delete-lines is one of the **defer** words of the display device interface. The terminal emulator package executes delete-lines when it has processed a character sequence that requires the deletion of lines of text below the line containing the cursor. All lines below the deleted lines are scrolled upwards by n lines, and n blank lines are placed at the bottom of the active text area.

Use this word for scrolling, by temporarily moving the cursor to the top of the screen and then calling delete-lines.

This word is initially empty, but **must** be loaded with an appropriate routine in order for the terminal emulator to function correctly. This can be done with **to**, or it can be loaded automatically with fb1-install or fb8-install (which loads fb1-delete-lines or fb8-delete-lines, respectively).

See also: fb8-install.
delete-property stack: ( name-str name-len -- )
code: 02 1E

Deletes the property named by name-str name-len in the active package, if such a property exists.

For example:

```
: unmap-me ( -- )
  my-reg my-size " map-out" $call-parent
  " address" delete-property

;```

depth stack: ( -- u )
code: 51

u is the number of entries contained in the data stack, not counting itself. Note that when an FCode Program is called, there could be other items on the stack from the calling program.

depth is especially useful for before/after stack depth checking, to determine if the stack was corrupted by a particular operation.

device-name stack: ( str len -- )
code: 02 01

Creates a name property with the given string value. For example:

```
" SUNW,ffb" device-name
```

This is equivalent to using the name macro or

```
encode-string " name" property
```

except that device-name performs the same function with only 2 bytes of FCode, instead of 10 bytes. This word could be useful for devices with extremely limited FCode space.
See also: name.

\texttt{diagnostic-mode?} \hfill stack: ( -- diag? )
\hfill code: 01 20

FCode Programs can use \texttt{diagnostic-mode?} to control the extent of the selftests performed. If \texttt{diagnostic-mode?} is true, more extensive tests can be performed and more messages displayed.

For example:

\begin{verbatim}
   diagnostic-mode?
   if   do-extended-tests
   else do-normal-tests
   then
\end{verbatim}

FCode should not generate character output during probing unless \texttt{diagnostic-mode?} is true, or unless an error is encountered. Error output during probing typically goes to the system serial port.

\texttt{diagnostic-mode?} will return true if any of the following conditions are met:

\begin{itemize}
\item \texttt{diag-switch?} is true.
\item A machine diagnostic switch (system-dependent) is ON.
\item Other system-dependent indicators request extensive diagnostics.
\end{itemize}

\texttt{digit} \hfill stack: ( char base -- digit true | char false )
\hfill code: A3

If the character \texttt{char} is a digit in the specified base, returns the numeric value of that digit under \texttt{true}, else returns the character under \texttt{false}. Appropriate characters are hex 30–39 (for digits 0–9) and hex 61–66 (for digits a–f), depending on \texttt{base}. 
For example:

```fcode
: probe-slot ( slot# -- ) ... ;
: probe-slots ( addr cnt -- )
  bounds ?do
    i c@ d# 16 digit if probe-slot else drop then
  loop
;
```

**do**

stack: ( C: -- dest-sys ) ( limit start -- ) ( R: -- sys )
code: 17 +offset
generates: b(do) +offset

Begins a counted loop in the form do...loop or do...+loop. The loop index begins at start, and terminates based on limit. See loop and +loop for details on how the loop is terminated. The loop is always executed at least once. For example:

```fcode
8 3 do i . loop \ would print 3 4 5 6 7
9 3 do i . 2 +loop \ would print 3 5 7
```

**do** may be used either inside or outside of colon definitions.

**?do**

stack: ( C: -- dest-sys ) ( limit start -- ) ( R: -- sys )
code: 18 +offset
generates: b(?do) +offset

Begin a counted loop in the form ?do...loop or ?do...+loop. The loop index begins at start, and terminates based on limit. See loop and +loop for details on how the loop is terminated. Unlike **do**, if **start** is equal to **limit** the loop is executed zero times. For example:

```fcode
8 1 ?do i . loop \ would print 1 2 3 4 5 6 7
2 1 ?do i . loop \ would print 1
1 1 ?do i . loop \ would print nothing
1 1 do i . loop \ would print 1 2 3 4 5 6 7 8 9 ...
...```
?do can be used in place of do in nearly all circumstances. ?do may be used either inside or outside of colon definitions.

**draw-character** stack: ( char -- )  
code: 01  57

A *defer* word that is called by the terminal emulator in order to display a single character on the screen at the current cursor location.

This word is initially empty, but *must* be loaded with an appropriate routine in order for the terminal emulator to function correctly. This can be done with *to*, or it can be loaded automatically with *fb1-install* or *fb8-install* (which loads *fb1-draw-character* or *fb8-draw-character*, respectively).

**draw-logo** stack: ( line# addr width height -- )  
code: 01  61

A *defer* word that is called by the system to display the power-on logo (the graphic displayed on the left side during power-up, or by *banner*).

This word is initially empty, but *must* be loaded with an appropriate routine in order for the terminal emulator to function correctly. This can be done with *to*, or it can be loaded automatically with *fb1-install* or *fb8-install* (which loads *fb1-draw-logo* or *fb8-draw-logo*, respectively).

It is possible to pack a custom logo into the FCode PROM and then re-initialize *draw-logo* to output the custom logo instead.

*draw-logo* is called by the system using the following parameters:

*line#* - The text line number at which to draw the logo.

*addr* - The address of the logo template to be drawn. In practice, this will always be either the address of the *oem-logo* field in NVRAM, the address of a custom logo in the FCode PROM, or the address of the built-in system logo. In any case, the logo is an array of 64x64 (hex) pixels.

*width* - The width of the passed-in logo (in pixels).

*height* - The height of the passed-in logo (in pixels).
**drop** stack: ( x -- )
code: 46
Removes one item from the stack.

**2drop** stack: ( x1 x2 -- )
code: 52
Removes two items from the stack.

**3drop** stack: ( x1 x2 x3 -- )
code: 46 52
generates: drop 2drop
Removes three items from the stack.

**dup** stack: ( x -- x x )
code: 47
Duplicates the top stack item.

**2dup** stack: ( x1 x2 -- x1 x2 x1 x2 )
code: 53
Duplicates the top two stack items.

**3dup** stack: ( x1 x2 x3 -- x1 x2 x3 x1 x2 x3 )
code: A7 4E A7 4E A7 4E
generates: 2 pick 2 pick 2 pick
Duplicates the top three stack items.

**?dup** stack: ( x -- 0 | x x )
code: 50
Duplicates the top stack item unless it is zero.
else stack: ( C: orig-sys1 -- orig-sys2 ) ( -- )
code: 13  +offset  B2
generates: bbranch +offset b(>resolve)

Begin the else clause of an if...else...then statement. See if for more details.

emit stack: ( char -- )
code: 8F
A defer word that outputs the indicated ASCII character. For example, h# 41 emit outputs an “A”, h# 62 emit outputs a “b”, h# 34 emit outputs a “4”.

emit-byte stack: ( FCode# -- )
code: N
generates: n
An FCode-only command used to manually output a desired byte of FCode. Use it together with tokenizer[ as follows:

```
tokenizer[
  44 emit-byte 20 emit-byte
]tokenizer
```

emit-byte would be useful, for example, if you wished to generate a new FCode command that the tokenizer did not understand. This command should be used with caution or else an invalid FCode Program will result.

See also: tokenizer[

encode+ stack: ( prop-addr1 prop-len1 prop-addr2 prop-len2 -- prop-addr3 prop-len3 )
code: 01  12
Merge two property value arrays into a single property value array. The two input arrays must have been created sequentially with no intervening dictionary allocation or other property value arrays having been created. This can be called repeatedly, to create complex, multi-valued property value arrays for passing to property.
For example, suppose you wished to create a property named `myprop` with the following information packed sequentially:

```
"size" 2000 "vals" 3 128 40 22
```

This could be written in FCode as follows:

```
: encode-string,num ( addr len number -- )
  >r encode-string
  r> encode-int encode+
;
" size" 2000 encode-string,num
" vals" 3 encode-string,num encode+
128 encode-int encode+
40 encode-int encode+
22 encode-int encode+
" myprop" property
```

**encode-bytes**

```
stack: ( data-addr data-len -- prop-addr prop-len )
code: 01 15

Encodes a byte array into a property value array. The external representation of a byte array is the sequence of bytes itself, with no appended null byte.

For example:

```
my-idprom h# 20 encode-bytes " idprom" property
```

**encode-int**

```
stack: ( n -- prop-addr prop-len )
code: 01 11

Convert an integer into a property value array, suitable for passing as a value to property. For example:

```
1152 encode-int " hres" property
```
**encode-phys** stack: ( phys.lo ... phys.hi -- prop-addr prop-len )
code: 01 13

Encodes a unit-address into a property value array by property encoding the
list of cells denoting a unit address in the order of phys.hi followed by the
encoding of the component that appears on the stack below phys.hi, and so on,
ending with the encoding of the phys.lo component.

The number of cells in the list phys.lo ... phys.hi is determined by the value of the
"#address-cells" property of the parent node.

For example:

```
my-address my-space encode-phys " resetloc" property
```

**encode-string** stack: ( str len -- prop-addr prop-len )
code: 01 14

Converts an ordinary string, such as created by ", into a property value array
suitable for property. For example:

```
" NSL,DTE,AUU" encode-string " authors" property
```

**end0** stack: ( -- )
code: 00

A word that marks the end of an FCode Program. This word must be present
at the end of your program or erroneous results may occur.

If you want to use end0 inside a colon definition, for example in a conditional
clause, use something like:

```
: exit-if-version1 fcode-revision h# 20000 < if ['] end0 execute then
`

" NSL,DTE,AUU" encode-string " authors" property

end0 stack: ( -- )
code: 00

A word that marks the end of an FCode Program. This word must be present
at the end of your program or erroneous results may occur.

If you want to use end0 inside a colon definition, for example in a conditional
clause, use something like:

```
: exit-if-version1 fcode-revision h# 20000 < if ['] end0 execute then
```
**end1**

stack: ( -- )
code: FF

An alternate word for end0, to mark the end of an FCode Program. end0 is recommended.

end1 is not intended to appear in source code. It is defined as a guard against misprogrammed ROMs. Unprogrammed regions of PROM usually appear as all ones or all zeroes. Having both 0x00 and 0xFF defined as end codes stops the FCode interpreter if it enters an unprogrammed region of a PROM.

**endcase**

stack: ( C: case-sys -- ) ( sel | <nothing> -- )
code: C5
generates: b(endcase)

Marks the end of a case statement. See case for more details.

**endof**

stack: ( C: case-sys1 of-sys -- case-sys2 ) ( -- )
code: C6 +offset
generates: b(endof) +offset

Marks the end of an of clause in a case statement. See case for more details.

**erase**

stack: ( addr len -- )
code: 95  79
generates: 0 fill

Sets len bytes of memory beginning at addr to zero. No action is taken if len is zero.

**erase-screen**

stack: ( -- )
code: 01  5A

A defer word that is called once during the terminal emulator initialization sequence in order to completely clear all pixels on the display. This word is called just before reset-screen, so that the user doesn’t actually see the frame buffer data until it has been properly scrubbed.
This word is initially empty, but must be loaded with an appropriate routine in order for the terminal emulator to function correctly. This can be done with to, or it can be loaded automatically with fb1-install or fb8-install (which loads fb1-erase-screen or fb8-erase-screen, respectively).

**eval**

- **stack:** ( ??? str len -- ??? )
- **code:** CD
- **generates:** `evaluate`
- **Synonym for evaluate.**

**evaluate**

- **stack:** ( ??? str len -- ??? )
- **code:** CD
- **Executes a string as a sequence of Forth commands. The overall stack effect depends on the commands being executed. For example:**

```
" 4000 20 dump" evaluate
```

You can use `evaluate`, like `$find`, to find and execute Forth commands that are not FCodes.

The same cautions apply to `evaluate` as for `$find` in that programs executing Forth commands are likely to encounter portability problems when moved to other systems.

**execute**

- **stack:** ( ??? xt -- ??? )
- **code:** ID
- **Executes the word definition whose execution token is xt. An error condition exists if xt is not an execution token.**

For example:

```
: my-word ( addr len -- )
  ." Given string is: " type cr
;
" great" ['] my-word execute
```
exit stack: ( -- ) ( R: sys -- )
code: 33

Compiled in a colon definition. When encountered, execution leaves the current word and returns control to the calling word. If used in a do loop must be preceded by unloop.

For example:

```
: probe-loop ( addr -- )
  \ generate a tight probe loop until any key is pressed.
  begin dup 10 drop key? if drop exit then again
;
```

See also: leave, unloop.

---

expect stack: ( addr len -- )
code: 8A

A defer word that receives a line of characters from the keyboard and stores them into memory, performing line editing as the characters are typed. Displays all characters actually received and stored into memory. The number of received characters is stored in span.

The transfer begins at addr proceeding towards higher addresses one byte per character until either a carriage return is received or until len characters have been transferred. No more than len characters will be stored. The carriage return is not stored into memory. No characters are received or transferred if len is zero.

For example:

```
h# 10 buffer: my-name-buff
: hello ( -- )
  ." Enter Your First name " my-name-buff h# 10 expect
  ." Sun Microsystems Welcomes " my-name-buff span @ type cr
;
```
**external** stack: ( -- )  
code: none  

After issuing `external`, all subsequent definitions are created so that names are later compiled into RAM, regardless of the value of the NVRAM variable `fcode-debug?`. `external` is used to define the package methods that may be called from other software external to the package, and whose names must therefore be present.

`external` stays in effect until `headers` or `headerless` is encountered.

For example:

```
external
: open ( -- ok? ) ... ;
```

**external-token** stack: ( -- )  
code: CA  

A token-type that is used to indicate that this word should always be compiled with the name header present. Activated by `external`, all subsequent words are created with `external-token` until deactivated with either `headers` or `headerless`. See named-token for more details. This word should never be used in source code.

**false** stack: ( -- false )  
code: A5  
generates: 0  

Leaves the value for `false` (i.e. zero) on the stack.

**fb1-blink-screen** stack: ( -- )  
code: 01 74  
The built-in default routine to blink or flash the screen momentarily on a generic 1-bit-per-pixel frame buffer. This routine is loaded into the `defer` word `blink-screen` by calling `fb1-install`. 
This routine is invalid unless the FCode Program has called \texttt{fb1-install} and has initialized \texttt{frame-buffer-adr} to a valid virtual address.

This word is implemented simply by calling \texttt{fb1-invert-screen} twice. In practice, this can be quite slow (around one full second). It is quite common for a frame buffer FCode Program to replace \texttt{fb1-blink-screen} with a custom routine that simply disables the video for 20 milliseconds or so. For example:

\begin{verbatim}
: my-blink-screen ( -- ) video-off 20 ms video-on ;
... fb1-install...
['] my-blink-screen to blink-screen
\end{verbatim}

\textbf{\texttt{fb1-delete-characters}}

\begin{verbatim}
stack: ( n -- )
code: 01 77

The built-in default routine to delete \texttt{n} characters at and to the right of the cursor, on a generic 1-bit-per-pixel frame buffer. This routine is loaded into the \texttt{defer} word \texttt{delete-characters} by calling \texttt{fb1-install}.

This routine is invalid unless the FCode Program has called \texttt{fb1-install} and \texttt{set-font} and has initialized \texttt{frame-buffer-adr} to a valid virtual address.

The cursor position is unchanged, the cursor character and the next \texttt{n-1} characters to the right of the cursor are deleted, and the remaining characters to the right are moved left by \texttt{n} places. The end of the line is filled with blanks.
\end{verbatim}

\textbf{\texttt{fb1-delete-lines}}

\begin{verbatim}
stack: ( n -- )
code: 01 79

The built-in default routine to delete \texttt{n} lines, starting with the line below the cursor line, on a generic 1-bit-per-pixel frame buffer. This routine is loaded into the \texttt{defer} word \texttt{delete-lines} by calling \texttt{fb1-install}.

This routine is invalid unless the FCode Program has called \texttt{fb1-install} and \texttt{set-font} and has initialized \texttt{frame-buffer-adr} to a valid virtual address.
\end{verbatim}
The \( n \) lines at and below the cursor line are deleted. All lines above the cursor line are unchanged. The cursor position is unchanged. All lines below the deleted lines are scrolled upwards by \( n \) lines, and \( n \) blank lines are placed at the bottom of the active text area.

**fbl-draw-character**

stack: ( char -- )
code: 01 70

The built-in default routine for drawing a character on a generic 1-bit-per-pixel frame buffer, at the current cursor location. This routine is loaded into the defer word `draw-character` by calling `fbl-install`.

This routine is invalid unless the FCode Program has called `fbl-install` and `set-font` and has initialized `frame-buffer-adr` to a valid virtual address.

If `inverse?` is true, then characters are drawn inverted (white-on-black). Otherwise (the normal case) they are drawn black-on-white.

**fbl-draw-logo**

stack: ( line# logoaddr logowidth logoheight -- )
code: 01 7A

The built-in default routine to draw the logo on a generic 1-bit-per-pixel frame buffer. This routine is loaded into the defer word `draw-logo` by calling `fbl-install`.

This routine is invalid unless the FCode Program has called `fbl-install` and `set-font` and has initialized `frame-buffer-adr` to a valid virtual address.

See `draw-logo` for more information on the parameters passed.

**fbl-erase-screen**

stack: ( -- )
code: 01 73

The built-in default routine to clear (erase) every pixel in a generic 1-bit-per-pixel frame buffer. This routine is loaded into the defer word `erase-screen` by calling `fbl-install`.

This routine is invalid unless the FCode Program has called `fbl-install` and has initialized `frame-buffer-adr` to a valid virtual address.
All pixels are erased (not just the ones in the active text area). If inverse-screen? is true, then all pixels are set to 1, resulting in a black screen. Otherwise (the normal case) all pixels are set to 0, resulting in a white screen.

`fb1-insert-characters`

Stack: ( n -- )
Code: 01 76

The built-in default routine to insert n blank characters to the right of the cursor, on a generic 1-bit-per-pixel frame buffer. This routine is loaded into the defer word `insert-characters` by calling `fb1-install`.

This routine is invalid unless the FCode Program has called `fb1-install` and `set-font` and has initialized `frame-buffer-adr` to a valid virtual address.

The cursor position is unchanged, but the cursor character and all characters to the right of the cursor are moved right by n places. An error condition exists if an attempt is made to create a line longer than the maximum line size (the value in #columns).

`fb1-insert-lines`

Stack: ( n -- )
Code: 01 78

The built-in default routine to insert n blank lines below the cursor on a generic 1-bit-per-pixel frame buffer. This routine is loaded into the defer word `insert-lines` by calling `fb1-install`.

This routine is invalid unless the FCode Program has called `fb1-install` and `set-font` and has initialized `frame-buffer-adr` to a valid virtual address.

The cursor position on the screen is unchanged. The cursor line is pushed down, but all lines above it are unchanged. Any lines pushed off of the bottom of the active text area are lost.

`fb1-install`

Stack: ( width height #columns #lines -- )
Code: 01 7B

This built-in routine installs all of the built-in default routines for driving a generic 1-bit-per-pixel frame buffer. It also initializes most necessary values needed for using these default routines.
set-font must be called, and frame-buffer-adr initialized, before
fb1-install is called, because the char-width and char-height values
set by set-font are needed when fb1-install is executed.

fb1-install loads the following defer routines with their corresponding
fb1-(whatever) equivalents: reset-screen, toggle-cursor, erase-
screen, blink-screen, invert-screen, insert-characters, delete-
characters, insert-lines, delete-lines, draw-character, draw-
logo.

The following values are initialized:

- *screen-width* - set to the value of the passed-in parameter *width* (screen
  width in pixels)
- *screen-height* - set to the value of the passed-in parameter *height* (screen
  height in pixels)
- *#columns* - set to the smaller of the following two: the passed-in parameter
  *#columns*, and the NVRAM parameter *screen-#columns*
- *#lines* - set to the smaller of the following two: the passed-in parameter
  *#lines*, and the NVRAM parameter *screen-#rows*
- *window-top* - set to half of the difference between the total screen height
  (*screen-height*) and the height of the active text area (*#lines* times
  *char-height*)
- *window-left* - set to half of the difference between the total screen width
  (*screen-width*) and the width of the active text area (*#columns* times
  *charwidth*), then rounded down to the nearest multiple of 32 (for
  performance reasons)

Several internal values used by various fb1- routine are also set.
The built-in default routine to invert every visible pixel on a generic 1-bit-per-pixel frame buffer. This routine is loaded into the defer word invert-screen by calling fb1-install.

This routine is invalid unless the FCode Program has called fb1-install and has initialized frame-buffer-adr to a valid virtual address.

All pixels are inverted (not just the ones in the active text area).

The built-in default routine to enable a generic 1-bit-per-pixel frame buffer to display data. This routine is loaded into the defer word reset-screen by calling fb1-install. (reset-screen is called just after erase-screen during the terminal emulator initialization sequence.)

This word is initially a NOP. Typically, an FCode Program will define a hardware-dependent routine to enable video, and then replace this generic function with:

```
: my-video-enable ( -- ) ... :
  fb1-install
...  
['] my-video-enable  to reset-screen
```

This is a utility routine. It behaves exactly like fb1-delete-lines, except that it doesn’t clear the lines at the bottom of the active text area. Its only purpose is to scroll the enable plane for frame buffers that have 1-bit overlay and enable planes.

This routine is invalid unless the FCode Program has called fb1-install and set-font and has initialized frame-buffer-adr to a valid virtual address.
**fb1-toggle-cursor**

stack: ( -- )
code: 01 72

The built-in default routine to toggle the cursor location in a generic 1-bit-per-pixel frame buffer. This routine is loaded into the `defer` word `toggle-cursor` by calling `fb1-install`. The behavior is to invert every pixel in the one-character-size space for the current position of the text cursor.

This routine is invalid unless the FCode Program has called `fb1-install` and `set-font` and has initialized `frame-buffer-adr` to a valid virtual address.

**fb8-blink-screen**

stack: ( -- )
code: 01 84

The built-in default routine to blink or flash the screen momentarily on a generic 8-bit-per-pixel frame buffer. This routine is loaded into the `defer` word `blink-screen` by calling `fb8-install`.

This routine is invalid unless the FCode Program has called `fb8-install` and has initialized `frame-buffer-adr` to a valid virtual address.

This word is implemented simply by calling `fb8-invert-screen` twice. In practice, this can be very slow (several seconds). It is quite common for a frame buffer FCode Program to replace `fb8-blink-screen` with a custom routine that simply disables the video for 20 milliseconds or so. For example:

```
: my-blink-screen ( -- ) video-off 20 ms video-on ;
...
fb8-install
...
['] my-blink-screen to blink-screen
```

**fb8-delete-characters**

stack: ( n -- )
code: 01 87

The built-in default routine to delete n characters to the right of the cursor, on a generic 8-bit-per-pixel frame buffer. This routine is loaded into the `defer` word `delete-characters` by calling `fb8-install`. 
This routine is invalid unless the FCode Program has called \texttt{fb8-install} and \texttt{set-font} and has initialized \texttt{frame-buffer-adr} to a valid virtual address.

The cursor position is unchanged. The cursor character and the next $n-1$ characters to the right of the cursor are deleted, and the remaining characters to the right are moved left by $n$ places. The end of the line is filled with blanks.

\begin{verbatim}
\textbf{fb8-delete-lines} stack: ( n -- )
code: 01 89

The built-in default routine to delete $n$ lines, starting with the line below the cursor line, on a generic 8-bit-per-pixel frame buffer. This routine is loaded into the \texttt{defer} word \texttt{delete-lines} by calling \texttt{fb8-install}.

This routine is invalid unless the FCode Program has called \texttt{fb8-install} and \texttt{set-font} and has initialized \texttt{frame-buffer-adr} to a valid virtual address.

The $n$ lines at and below the cursor line are deleted. All lines above the cursor line are unchanged. The cursor position is unchanged. All lines below the deleted lines are scrolled upwards by $n$ lines, and $n$ blank lines are placed at the bottom of the active text area.
\end{verbatim}

\begin{verbatim}
\textbf{fb8-draw-character} stack: ( char -- )
code: 01 80

The built-in default routine for drawing a character on a generic 8-bit-per-pixel frame buffer, at the current cursor location. This routine is loaded into the \texttt{defer} word \texttt{draw-character} by calling \texttt{fb8-install}.

This routine is invalid unless the FCode Program has called \texttt{fb8-install} and \texttt{set-font} and has initialized \texttt{frame-buffer-adr} to a valid virtual address.

If \texttt{inverse?} is true, then characters are drawn inverted (white-on-black). Otherwise (the normal case) they are drawn black-on-white.
\end{verbatim}

\begin{verbatim}
\textbf{fb8-draw-logo} stack: ( line# addr width height -- )
code: 01 8A

The built-in default routine to draw the logo on a generic 8-bit-per-pixel frame buffer. This routine is loaded into the \texttt{defer} word \texttt{draw-logo} by calling \texttt{fb8-install}.
\end{verbatim}
This routine is invalid unless the FCode Program has called fb8-install and set-font and has initialized frame-buffer-adr to a valid virtual address.

See draw-logo for more information on the parameters passed.

---

**fb8-erase-screen**

stack: ( -- )
code: 01  83

The built-in default routine to clear (erase) every pixel in a generic 8-bit-per-pixel frame buffer. This routine is loaded into the defer word erase-screen by calling fb8-install.

This routine is invalid unless the FCode Program has called fb8-install and has initialized frame-buffer-adr to a valid virtual address.

All pixels are erased (not just the ones in the active text area). If inverse-screen? is true, then all pixels are set to 0xff, resulting in a black screen. Otherwise (the normal case) all pixels are set to 0, resulting in a white screen.

---

**fb8-insert-characters**

stack: ( n -- )
code: 01  86

The built-in default routine to insert $n$ blank characters to the right of the cursor, on a generic 8-bit-per-pixel frame buffer. This routine is loaded into the defer word insert-characters by calling fb8-install.

This routine is invalid unless the FCode Program has called fb8-install and set-font and has initialized frame-buffer-adr to a valid virtual address.

The cursor position is unchanged, but the cursor character and all characters to the right of the cursor are moved right by $n$ places. An error condition exists if an attempt is made to create a line longer than the maximum line size (the value in #columns).

---

**fb8-insert-lines**

stack: ( n -- )
code: 01  88

The built-in default routine to insert $n$ blank lines below the cursor on a generic 8-bit-per-pixel frame buffer. This routine is loaded into the defer word insert-lines by calling fb8-install.
This routine is invalid unless the FCode Program has called `fb8-install` and `set-font` and has initialized `frame-buffer-adr` to a valid virtual address.

The cursor position is unchanged. The cursor line is pushed down, but all lines above it are unchanged. Any lines pushed off of the bottom of the active text area are lost.

---

**fb8-install** stack: ( width height #columns #lines -- )
code: 01 8B

This built-in routine installs all of the built-in default routines for driving a generic 8-bit-per-pixel frame buffer. It also initializes most necessary values needed for using these default routines.

`set-font` must be called, and `frame-buffer-adr` initialized, before `fb8-install` is called, because the `char-width` and `char-height` values set by `set-font` are needed when `fb8-install` is executed.

`fb8-install` loads the following defer routines with their corresponding `fb8-`(whatever) equivalents: `reset-screen`, `toggle-cursor`, `erase-screen`, `blink-screen`, `invert-screen`, `insert-characters`, `delete-characters`, `insert-lines`, `delete-lines`, `draw-character`, `draw-logo`

The following values are initialized:

- `screen-width` - set to the value of the passed-in parameter `width` (screen width in pixels)
- `screen-height` - set to the value of the passed-in parameter `height` (screen height in pixels)
- `#columns` - set to the smaller of the following two: the passed-in parameter `#columns`, and the NVRAM parameter `screen-#columns`
- `#lines` - set to the smaller of the following two: the passed-in parameter `#lines`, and the NVRAM parameter `screen-#rows`
- `window-top` - set to half of the difference between the total screen height (`screen-height`) and the height of the active text area (`#lines` times `char-height`)
window-left - set to half of the difference between the total screen width (screen-width) and the width of the active text area (#columns times char-width), then rounded down to the nearest multiple of 32 (for performance reasons)

Several internal values are also set that are used by various fb8- routines.

**fb8-invert-screen**

| stack: ( -- ) |
| code: 01 85 |

The built-in default routine to XOR (with hex 0xFF) every visible pixel on a generic 8-bit-per-pixel frame buffer. This routine is loaded into the defer word invert-screen by calling fb8-install.

This routine is invalid unless the FCode Program has called fb8-install and has initialized frame-buffer-adr to a valid virtual address.

All pixels are inverted (not just those in the active text area).

**fb8-reset-screen**

| stack: ( -- ) |
| code: 01 81 |

The built-in default routine to enable a generic 8-bit-per-pixel frame buffer to display data. This routine is loaded into the defer word reset-screen by calling fb8-install. (reset-screen is called just after erase-screen during the terminal emulator initialization sequence.)

This word is initially a NOP. Typically, an FCode Program will define a hardware-dependent routine to enable video, and then replace this generic function with:

```
: my-video-enable ( -- ) ... :
  fb8-install
...
['] my-video-enable to reset-screen
```
**fb8-toggle-cursor** stack: ( -- )
code: 01  82

The built-in default routine to toggle the cursor location in a generic 8-bit-per-pixel frame buffer. This routine is loaded into the defer word `toggle-cursor` by calling `fb8-install`. The behavior is to XOR every pixel with 0xFF in the one-character-size space for the current position of the text cursor.

This routine is invalid unless the FCode Program has called `fb8-install` and `set-font` and has initialized `frame-buffer-adr` to a valid virtual address.

**fcode-revision** stack: ( -- n )
code: 87

Returns a 32-bit number identifying the version of the device interface. The high 16 bits is the major version number and the low 16 bits is the minor version number. The revision of the device interface described by *IEEE Standard 1275-1994* is “3.0”. In a system compatible with that specification, `fcode-revision` will return 0x0003.0000.

For example:

```
: exit-if-not-1275-1994 ( -- )
  fcode-revision h# 30000 < if '[' end0 execute then
```

**fcode-version1** stack: ( -- )
code: FD 00
generates: `version1 (null)(reserved)(length)`

This tokenizer macro is used to start FCode programs intended to be compatible with early OpenBoot systems.

`fcode-version1` generates the FCode header for an FCode program (based on tokenizer switches). If the default tokenizer switches are used, `fcode-version1` begins the header with the `version1` FCode as:

```
(fd) version1 ( 1 byte )
```
The length field specifies the total usable length of FCode data, from version1 to end0 inclusive. Additional end0 bytes are appended to the end of the data, if needed, to leave a total length which is evenly divisible by 4. The null byte position may be used to carry a version number or other information.

**fcode-version2**

<table>
<thead>
<tr>
<th>Stack: ( -- )</th>
</tr>
</thead>
<tbody>
<tr>
<td>code: F1 00</td>
</tr>
<tr>
<td>generates: start1 (null) (reserved) (length)</td>
</tr>
</tbody>
</table>

Starts a version2 FCode program, generating an 8-byte header similar to fcode-version1, except that the starting byte is start1 (f1) instead of version1 (fd).

For example:

```fcode-version2
" SUNW,nvsmim" encode-string " name" property
...
end0```

**Caution** – FCode programs created with fcode-version2 will only run on OpenBoot 2 or later systems. They will not work on OpenBoot 1.0 systems.

**ferror**

<table>
<thead>
<tr>
<th>Stack: ( -- )</th>
</tr>
</thead>
<tbody>
<tr>
<td>code: FC</td>
</tr>
<tr>
<td>Displays an “Unimplemented FCode” error message and stops FCode interpretation at the completion of the function whose evaluation resulted in the execution of ferror. All unimplemented FCode numbers resolve to ferror in OpenBoot.</td>
</tr>
</tbody>
</table>

The intended use of ferror is to determine whether or not a particular FCode is implemented, without checking the FCode version number.
For example:

```fcode
: implemented? ( xt -- flag) '['] ferror <> ;
: my-peer ( prev -- next )
  '['] peer implemented? if
  peer
  else
  ." peer is not implemented" cr
  then

;)
```

**field** stack: ( E: addr -- addr+offset ) ( offset size “new-name<>” -- offset+size )
code: (header) BE
generates: new-token|named-token|external-token b(field)

**struct** and **field** are used to create named offset pointers into a structure. For each field in the structure, a name is assigned to the location of that field (as an offset from the beginning of the structure).

The structure being described is:

```
\ size  Bytes 0 - 1
\ flags Bytes 2 - 5
\ bits  Byte 6
\ key   Byte 7
\ fullname Bytes 8 - 17
\ initials Bytes 8 - 9
\ lastname Bytes 10 - 17
\ age   Bytes 18 - 19
```
The field definitions are shown below. (The numbers in parentheses show the stack after each word is created.)

<table>
<thead>
<tr>
<th>Field Name</th>
<th>Stack</th>
</tr>
</thead>
<tbody>
<tr>
<td>struct</td>
<td>(0)</td>
</tr>
<tr>
<td>2 field size</td>
<td>(2)</td>
</tr>
<tr>
<td>4 field flags</td>
<td>(6)</td>
</tr>
<tr>
<td>1 field bits</td>
<td>(7)</td>
</tr>
<tr>
<td>1 field key</td>
<td>(8)</td>
</tr>
<tr>
<td>0 field fullname</td>
<td>(8)</td>
</tr>
<tr>
<td>2 field initials</td>
<td>(10)</td>
</tr>
<tr>
<td>8 field lastname</td>
<td>(18)</td>
</tr>
<tr>
<td>2 field age</td>
<td>(20)</td>
</tr>
<tr>
<td>constant /record</td>
<td>()</td>
</tr>
</tbody>
</table>

Typical usage of these defined words would be:

```
/record buffer: myrecord \ Create the "myrecord" buffer
myrecord flags l@       \ get flags data
myrecord key  c@         \ get key data
myrecord size w@         \ get size data
/record                 \ get total size of the array
```

Note that struct is primarily a documentation aid that the initial value of the structure’s size (i.e. 0) on the stack.

**fill** stack: ( addr len byte -- )

code: 79

Sets len bytes of memory beginning at addr to the value byte. No action is taken if len = 0.

**$find** stack: ( name-str name-len -- xt true | name-str name-len false )

code: CB

Takes a string from the stack and searches the current search order for it. During normal FCode evaluation, the search order consists of the vocabulary containing the visible methods of the current device node, followed by the Forth vocabulary.
If the word is not found, the original string is left on the stack, with a \textit{false} on top of the stack. If the word is found, the execution token of that word is left on the stack with \textit{true} on top of the stack.

\$\textit{find} is an escape hatch, allowing an FCode Program to perform any function that is available in the OpenBoot User Interface but that is not defined as part of the standard FCode interface.

Use \$\textit{find} with caution! Different systems or even different versions of OpenBoot may implement different subsets of the User Interface. If your FCode Program depends on a User Interface word, it might not work on some systems.

Example of use:

\begin{verbatim}
" root-info" $find  ( addr len false | xt true )
if execute    \ if found, then do the function
else ( addr len ) type " was not found!" cr
then
\end{verbatim}

\begin{table}

\begin{verbatim}
\textbf{find-method} stack: method-str method-len phandle -- false | xt true 
code: 02 07

Locates the method named by \textit{method-str method-len} in the package \textit{phandle}. Returns \textit{false} if the package has no such method, or \textit{xt} and \textit{true} if the operation succeeds. Subsequently, \textit{xt} can be used with \textit{call-package}.

For example:

\begin{verbatim}
: tftp-load-avail? ( -- exist? )
  " obp-tftp" find-package if { phandle }
  " load" rot find-method if { xt }
  drop true exit
then
then
false
;
\end{verbatim}
\end{verbatim}
\end{table}
**find-package**

stack: ( name-str name-len -- false | phandle true )

code: 02 04

Locates a package whose name is given by the string `name-str name-len`. If the package can be located, returns its `phandle` and `true`. Otherwise returns `false`.

The name is interpreted relative to the `/packages` device node. For example, if `name-str name-len` represents the string "disk-label", the package in the device tree at "/packages/disk-label" will be located.

If there are multiple packages with the same name (in the `/packages` node), the `phandle` for the most recently created one is returned.

For example:

```fcode
: tftp-load-avail? ( -- exist? )
  " obp-tftp" find-package if ( phandle )
  " load" rot find-method if ( xt )
  drop true exit
  then
  then
  false
;
```

**finish-device**

stack: ( -- )

code: 01 27

The two words `finish-device` and `new-device` let a single FCode Program declare more than one entry into the device tree. This capability is useful when a single SBus card contains two or more essentially independent devices, to be controlled by two or more separate operating system device drivers.
Typical usage:

```
version1
...driver#1...
finish-device \ terminate device tree entry#1
new-device \ begin a new device tree entry
...driver#2
finish-device \ terminate device tree entry#2
new-device \ begin a new device tree entry
...driver#3...
end0
```

There is an implicit `new-device` call at the beginning of an FCode Program (at `version1` or `start1`), and an implicit `finish-device` call at the end of an FCode Program (at `end0`). Thus, FCode Programs that only define a single device and driver will never need to call `finish-device` or `new-device`.

**fload**

```
stack: ([filename<cr>] --)
code: none
```

This command allows FCode text programs to be broken into function blocks for better clarity, portability and re-use. It behaves similarly to the `#include` statement in the C language. Arbitrary nesting of files with `fload` is allowed i.e. an `fload`'d file may include `fload` commands.

When `fload` is encountered, the Tokenizer continues tokenizing the FCode found in the file `filename`. When the filename has been tokenized, tokenizing resumes on the file that called `filename` with `fload`.

For example:

```
fload my-disk-package.fth
```

**Note** – `fload` commands won’t work when downloading text in source-code form. You can either manually merge the files into one larger text file and use `dl` for downloading, or you can tokenize the files first and then download and execute the FCode in binary form.
>font stack: ( char -- addr )
code: 01 6E

This routine converts a character value (ASCII 0-0xFF) into the address of the font table entry for that character. For the normal, built-in font, only ASCII values 0x21-0x7E result in a printable character, other values will be mapped to a font entry for “blank”.

This word is only of interest if you are implementing your own character-drawing routines. Note that >font will generate invalid results unless set-font has been called to initialize the font table to be used.

fontbytes stack: ( -- bytes )
code: 01 6F

A value, containing the interval between successive entries in the font table. Each entry contains the next scan line bits for the desired character. Each scan line is normally 12 pixels wide, and is stored as one bit per pixel, thus taking 1 1/2 bytes per scan line. The standard value for fontbytes is 2, meaning that the next scan line entry is 2 bytes after the previous one (the last 1/2 byte is wasted space).

This word must be set to the appropriate value if you wish to use any fb1- or fb8- utility routines or >font. This can be done with to, but is normally done by calling set-font.

The standard value for fontbytes is one of the parameters returned by default-font.

frame-buffer-adr stack: ( -- addr )
code: 01 62

This value returns the virtual address of the beginning of the current frame buffer memory. It must be set to an appropriate virtual address (using to) in order to use any of the fb1- or fb8- utility routines. It is suggested that this same value variable be used in any of your custom routines that require a frame buffer address, although of course you are free to create and use your own variable if you wish.
Generally, you should only map in the frame buffer memory just before you are ready to use it, and unmap it if it is no longer needed. Typically, this means you should do your mapping in your “install” routine, and unmap it in your “remove” routine (see is-install and is-remove). Here’s some sample code:

```fcode
h# 2.0000 constant /frame \ # of bytes in frame buffer
h# 40.0000 constant foffset \ Location of frame buffer

: video-map ( -- )
  my-address foffset + /frame map-low to frame-buffer-adr

: video-unmap ( -- )
  frame-buffer-adr /frame free-virtual
  -1 to frame-buffer-adr \ Flag accidental accesses to a
  \ now-illegal address

: power-on-selftest ( -- )
  video-map
  ( test video memory )
  video-unmap

power-on-selftest

: my-install ( -- )
  video-map
  ...

: my-remove ( -- )
  video-unmap
  ...

' ['] my-install is-install
' ['] my-remove is-remove
```

`free-mem` stack: ( a-addr len -- )
code: 8C

Frees up `len` memory allocated by alloc-mem. The arguments `a-addr` and `len` must be the same as those used in a previous alloc-mem command.
For example:

```plaintext
0 value my-string \ Holds address of temporary
: .upc-string ( addr len -- ) \ convert to uppercase and print.
  dup alloc-mem to my-string ( addr len )
  tuck my-string swap move ( len )
  my-string over bounds ?do i c@ upc i c! loop ( len )
  my-string over type ( len )
  my-string swap free-mem
;
```

**free-virtual**

stack: ( virt size -- )
code: 01 05

Destroys an existing mapping and any "address" property.

If the package associated with the current instance has an "address" property whose first value encodes the same address as `virt`, delete that property. In any case, execute the parent instance’s map-out method with `virt` size as its arguments.

**get-inherited-property**

stack: ( name-str name-len -- true | prop-addr prop-len false )
code: 02 1D

Locates, in the package associated with the current instance or any of its parents, the property whose name is `name-addr name-len`. If the property exists, returns the property value array `prop-addr prop-len` and `false`. Otherwise returns `true`.

The order in which packages are searched is the current instance first, followed by its immediate parent, followed by its parent’s parent, and so on. This is useful for properties with default values established by a parent node, with the possibility of a particular child node “overriding” the default value.
For example:

```fcode
: clock-frequency ( -- val.addr len false | true )
  " clock-frequency" get-inherited-property
;
```

**get-msecs**

stack: ( -- n )

code: 01 25

Returns the current value in a free-running system counter. The number returned is a running total, expressed in milliseconds. You can use this for measuring time intervals (by comparing the starting value with the ending value). No assumptions should be made regarding the absolute number returned; only relative interval comparisons are valid.

No assumptions should be made regarding the precision of the number returned. In some systems, the value is derived from the system clock, which typically ticks once per second. Thus, the value returned by `get-msecs` on such a system will be seen to increase in jumps of 1000 (decimal), once per second.

For a delay timer of millisecond accuracy, see `ms`.

```fcode
: show-model-name ( -- )
  " model" get-my-property 0= if ( val.addr len )
    ." model name is " type cr
  else ( )
    ." model property is missing " cr
  then ( )
;
```

**get-my-property**

stack: ( name-str name-len -- true | prop-addr prop-len false )

code: 02 1A

Locates, in the package associated with the current instance, the property named by `name-addr name-len`. If the property exists, returns the property value array `val-addr val-len` and `false`. Otherwise returns `true`.

For example:
get-package-property

stack: ( name-str name-len phandle -- true | prop-addr prop-len false )
code: 02 1F

Locates, in the package phandle, the property named by name-addr name-len. If the property exists, returns the property value array prop-addr prop-len and false. Otherwise returns true.

For example:

```lisp
: show-model-name ( -- )
    my-self ihandle>phandle ( phandle )
    " model" rot get-package-property 0= if ( val.addr len )
        ." model name is " type cr
    else ( )
        ." model property is missing " cr
    then ( )
```

get-token

stack: ( fcode# -- xt immediate? )
code: DA

Returns the execution token xt of the word associated with FCode number fcode# and a flag immediate? that is true if and only if that word will be executed (rather than compiled) when the FCode Evaluator encounters its FCode number while in compilation state.

h# number

stack: ( -- )
code: 10 xx xx xx xx xx xx xx

Causes the compiler/interpreter to interpret the immediately following number as a hexadecimal number (base sixteen), regardless of any previous settings of hex, or decimal. Only the immediately following number is affected. The value of base is unchanged.
For example:

```
decimal
h# 100 ( equals decimal 256 )
100   ( equals decimal 100 )
```

See also: d#.

```
.h stack: ( n -- )
  code: a0 6d 49 10 00 00 10 a0 72 9d a0 72
  generates: base @ swap d# 16 base ! . base !
```

Displays $n$ in hex (using .) The value of base is not permanently affected.

```
headerless stack: ( -- )
code: none

Causes all subsequent FCode definitions to be created without the name field (the “head”). (See named-token and new-token.) This is sometimes done to save space in the final FCode PROM, or possibly to make it more difficult to reverse-engineer an FCode Program.

All such headerless words can be used normally in the FCode Program, but cannot be called interactively from the User Interface for testing and development purposes.

Unless PROM space and/or dictionary space is a major consideration, try not using headerless words, because they make debugging more difficult.

headerless remains in effect until headers or external is encountered.

For example:

```
headerless
h# 3 constant reset-scsi
```
headers stack: ( -- )
code: none

Causes all subsequent definitions to be saved with the name field (the “head”) intact. This is the initial default behavior.

Note that even normal FCode words (with heads) cannot be called interactively from the User Interface unless the NVRAM parameter fcodedebug? has been set to true before a system reset.

headers remains in effect until headerless or external is encountered.

For example:

```
headers
  : cnt@  ( -- w )
  transfer-count-lo rb@
  transfer-count-hi rb@
  bwjoin

;```

here stack: ( -- addr )
code: AD

here returns the address of the next available dictionary location.

hex stack: ( -- )
code: 10 00 00 00 10 a0 72
generates: b(lit) 16 base !

If used outside of a definition, commands the tokenizer program to interpret subsequent numbers in hex (base 16). If used in a definition, changes the value in base affecting later numeric output when the FCode Program is executed.

See also: base.
**hold** stack: ( char -- )
code: 95

Inserts char into a pictured numeric output string. Typically used between <# and #>.

For example:

```
: .32 ( n -- )
  base @ >r hex
  <# ## # ascii . hold ### #> type
  r> base !
space
;
```

**i** stack: ( -- index ) ( R: sys -- sys )
code: 19

index is a copy of the loop index. May only be used inside of a do or ?do loop.

For example:

```
: simple-loop ( start len -- )
  bounds ?do i .h cr loop
;
```

**if** stack: ( C: -- orig-sys ) ( do-next? -- )
code: 14 +offset
generates: b?branch +offset

Execute the following code if do-next? is true. Used in the form:

```
do-next? if...else...then
```

or

```
do-next? if...then
```
If \textit{do-next?} is true, the words following \textit{if} are executed and the words following \textit{else} are skipped. The \textit{else} part is optional. If \textit{do-next?} is false, words from \textit{if} through \textit{else}, or from \textit{if} through \textit{then} (when no \textit{else} is used), are skipped.

\textbf{ihandle>phandle} \hfill stack: ( ihandle -- phandle ) \hfill code: 02 0B

Returns the \textit{phandle} of the package from which the instance \textit{ihandle} was created. This is often used with \texttt{get-package-property} to read the properties of the package corresponding to a given \textit{ihandle}.

For example:

\begin{verbatim}
: show-parent ( -- )
   my-parent ihandle>phandle " name" rot
   get-package-property 0= if
   ." my-parent is " type cr
   then
;  
\end{verbatim}

\textbf{insert-characters} \hfill stack: ( n -- ) \hfill code: 01 5D

\textit{insert-characters} is one of the \texttt{defer} words of the display device interface. The terminal emulator package executes \texttt{insert-characters} when it has processed a character sequence that calls for opening space for characters to the right of the cursor. Without moving the cursor, \texttt{insert-characters} moves the remainder of the line to the right, thus losing the \textit{n} rightmost characters in the line, and fills the \textit{n} vacated character positions with the background color.

This word is initially empty, but must be loaded with an appropriate routine in order for the terminal emulator to function correctly. This can be done with \texttt{to}, or it can be loaded automatically with \texttt{fb1-install} or \texttt{fb8-install} (which loads \texttt{fb1-insert-characters} or \texttt{fb8-insert-characters}, respectively).
**insert-lines**  
stack: ( n -- )  
code: 01 5F

*insert-lines* is one of the defer words of the display device interface. The terminal emulator package executes *insert-lines* when it has processed a character sequence that calls for opening space for lines of text below the cursor. Without moving the cursor, *insert-lines* moves the cursor line and all following lines down, thus losing the *n* bottom lines, and fills the *n* vacated lines with the background color.

This word is initially empty, but must be loaded with an appropriate routine in order for the terminal emulator to function correctly. This can be done with to, or it can be loaded automatically with fb1-install or fb8-install (which loads fb1-insert-lines or fb8-insert-lines, respectively).

---

**instance**  
stack: ( -- )  
code: C0

Modifies the next occurrence of value, variable, defer or buffer: to create instance-specific data instead of static data. Re-allocates the data each time a new instance of this package is created.

For example:

```
-1 instance value my-chip-reg
```

---

**inverse?**  
stack: ( -- white-on-black? )  
code: 01 54

This value is part of the display device interface. The terminal emulator package sets inverse? to true when the escape sequences that it has processed have indicated that subsequent characters are to be shown with foreground and background colors exchanged, and to false, indicating normal foreground and background colors, otherwise.

The fb1- and fb8- frame buffer support packages draw characters with foreground and background colors exchanged if inverse? is true, and with normal foreground and background colors if inverse? is false.
inverse? affects the character display operations draw-character, insert-characters, and delete-characters, but not the other operations such as insert-lines, delete-lines and erase-screen.

inverse-screen? should be monitored as needed if your FCode Program is implementing its own set of frame buffer primitives.

See also: inverse-screen?

**inverse-screen?**

```
stack: ( -- black? )
code: 01 55
```

This value is part of the display device interface. The terminal emulator package sets inverse-screen? to true when the escape sequences that it has processed have indicated that the foreground and background colors are to be exchanged for operations that affect the background, and to false, indicating normal foreground and background colors, otherwise.

The fb1- and fb8- frame buffer support packages perform screen drawing operations other than character drawing operations with foreground and background colors exchanged if inverse-screen? is true, and with normal foreground and background colors is false.

Inverse-screen? affects background operations such as insert-lines, delete-lines and erase-screen, but not character display operations such as draw-character, insert-characters and delete-characters.

When inverse-screen? and inverse? are both true, the colors are exchanged over the entire screen, and subsequent characters are not highlighted with respect to the (inverse) background. For exchanged screen colors and highlighted characters, the setting are inverse-screen? true and inverse? false.

inverse-screen? should be monitored as needed if your FCode Program is implementing its own set of frame buffer primitives.

**invert**

```
stack: ( x1 -- x2 )
code: 26
```

x2 is the one’s complement of x1 i.e. all the one bits in x1 are changed to zero, and all the zero bits are changed to one.
For example:

```
: clear-lastbit ( -- )
  my-reg rl@ 1 not and my-reg rl!

;
```

See also `0=`.

**invert-screen** stack: ( -- )
code: 01 5C

`invert-screen` is one of the `defer` words of the display device interface. The terminal emulator package executes `invert-screen` when it has processed a character sequence that calls for exchanging the foreground and background colors (e.g. changing from black-on-white to white-on-black).

`invert-screen` changes all pixels on the screen so that pixels of the foreground color are given the background color, and vice versa, leaving the colors that will be used by subsequent text output unaffected.

This word is initially empty, but *must* be loaded with an appropriate routine in order for the terminal emulator to function correctly. This can be done with `to`, or it can be loaded automatically with `fb1-install` or `fb8-install` (which loads `fb1-invert-screen` or `fb8-invert-screen`, respectively).

**is-stack** stack: ( param [ old-name <> ] -- )
code: C3 old FCode #

Synonym for `to`.

Creates `open`, `write`, and `draw-logo` methods for display devices.

For any SBus frame buffer that is to be used by the boot PROM before or during booting, `is-install` declares the FCode procedure that should be used to install (i.e. initialize) that frame buffer. Note that this is distinct from any once-only power-on initialization that should be performed during the probing process itself.
**is-install**

stack: ( xt -- )
code: 01 1C

Creates `open`, `write`, and `draw-logo` methods for display devices.

For any SBus frame buffer that is to be used by the boot PROM before or during booting, `is-install` declares the FCode procedure that should be used to install (i.e. initialize) that frame buffer. Note that this is distinct from any once-only power-on initialization that should be performed during the probing process itself.

The `is-install` routine and `is-remove` routine should comprise a matched pair that may be performed alternately as many times as needed. Typically, the `is-install` routine performs mapping functions and some initialization, and the `is-remove` performs any cleanup functions and then does a complementary unmapping.
A partial, typical code example follows:

```
version1
...
: power-on ( -- ) \ Once-only, power-on initialization
  map-register
  init-register
  unmap-register
;
...
: map-devices ( -- ) \ Map register and buffer
  map-register
  map-buffer
;
...
: install-me ( -- ) \ Do this to start using this device
  map-devices
  initialize-devices
;
...
: remove-me ( -- ) \ Do this to stop using this device
  reset-buffers
  unmap-devices
;
...
\ This routine executed during the probe of this FCode
: my-probe ( -- ) \ First, define the routine
  power-on \ Power-on initialization
  ['] install-me is-install \ Declare "install" routine
  ['] remove-me is-remove \ Declare "remove" routine
  ['] test-me is-selftest \ Declare "selftest" routine
;
\ End of the definition
my-probe \ Now execute the routine
end0
```
**is-remove**

stack: ( xt -- )
code: 01 1D

Creates a close method for display devices that will de-allocate a frame buffer that is no longer going to be used. Typical de-allocation would include unmapping memory and clearing buffers. For example:

```
version1
...
: remove-me ( -- ) \ Do this to stop using this device
  reset-buffers
  unmap-devices
;
...
\ This routine executed during the "probe" of this FCode
: my-probe ( -- ) \ First, define the routine
  power-on \ Power-on initialization
  ['] install-me is-install \ Declare "install" routine
  ['] remove-me is-remove \ Declare "remove" routine
  ['] test-me is-selftest \ Declare "selftest" routine
;
\ End of the definition
my-probe \ Now, execute this routine
end0
```

The routine loaded with **is-remove** should form a matched pair with the routine loaded with **is-install**. See **is-install** for more details.
is-selftest stack: ( xt -- )
code: 01 1E

Creates a selftest method for display devices that will perform a self test of the frame buffer. For example:

```
version1
...
: test-me ( -- fail? ) \ self test method
  ...
  ;
  ...
\ This routine executed during the "probe" of this FCode
: my-probe ( -- ) \ First, define the routine
  power-on \ Power-on initialization
  '[' install-me is-install \ Declare "install" routine
  '[' remove-me is-remove \ Declare "remove" routine
  '[' test-me is-selftest \ Declare "selftest" routine
  ; \ End of the definition
my-probe \ Now, execute this routine
end0
```

This declaration is typically performed in the same place in the code as is-install and is-remove.

The self test routine should return a status parameter on the stack indicating the results of the test. A zero value indicates that the test passed. Any nonzero value indicates that the self test failed, but the actual meaning for any nonzero value is not specified. (memory-test-suite returns a flag meeting these specifications.)

selftest is not automatically executed. For automatic testing, devices should perform a quick sanity check as part of the install routine. See "selftest" on page 52.
(is-user-word) stack: ( E: ... -- ??? ) ( name-str name-len xt -- )
code: 02 14

Creates a Forth word (not a package method) whose name is given by name-str
name-len and whose behavior is given by the execution token xt which must
refer to a static method. This allows an FCode Program to define new User
Interface commands.

For example:

" xyz-abort" ' my-abort (is-user-word)

j stack: ( -- index ) ( R: sys -- sys )
code: 1A

index is a copy of the index of the next outer loop. May only be used in a
nested do or ?do loop. For example:

do
  ...
  do ... j ... loop
  ...
loop

Usually, do loops should not be nested this deeply inside a single definition.
Forth programs are generally more readable if inner loops are defined inside a
separate word.

key stack: ( -- char )
code: 8E

A defer word that reads the next ASCII character from the keyboard. If no
character has been typed since key or expect was last executed, key waits
until a new character is typed. All valid ASCII characters can be received.
Control characters are not processed by the system for any editing purpose.
Characters received by key are not displayed.
For example:

```
: continue? ( -- continue? )
  ." Want to Continue? Enter Y/N" key dup emit
  dup ascii Y = ascii y rot = or
 ;
```

See also: key?

---

**key?** stack: ( -- pressed? )

code: 8D

A *defer* word returning *true* if a character has been typed on the keyboard since the last time that `key` or `expect` was executed. The keyboard character is not consumed.

Use `key?` to make simple, interruptible infinite loops:

```
begin ... key? until
```

The contents of the loop will repeat indefinitely until any key is pressed.

See also: key

---

**l!** stack: ( quad qaddr -- )

code: 73

The 32-bit value *quad* is stored at location *qaddr*. *qaddr* must be 32-bit aligned.

See also: rl!

---

**l,** stack: ( quad -- )

code: D2

Compile a 32-bit number into the dictionary. The dictionary pointer must be 2-byte-aligned.
For example:

```
\ to create an array containing integers 40004000 23 45 6734
create my-array 40004000 1, 23 l, 45 l, 6734 l,
```

**l@** stack: ( qaddr -- quad )
code: 6E

Fetch the 32-bit number stored at qaddr. qaddr must be 32-bit aligned.

See also: r1@

**/1** stack: ( -- n )
code: 5C

n is the number of address units to a 32-bit word, typically 4.

**/1** stack: ( nu1 -- nu2 )
code: 68

nu2 is the result of multiplying nu1 by /1. This is the portable way to convert an index into a byte offset.

**<l@** stack: ( qaddr -- n )
code: 02 42

Fetch quadlet from qaddr, sign-extended.

**1a+** stack: ( addr1 index -- addr2 )
code: 60

Increments addr1 by index times the value of /1. This is the portable way to increment an address.

**1a1+** stack: ( addr1 -- addr2 )
code: 64

Increments addr1 by the value of /1. This is the portable way to increment an address.
**lbflip**  stack: ( quad1 -- quad2 )  
code: 02 27  
Reverse the bytes in a 32-bit datum.

**lbflips**  stack: ( qaddr len -- )  
code: 02 28  
Reverse the bytes in each 32-bit datum in the given region.  
The region begins at qaddr and spans len bytes. The behavior is undefined if len is not a multiple of /1.

**lbsplit**  stack: ( quad -- byte1.lo byte2 byte3 byte4.hi )  
code: 7E  
Splits a 32-bit datum into four bytes. The high bytes of each stack result are all zeroes.

**lcc**  stack: ( char1 -- char2 )  
code: 82  
char2 is the lower case version of char1. If char1 is not an upper case letter, it is unchanged. For example:

```
ok ascii M lcc emit
m
ok
```

See also: upc

**leave**  stack: ( -- ) ( R: sys -- )  
code: 1B  
generates: b(leave)  
Transfers execution to just past the next loop or +loop. The loop is terminated and loop control parameters are discarded. May only be used in a do or ?do loop.
leave may appear in other control structures that are nested in the do loop structure. More than one leave may appear in a do loop.

For example:

```plaintext
: search-pat ( pat addr len -- found? )
  rot false swap 2swap ( false pat addr len )
  bounds ?do ( flag pat )
    i 0 over = if drop true swap leave then
  loop
  drop
;
```

See also: exit, unloop

---

?leave stack: ( exit? -- ) ( R: sys -- )
code: 14 + offset 1B B2
generates: if leave then

If exit? is true (nonzero), ?leave transfers control to just beyond the next loop or +loop. The loop is terminated and loop control parameters are discarded. If exit? is zero, no action is taken. May only be used in a do or ?do loop.

?leave may appear in other control structures that are nested in the do loop structure. More than one ?leave may appear in a do loop.

For example:

```plaintext
: show-mem ( vaddr -- ) \ display h# 10 bytes
  dup h# 9 u.r 5 spaces h# 10 bounds do i c@ 3 u.r loop
;
: .mem ( vaddr size -- )
  bounds ?do i show-mem key? ?leave h# 10 +loop
;
```
left-parse-string stack: ( str len char -- R-str R-len L-str L-len )
code: 02 40

Splits the input string at the first occurrence of delimiter char. For example:

```
" test;in;g" ascii ; left-parse-string
```

would leave the address and length of two strings on the stack:  

"in;g" and "test".

The delimiter character may be any ASCII character. Note that if the delimiter
is not found in the string, the effect is as if the delimiter was found at the very
end. For example:

```
" testing" ascii q left-parse-string
```

would leave on the stack "" and "testing".

line# stack: ( -- line# )
code: 01 52

A value, set and controlled by the terminal emulator, that contains the current
cursor line number. A value of 0 represents the topmost line of available text space — not
the topmost pixel of the frame buffer.

This word should be monitored as needed if your FCode Program is implementing
its own set of frame buffer primitives.

For example:

```
: set-line ( line -- ) 0 max #lines 1- min to line# ;
```

See also: window-top.
#line stack: ( -- a-addr )
  code: 94
  A variable containing the number of output lines since the last user interaction i.e. since the last ok prompt. #line is incremented whenever cr executes. The value in this variable is used to determine when to pause during long display output, such as dump. Its value is reset each time the ok prompt displays.

See also: exit?

linefeed stack: ( -- 0x0A )
  code: 10 00 00 00 0A
  generates: b(lit) 00 00 00 0x0A
  Leaves the ASCII code for the linefeed character on the stack.

#lines stack: ( -- rows )
  code: 01 50
  #lines is a value that is part of the display device interface. The terminal emulator package uses it to determine the height (number of rows of characters) of the text region that it manages. The fb1- and fb8- frame buffer support packages also use it for a similar purpose.

  The value of #lines must be set to the desired height of the text region. This can be done with to, or it can be handled automatically as one of the functions performed by fb1-install or fb8-install. The value set by fbx-install is the smaller of the passed #lines parameter and the screen-#rows NVRAM parameter.

  For example:

  : set-line ( line -- ) 0 max #lines 1- min to line# ;
**loop** stack: ( C: dodest-sys -- ) ( -- ) ( R: sys1 -- <nothing> | sys2 )

code: 15 -offset

generates: b(loop) -offset

Terminates a do or ?do loop. Increments the loop index by one. If the index was incremented across the boundary between limit-1 and limit, the loop is terminated and loop control parameters are discarded. When the loop is not terminated, execution continues just after the corresponding do or ?do.

For example, the following do loop:

```
8 0 do…loop
```

terminates when the loop index changes from 7 to 8. Thus, the loop will iterate with loop index values from 0 to 7, inclusive.

**loop** may be used either inside or outside of colon definitions.

**+loop** stack: ( C: dodest-sys -- ) ( n -- ) ( R: sys1 -- <nothing> | sys2 )

code: 16 -offset

generates: b(+loop) -offset

Terminates a do or ?do loop. Increments the loop index by n (or decrements the index if n is negative). If the index was incremented (or decremented) across the boundary between limit-1 and limit the loop is terminated and loop control parameters are discarded. When the loop is not terminated, execution continues just after the corresponding do or ?do.

The following do loop:

```
8 0 do…2 +loop
```

terminates when the loop index crosses the boundary between 7 and 8. Thus, the loop will iterate with loop index values of 0, 2, 4, 6.

By contrast, a do loop created as follows:

```
0 8 do…-2 +loop
```
terminates when the loop index crosses the boundary between -1 and 0. Thus, the loop will iterate with loop index values of 8, 6, 4, 2, 0.

`+loop` may be used either inside or outside of colon definitions.

---

**lpeek**

| stack: ( qaddr -- false | quad true ) |
|---|
| code: 02 22 |

Tries to read the 32-bit word at address `qaddr`. Returns `quad` and `true` if the access was successful. A `false` return indicates that a read access error occurred. `qaddr` must be 32-bit aligned.

---

**lpoke**

<table>
<thead>
<tr>
<th>stack: ( quad qaddr -- okay? )</th>
</tr>
</thead>
<tbody>
<tr>
<td>code: 02 25</td>
</tr>
</tbody>
</table>

Tries to write `quad` at address `qaddr`. Returns `true` if the access was successful. A `false` return indicates a write access error. `qaddr` must be 32-bit aligned.

**Note** – `lpoke` may be unreliable on bus adapters that “buffer” write accesses.

---

**lshift**

<table>
<thead>
<tr>
<th>stack: ( x1 u -- x2 )</th>
</tr>
</thead>
<tbody>
<tr>
<td>code: 27</td>
</tr>
</tbody>
</table>

Shifts `x1` left by `u` bit-places. Zero-fills the low bits.

---

**lwflip**

<table>
<thead>
<tr>
<th>stack: ( quad1 -- quad2 )</th>
</tr>
</thead>
<tbody>
<tr>
<td>code: 02 26</td>
</tr>
</tbody>
</table>

Swaps the doublets in a quadlet.

---

**lwflips**

<table>
<thead>
<tr>
<th>stack: ( qaddr len -- )</th>
</tr>
</thead>
<tbody>
<tr>
<td>code: 02 37</td>
</tr>
</tbody>
</table>

Swaps the order of the 16-bit words in each 32-bit word in the memory buffer `qaddr len`. `qaddr` must be four-byte-aligned. `len` must be a multiple of `/1`.
For example:

```
ok h# 12345678 8000 1!
ok 8000 4 lwflips
ok 8000 l@ .h
56781234
```

**lwsplit** stack: ( quad -- w1.lo w2.hi )
code: 7C

Splits the 32-bit value *quad* into two 16-bit words. The upper bytes of the two generated words are zeroes.

**lxjoin** stack: ( quad.lo quad.hi -- o )
code: 02 43

Joins two quadlets to form an octlet. Combines the 32 least-significant bits of each operand to form an octlet. Ignores the high-order bits of each operand.

**mac-address** stack: ( -- mac-str mac-len )
code: 01 A4

Usually used only by the "network" device type, this FCode returns the value for the Media Access Control, or MAC address, that this device should use for its own address. The data is encoded as a byte array, generally 6 bytes long.

The value returned by `mac-address` is system-dependent.

See also:"mac-address", "local-mac-address", and "network" in Chapter 5, “Properties” and Chapter 9, “Network Devices”.

**map-low** stack: ( phys.lo ... size -- virt )
code: 01 30

Creates a mapping associating the range of physical addresses beginning at `phys.lo` ... *my-space* and extending for `size` bytes in this device’s physical address space with a processor virtual address. Return that virtual address `virt`.
Equivalent to:

```
my-space swap " map-in" $call-parent
```

The number of cells in the list `phys.lo ...` is one less than the number determined by the value of the "#address-cells" property of the parent node.

If the requested operation cannot be performed, `throw` is called with an appropriate error message.

Out-of-memory conditions can be detected and handled with the phrase:

```
['] map-low catch
```

See also: `map-out`

---

**mask**

stack: ( -- a-addr )
code: 01 24

This variable defines which bits out of every 32-bit word will be tested by `memory-test-suite`. To test all 32-bits, set `mask` to all ones with:

```
ffff.ffff mask !
```

To test only the low-order byte out of each word, set the lower bits of `mask` with:

```
0000.00ff mask !
```

Any arbitrary combination of bits can be tested or masked.

---

**max**

stack: ( n1 n2 -- n1 | n2 )
code: 2F

Returns the greater of `n1` and `n2`. 
memory-test-suite stack: ( addr len -- fail? )
code: 01 22

Performs a series of tests on the memory beginning at \texttt{addr} for \texttt{len} bytes. If any of the tests fail, \texttt{failed?} is \texttt{true} and a failure message is displayed on a system-dependent diagnostic output device.

The actual tests performed are machine specific and often vary depending on whether \texttt{diagnostic-mode?} is \texttt{true} or \texttt{false}. Typically, if \texttt{diagnostic-mode?} is \texttt{true}, a message is sent to the console output device giving the name of each test.

The value stored in \texttt{mask} controls whether only some or all data lines are tested.

For example:

```
: test-result ( -- )
  frame-buffer-adr my-frame-size memory-test-suite ( failed? )
  encode-int " test-result" property

See also: diag-switch?
```

\textbf{min} stack: ( n1 n2 -- n1|n2 )
code: 2E

Returns the lesser of \texttt{n1} and \texttt{n2}.

\textbf{mod} stack: ( n1 n2 -- rem )
code: 22

\texttt{rem} is the remainder after dividing \texttt{n1} by the divisor \texttt{n2}. \texttt{rem} has the same sign as \texttt{n2} or is zero. An error condition results if the divisor is zero.
*/mod stack: ( n1 n2 n3 -- rem quot )
code: 30 20 31 2A

Calculates \( n1 \times n2 / n3 \) and returns the remainder and quotient. The inputs, outputs, and intermediate products are all 32-bit. \( rem \) has the same sign as \( n3 \) or is zero. An error condition results if the divisor is zero.

/mod stack: ( n1 n2 -- rem quot )
code: 2A

\( rem \) is the remainder and \( quot \) is the quotient of \( n1 \) divided by the divisor \( n2 \). \( rem \) has the same sign as \( n2 \) or is zero. An error condition results if the divisor is zero.

model stack: ( str len -- )
code: 01 19

This is a shorthand word for creating a "model" property. By convention, "model" identifies the model name/number for a SBus card, for manufacturing and field-service purposes. A sample usage would be:

```
" SUNW,501-1623-1" model
```

This is equivalent to:

```
" SUNW,501-1623-1" encode-string " model" property
```

The "model" property is useful to identify the specific piece of hardware (the SBus card), as opposed to the "name" property (since several different but functionally-equivalent cards would have the same "name" property, thus calling the same operating system device driver).

See also: property, "model" in Chapter 5, "Properties".
move stack: ( src_addr dest_addr len -- )
code: 78

len bytes starting at src_addr (through src_addr+len-1 inclusive) are moved to address dest_addr (through dest_addr+len-1 inclusive). If len is zero then nothing is moved.

The data are moved such that the len bytes left starting at address dest_addr are the same data as was originally starting at address src_addr. If src_addr > dest_addr then the first byte of src_addr is moved first, otherwise the last byte (src_addr+len-1) is moved first. Thus, moves between overlapping fields are properly handled.

move will perform 16-bit, 32-bit or possibly even 64-bit operations (for better performance) if the alignment of the operands permits. If your hardware requires explicit 8-bit or 16-bit accesses, you will probably wish to use an explicitly-coded do ... loop instead.

ms stack: ( n -- )
code: 01  26

Delays all execution for at least n milliseconds, by executing an empty delay loop for an appropriate number of iterations. The maximum allowable delay will vary from system to system, but is guaranteed to be valid for all values up to at least 1,000,000 (decimal). No other CPU activity takes place during delays invoked with ms, although generally this is not a problem for FCode drivers since there is nothing else to do in the meantime anyway. If this word is used excessively, noticeable delays could result.

For example:

: probe-loop-wait ( addr -- )
  \ wait h# 10 ms before doing another probe at the location
  begin dup l@ drop h# 10 ms key? until drop
  ;
**my-address** stack: ( -- phys.lo ... )  
code: 01  02  

Returns the low component(s) of the device’s probe address, suitable for use with the map-in method, and with reg and encode-phys. The returned number, along with my-space, encodes the address of location 0 of this device in a bus-specific format. The number of cells in the list phys.lo ... is one less than the number determined by the value of the "#address-cells" property of the parent node.

The OpenBoot PROM automatically sets my-address to the correct value before each slot is probed. Usually, this value is used to calculate the location(s) of the device registers, which are then saved as the property value of the "reg" property and later accessed with my-unit.

For example for a SBus device:

```fcode-version2
  " audio" encode-string " name" property
my-address my-space encode-phys          \ SBus Configuration Space
  0 encode-int encode+  0 encode-int encode+
  ...
  " reg" property
end0
```

**my-args** stack: ( -- arg-str arg-len )  
code: 02  02  

Returns the instance argument string arg-str arg-len that was passed to the current instance when it was created, if the argument string exists. Otherwise returns with a length of 0.

For example:

```ok " /obio:TEST-ARGS" open-dev my-args type
TEST-ARGS```
**my-parent** stack: ( -- ihandle )

code: 02 0A

Returns the *ihandle* of the instance that opened the current instance. For device driver packages, the relationships of parent/child instances mimic the parent/child relationships in the device tree.

For example for an SBus device:

```fcode
: show-parent ( -- )
  my-parent ihandle>phandle " name" rot
  get-package-property 0= if
  ." my-parent is " type cr
  then

;''`
```

**my-self** stack: ( -- ihandle )

code: 02 03

A *value* word that returns the current instance’s *ihandle*. If there is no current instance, the value returned is zero.

For example:

```fcode
: show-model-name ( -- )
  my-self ihandle>phandle ( phandle )
  " model" rot get-package-property 0= if ( val.addr,len )
  ." model name is " type cr
  else ( )
  ." model property is missing " cr
  then ( )

;''`
```

**my-space** stack: ( -- phys.hi )

code: 01 03

Returns the high component of the device’s probe address representing the device space that this card is plugged into. The meaning of the returned value is bus-specific.
For example for an SBus device:

```
fcode-version1
  " audio" encode-string " name" property
  my-address h# 130.0000 + my-space h# 8 reg
  ...
end0
```

See `my-address` for more details.

---

**my-unit**

```
stack: ( -- phys.lo ... phys.hi )
code: 02 0D
```

Returns the unit address `phys.lo ... phys.hi` of the current instance. The unit address is set when the instance is created, as follows:

- If the `node-name` used to locate the instance’s package contained an explicit `unit-address`, that is the instance’s unit address. This handles the case of a “wildcard” node with no associated "reg" property.
- Otherwise, if the device node associated with the package from which the instance was created contains a "reg" property, the first component of its "reg" property value is the instance’s unit address.
- Otherwise, the instance’s unit address is 0 0.

The number of cells in the list `phys.lo ... phys.hi` is determined by the value of the "#address-cells" property of the parent node.

---

**/n**

```
stack: ( -- n )
code: 5D
```

The number of address units in a cell.

---

**/n**

```
stack: ( nu1 -- nu2 )
code: 69
```

Synonym for `cells`.
**na+** stack: ( addr1 index -- addr2 )  
code: 61  

Increments *addr1* by *index* times the value of /n.

*na+* should be used in preference to *wa+* or *la+* when the intent is to address items that are the same size as items on the stack.

---

**na1+** stack: ( addr1 -- addr2 )  
code: 65  

Synonym for *cell+.*

---

**name** stack: ( adr len -- )  
code: 1 14 12 04 6E 61 6D 65 1 10  
generates: encode-string " name" property

A shorthand word for creating a “name” property, used to match a device node with the appropriate Solaris driver. The “name” declaration is required for booting with Solaris, and should be present in every FCode program. For example:

```
" SUNW,bison" name
```

is equivalent to:

```
" SUNW,bison" encode-string " name" property
```

See also property, device-name.

See “name” in Chapter 5, “Properties”.

---

**named-token** stack: ( -- ) ( F: /FCode-string FCode#/ -- )  
code: B6  

Creates a new, possibly-named FCode function. *named-token* should never be used directly in source code.
**negate** stack: ( n1 -- n2 )
code: 2C

n2 is the negation of n1. This is equivalent to 0 swap -.

**new-device** stack: ( -- )
code: 01 1F

Starts a new entry in the device tree. This word is used for creating multiple devices in a single FCode Program.

See also: finish-device

**new-token** stack: ( -- ) ( F: /FCode#/ -- )
code: B5

Creates a new unnamed FCode function. new-token should never be used directly in source code.

**next-property** stack: ( previous-str previous-len phandle -- false | name-str name-len true )
code: 02 3D

Returns the name of the property following previous-string of phandle.

name-string is a null-terminated string that is the name of the property following previous-string in the property list for device phandle. If previous-string is zero or points to a zero-length string, name-string is the name of phandle’s first property. If there are no more properties after previous-string or if previous-string is invalid (i.e. names a property which does not exist in phandle), name-string is a pointer to a zero-length string.

**nip** stack: ( x1 x2 -- x2 )
code: 4D

Removes the second item on the stack.
noop stack: ( -- )
code: 7B

Does nothing. This can be used to provide short delays or as a placeholder for patching in other commands later.

not stack: ( x1 -- x2 )
code: 26

Synonym for invert.

See also: 0=

$number stack: ( addr len -- true | n false )
code: A2

A numeric conversion primitive that converts a string to a number, according to the current base value. An error flag is returned if an inconvertible character is encountered.

For example:

```
ok hex
ok " 123f" $number .s
123f 0
ok " 123n" $number .s
ffffffff
```
of stack: ( C: case-sys1 -- case-sys2 of-sys ) ( sel of-val -- sel | <nothing> )
code: 1C +offset
generates: b(of) +offset

Begins the next test clause in a case statement. See case for more details.

off stack: ( a-addr -- )
code: 6B

Sets the contents at a-addr to false (i.e. zero).
offset16  stack: ( -- )
code: CC

Instructs the tokenizer program, and the boot PROM, to expect all further branch offsets to be 16-bit values. This word is automatically generated by some current tokenizers.

Once offset16 is executed, the offset size remains 16 bits for the duration of the FCode Program; it cannot be set back to 8 bits. Multiple calls of offset16 have no additional effect. offset16 is only useful in an FCode Program that begins with version1. All other starting tokens (start0, start1, start2, and start4) automatically set the offset size to 16 bits.

See also: fcode-version2

on  stack: ( a-addr -- )
code: 6A

Set the contents at a-addr to true (i.e. -1).

open-package  stack: ( arg-str arg-len phandle -- ihandle | 0 )
code: 02 05

Creates an instance of the package identified by phandle, saves in that instance an argument string specified by arg-str arg-len, and invokes the package's open method. The parent instance of the new instance is the instance that invoked open-package.

Returns the instance handle ihandle of the new instance if it can be opened. It returns 0 if the package could not be opened, either because that package has no open method or because its open method returned false indicating an error. In this case, the current instance is not changed.
For example:

```fcode
: test-tftp-open ( -- ok? )
    " obp-tftp" find-package if ( phandle )
    0 0 rot open-package if true else false then
else
    false
then
:
```

**$open-package**

stack: ( arg-str arg-len name-str name-len -- ihandle | 0 )
code: 02 0F

Similar to using find-package open-package except that if find-package fails, 0 is returned immediately, without calling open-package.

The name is interpreted relative to the /packages device node. For example, if name-str name-len represents the string "disk-label", the package in the device tree at "/packages/disk-label" will be located.

If there are multiple packages with the same name (in the /packages node), the most recently created one is opened.

For example:

```fcode
0 0 " obp-tftp" $open-package ( ihandle )
```

**or**

stack: ( x1 x2 -- x3 )
code: 24

x3 is the bit-by-bit inclusive-or of x1 with x2.

**#out**

stack: ( -- a-addr )
code: 93

A variable containing the current column number on the output device. This is updated by emit, cr and some other words that modify the cursor position. It is used for display formatting.
For example:

```fcode
: to-column ( column -- ) #out @ - 1 max spaces ;
```

---

**over** stack: ( x1 x2 -- x1 x2 x1 )
code: 48

The second stack item is copied to the top of the stack.

---

**2over** stack: ( x1 x2 x3 x4 -- x1 x2 x3 x4 x1 x2 )
code: 54

Copies the third and fourth stack items to the stack top.

---

**pack** stack: ( str len addr -- pstr )
code: 83

Stores the string specified by `str len` as a packed string at the location `addr` returning `pstr` (which is the same address as `addr`). The byte at address `pstr` is the length of the string and the string itself starts at address `pstr+1`. A packed string can contain at most 255 characters.

Packed strings are generally not used in FCode. Virtually all string operations are in the `addr len` format.

For example:

```fcode
h# 20 buffer: my-packed-string
" This is test string " my-packed-string pack
```

---

**parse-2int** stack: ( str len -- val.lo val.hi )
code: 01 1B

Converts a “`hi,lo`” string into a pair of values according to the current value in base.
If the string does not contain a comma, val.lo is zero and val.hi is the result of converting the entire string. If either component contains non-numeric characters, according to the value in base, the result is undefined.

For example:

```
ok " 4,ff001200" parse-2int .s
ff001200 4
ok " 4" parse-2int .s
0 4
```

**peer**

stack: ( phandle -- phandle.sibling )
code: 02 3C

peer returns the phandle phandle.sibling of the package that is the next child of the parent package phandle.

If there are no more siblings, peer returns 0.

If phandle is 0, peer returns phandle of the root node.

Together with child, peer lets you enumerate (possibly recursively) the children of a particular device. A common application would be for a device driver to use child to determine the phandle of a node’s first child, and use peer multiple times to determine the phandles of the node’s other children.

For example:

```
: my-children ( -- )
  my-self ihandle>phandle child ( first-child )
  begin ?dup while dup . peer repeat

;```
**pick** stack: ( xu ... x1 x0 u -- xu ... x1 x0 xu )
code: 4E

Copies the u-th stack value, not including u itself, where the remaining stack items have indices beginning with 0. u must be between 0 and two less than the total number of elements on the stack (including u).

0 pick is equivalent to dup     ( n1 -- n1 n1 )
1 pick is equivalent to over    ( n1 n2 -- n1 n2 n1 )
2 pick is equivalent to ( n1 n2 n3 -- n1 n2 n3 n1 )

For the sake of readability, the use of pick should be minimized.

**property** stack: ( prop-addr prop-len name-str name-len -- )
code: 01 10

Creates a new property with the specified name and previously prop-encoded value. If there is a current instance, creates the property in the package from which the current instance was created. Otherwise, if there is an active package, creates the property in the active package. If there is neither a current instance nor an active package, the result is undefined.

If a property with the specified name already exists in the active package in which the property would be created, replace its value with the new value.

Properties provide a mechanism for an FCode Program to pass information to an operating system device driver. A property consists of a property name string and a property value array. The name string gives the name of the property, and the value array gives the value associated with that name. For example, a frame buffer may wish to declare a property named "hres" (for horizontal resolution) with a value of 1152.

The property command requires two arrays on the stack — the value array and the name string. The name string is an ordinary Forth string, such as any string created with " . This string should be written in lower case, since the property name is stored only after converting uppercase letters, if any, to lower case. For example:

" A21-b" encode-string " New_version" property
is stored as if entered

```
" A21-b" encode-string " new_version" property
```

The value array, however, must be in the property value array format. See Chapter 5, “Properties” for more information on creating property value arrays.

All properties created by an FCode Program are stored in a “device tree” by OpenBoot. This tree can then be queried by an operating system device driver, using the Client Interface’s `getprop` or `nextprop` services.

The FCode Program and the operating system device driver may agree on any arbitrary set of names and values to be passed, with virtually no restrictions. Several property names, though, are reserved and have specific meanings. For many of them, a shorthand command also exists that makes the property declaration a bit simpler.

For example:

```
" SUNW,new-model" encode-string model
```

See also: "name", `device-name`, `model`, `reg` and Chapter 5, “Properties” for more information.

```
\texttt{\textasciitilde r\textasciitilde} stack: ( \textemdash x ) ( R: x \textemdash )
code: 31

Removes \textit{x} from the return stack and places it on the stack. See \texttt{\textasciitilde r\textasciitilde} for restrictions on the use of this word.

For example:

```
: copyout ( buf addr len -- len ) >r swap r@ move r> ;
```
\r stack: ( -- x ) ( R: x -- x )
code: 32

Places a copy of the top of the return stack on the stack.

For example:

\begin{verbatim}
: copyout ( buf addr len -- len ) >r swap r@ move r> ;
\end{verbatim}

See \texttt{\textgreater r} for more details.

\texttt{\textless r} stack: ( n size -- )
code: 9E

Converts \texttt{n} using the value of \texttt{base} and then displays it right-aligned in a field \texttt{size} digits wide. Displays a leading minus sign if \texttt{n} is negative. A trailing space is \textit{not} displayed.

If the number of digits required to display \texttt{n} is greater than \texttt{size}, displays all the digits required with no leading spaces in a field as wide as necessary.

For example:

\begin{verbatim}
: formatted-output ( -- )
  my-length h# 8 .r ." length" cr
  my-width h# 8 .r ." width" cr
  my-depth h# 8 .r ." depth" cr
 ;
\end{verbatim}

\texttt{\textgreater r} stack: ( x -- ) ( R: -- x )
code: 30

Removes \texttt{x} from the stack and places it on the top of the return stack.
The return stack is a second stack, occasionally useful as a place to temporarily place numeric parameters i.e. to “get them out of the way” for a little while. For example:

```
: encode-intr ( int-level vector -- )
  >r sbus-intr>cpu encode-int r> encode-int encode+
;  
```

However, since the return stack is also used by the system for transferring control from word to word (and by do loops), improper use of \texttt{>r} or \texttt{r>} is guaranteed to crash your program. Some restrictions that \textit{must} be observed are:

- All values placed on the return stack in a colon definition must be removed before the colon definition is exited by normal termination, \texttt{exit} or \texttt{throw}, or else the program will crash.
- No values from the return stack should be removed from in a colon definition unless they were placed there in that definition.
- Entering a \texttt{do} loop automatically places values onto the return stack. Therefore,
  - Values placed on the return stack before the loop was started will not be accessible from in the loop.
  - Values placed on the return stack in the loop must be removed before \texttt{loop}, +\texttt{loop}, or \texttt{leave} is encountered.
  - The loop indices \texttt{i} or \texttt{j} will no longer be valid when additional values have been placed on the return stack in the loop.

```
r! stack: ( byte addr -- )
code: 02 31
Stores an 8-bit byte to a device register at \texttt{addr} with identical bit ordering as the input stack item. Data is stored with a single access operation and flushes any intervening write buffers, so that the data reaches its final destination before the next FCode Function is executed.
```

For example:

```
: my-stat! ( byte -- ) my-stat r! ;
```
rb@ stack: ( addr -- byte )
code: 02 30
Fetches byte from the device register at addr. Data is read with a single access operation. The result has identical bit ordering as the original register data.
For example:

: my-stat@ ( -- byte ) my-stat rb@ ;

reg stack: ( phys.lo ... phys.hi size -- )
code: 01 16
This is a shorthand word for declaring the "reg" property. Typical usage for an SBus device:

my-address 40.0000 + my-space 20 reg

This declares that the device registers are located at offset 40.0000 through 40.001f in this slot. The following code would accomplish the same thing:

my-address 40.0000 + my-space encode-phys 20 encode-int encode+ " reg" property

Note that if you need to declare more than one block of register addresses, you must repeatedly use encode-phys, encode-int and encode+ to build the structure to be passed into the "reg" property.

For example, to declare two register fields at 10.0000-10.00ff and 20.0000-20.037f on a SBus device, use the following:

my-address 10.0000 + my-space encode-phys\ Offset#1 100 encode-int encode+\ Merge size#1 my-address 20.0000 + my-space encode-phys encode+\ Merge offset#2 380 encode-int encode+\ Merge size#2 " reg" property
Repeat

\[ \text{code: 13 \ -offset \ b2} \]

Generates: \( \text{bbranch \ -offset \ b(>resolve)} \)

Terminates a begin...while...repeat conditional loop. See while for more details.

Reset Screen

\[ \text{code: 01 \ 58} \]

Reset-screen is one of the defer words of the display device interface. The terminal emulator package executes reset-screen when it has processed a character sequence that calls for resetting the display device to its initial state. reset-screen puts the display device into a state in which display output is visible (e.g. enable video).

This word is initially empty, but must be loaded with an appropriate routine in order for the terminal emulator to function correctly. This can be done with to, or it can be loaded automatically with fb1-install or fb8-install (which loads fb1-reset-screen or fb8-reset-screen, respectively). These words are NOPs, so it is very common to first call fbx-install and then to override the default setting for reset-screen with:

```
['] my-video-on to reset-screen
```

See also: blink-screen

R1!

\[ \text{code: 02 \ 35} \]

Stores a 32-bit word to a device register at qaddr with identical bit ordering as the input stack item. qaddr must be 32-bit aligned. Data is stored with a single access operation and flushes any intervening write buffers, so that the data reaches its final destination before the next FCode Function is executed.
For example:

```plaintext
: my-reg! ( n -- ) my-reg rl! ;
```

**rl@**

Stack: ( qaddr -- quad )

Code: 02 34

Fetches a 32-bit word from the device register at qaddr. qaddr must be 32-bit aligned. Data is read with a single access operation. The result has identical bit ordering as the original register data.

For example:

```plaintext
: my-reg@ ( -- n ) my-reg rl@ ;
```

**roll**

Stack: ( xu ... x1 x0 u -- xu-1 ... x1 x0 xu )

Code: 4F

Removes the u-th stack value, not including u itself, where the remaining stack items have indices beginning with 0. The u-th stack item is then placed on the top of the stack, moving the remaining items down one position. u must be between 0 and two less than the total number of elements on the stack (including u).

0 roll is a null operation
1 roll is equivalent to swap ( n1 n2 -- n2 n1 )
2 roll is equivalent to rot ( n1 n2 n3 -- n2 n3 n1 )
3 roll is equivalent to ( n1 n2 n3 n4 -- n2 n3 n4 n1 )

For the sake of readability and performance, minimize your use of roll.

**rot**

Stack: ( x1 x2 x3 -- x2 x3 x1 )

Code: 4A

Rotates the top three stack entries, bringing the deepest to the top.
-rot  stack: ( x1 x2 x3 -- x3 x1 x2 )
  code: 4B
  Rotates the top three stack entries in the direction opposite from rot, putting
  the top number underneath the other two.

2rot  stack: ( x1 x2 x3 x4 x5 x6 -- x3 x4 x5 x6 x1 x2 )
  code: 56
  Rotates the top three pairs of numbers, bringing the third pair to the top of the
  stack.

rshift stack: ( x1 u -- x2 )
  code: 28
  Shifts x1 right by u bit-places while zero-filling the high bits.

rw!   stack: ( w waddr -- )
  code: 02 33
  Stores a 16-bit word to a device register at waddr.  waddr must be 16-bit aligned.
  Data is stored with a single access operation and flushes any intervening write
  buffers, so that the data reaches its final destination before the next FCode
  Function is executed.

  The register is stored with identical bit ordering as the input stack item.

  For example:

  : my-count! ( w -- ) my-count rw! ;

rw@   stack: ( waddr -- w )
  code: 02 32
  Fetches a 16-bit word from the device register at waddr.  waddr must be 16-bit
  aligned.  Data is read with a single access operation.  The result has identical bit
  ordering as the original register data.
For example:

```fcode
: my-count@ ( -- w ) my-count rw@ ;
```

**rx@**

stack: ( o addr -- o )
code: 02 2E

Fetches an octlet from device register at oaddr. Reads data with a single-access operation. The result has identical bit ordering as the original register data.

**rx!**

stack: ( o addr -- )
code: 02 2F

Stores an octlet to device register at oaddr.

Stores data with a single-access operation and flushes any intervening write buffers so that data reaches its final destination before the next “word” is executed. Stores the result with identical bit ordering as the input stack item.

**s"**

stack: ([text">] -- text-str text-len )
code: 12
generates: b(" ) len-byte xx-byte xx-byte ... xx-byte

Gather the immediately-following string delimited by " . Return the location of the string text-str text-len.

Since an implementation is only required to provide two temporary buffers, a program cannot depend on the system’s ability to simultaneously maintain more than two distinct interpreted strings. Compiled strings do not have this limitation, since they are not stored in the temporary buffers.

**s.**

stack: ( n -- )
code: 47 2D 96 9A 49 98 97 90 A9 8F
generates: () type space

Displays the absolute value of n in a free-field format according to the current value of base. Displays a trailing space and, if n is negative, a leading minus sign. Even if the base is hexadecimal, n will be printed in signed format.

See also: .
# stack: ( ud -- 0 0 )
code: C8

Converts the remaining digits in pictured numeric output.

.. stack: ( ... -- ... )
code: 9F

Displays the contents of the data stack (using .) according to base. The top of the stack appears on the right. The contents of the stack are unchanged.

For example:

```
ok 1 2 3 .s
1 2 3
ok . . .
3 2 1
```

**sbus-intr>cpu** stack: ( sbus-intr# -- cpu-intr# )
code: 01 31

Convert the SBus interrupt level (1-7) to the CPU interrupt level. The mapping performed will be system-dependent.

This word is included because of the possibility that, even on systems that nominally do not support SBus, SBus devices might be used via a bus-to-bus bridge.

**screen-height** stack: ( -- height )
code: 01 63

A value, containing the total height of the display (in pixels). It can also be interpreted as the number of “lines” of memory.

*screen-height* is an internal value used by the *fb1-* and *fb8-* frame buffer support packages. In particular, this value is used in *fbx-invert*, *fbx-erase-screen*, *fbx-blink-screen* and in calculating *window-top*. *fb1-install* and *fb8-install* set it to the value of their height argument.
This function is included for historical compatibility. There is little reason for an FCode Program to use it. In fact, “standard” FCode Programs are forbidden from altering its value directly.

**screen-width**

stack: ( -- width )
code: 01 64

A value, containing the number of pixels (in memory) between one screen location and the next location immediately below it. The latter definition takes precedence if there is a conflict (e.g. there are unused/invisible memory locations at the end of each line).

screen-width is an internal value used by the fb1- and fb8- frame buffer support packages. fb1-install and fb8-install set it to their width argument.

This function is included for historical compatibility. There is little reason for an FCode Program to use it. In fact, “standard” FCode Programs are forbidden from altering its value directly.

**set-args**

stack: ( arg-str arg-len unit-str unit-len -- )
code: 02 3F

Sets the address and arguments of a new device node.

unit-string is a text string representation of a physical address in the address space of the parent device. set-args translates unit-string to the equivalent numerical representation by executing the parent instance’s decode-unit method, and sets the current instance’s probe-address (i.e. the values returned by my-address and my-space) to that numerical representation.

set-args then copies the string arg-string to instance-specific storage, and arranges for my-args to return the address and length of that copy when executed from the current instance.

set-args is typically used just after new-device. new-device creates and selects a new device node, and set-args sets its probe-address and arguments. Subsequently, the device node’s properties and methods are created by interpreting an FCode Program with byte-load or by interpreting Forth source code.
The empty string is commonly used as the arguments for a new device node. For example:

```
0 0 " 3.0" set-args
```

---

**set-font**

```
stack: ( addr width height advance min-char #glyphs -- )
code: 01 6B
```

This routine declares the font table to be used for printing characters on the screen. This routine must be called if you wish to use any of the fb1- or fb8-utility routines or >font.

Normally, set-font is called just after default-font. default-font leaves the exact set of parameters needed by set-font on the stack. This approach allows your FCode Program to inspect and/or alter the default parameters if desired. See default-font for more information on these parameters.

---

**set-token**

```
stack: ( xt immediate? fcode# -- )
code: DB
```

Assigns the FCode number fcode# to the FCode function whose execution token is xt, with compilation behavior specified by immediate? as follows:

- If immediate? is zero, then the FCode Evaluator will execute the function’s execution semantics if it encounters that FCode number while in interpretation state, or append those execution semantics to the current definition if it encounters that FCode number while in compilation state.
- If immediate? is nonzero, the FCode Evaluator will execute the function’s FCode Evaluation semantics anytime it encounters that FCode number.

---

**sign**

```
stack: ( n -- )
code: 98
```

If n is negative, appends an ASCII “-” (minus sign) to the pictured numeric output string. Typically used between <# and #>. See (.) for a typical usage.
space stack: ( -- )
code: A9 8F
generates: bl emit

Displays a single ASCII space character.

spaces stack: ( cnt -- )
code: A5 2F A5 18 +offset A9 8F 15 -offset
generates: 0 max 0 ?do space loop

Displays cnt ASCII space characters. Nothing is displayed if cnt is zero.

span stack: ( -- a-addr )
code: 88

A variable containing the count of characters actually received and stored by the last execution of expect.

For example:

```
h# 10 buffer: my-name-buff
: hello ( -- )
  ." Enter Your First name " my-name-buff h# 10 expect
  ." Sun Microsystems Welcomes " my-name-buff span 0 ?do type cr
;```

start0 stack: ( -- )
code: F0

start0 may only be used as the first byte of an FCode Program. start0:

- Sets the spread value to 0 causing the FCode Evaluator to read successive bytes of the current FCode Program from the same address.
- Establishes the use of 16-bit branches.
- Reads an FCode header from the current FCode Program and either discards it or uses it to verify the integrity of the current FCode program in an implementation-dependent manner.

See also: fcode-version2, start1, start2, start4, version1
start1

stack: ( -- )
code: F1

start1 may only be used as the first byte of an FCode Program. start1:

• Sets the spread value to 1 causing the FCode Evaluator to read successive bytes of the current FCode Program from addresses one address unit apart.
• Establishes the use of 16-bit branches.
• Reads an FCode header from the current FCode Program and either discards it or uses it to verify the integrity of the current FCode program in an implementation-dependent manner.

See also: fcode-version2, start0, start2, start4, version1

start2

stack: ( -- )
code: F2

start2 may only be used as the first byte of an FCode Program. start2:

• Sets the spread value to 2 causing the FCode Evaluator to read successive bytes of the current FCode Program from addresses two address units apart.
• Establishes the use of 16-bit branches.
• Reads an FCode header from the current FCode Program and either discards it or uses it to verify the integrity of the current FCode program in an implementation-dependent manner.

See also: fcode-version2, start0, start1, start4, version1

start4

stack: ( -- )
code: F3

start4 may only be used as the first byte of an FCode Program. start4:

• Sets the spread value to 4 causing the FCode Evaluator to read successive bytes of the current FCode Program from addresses four address units apart.
• Establishes the use of 16-bit branches.
• Reads an FCode header from the current FCode Program and either discards it or uses it to verify the integrity of the current FCode program in an implementation-dependent manner.

See also: fcode-version2, start0, start1, start2, version1
**state** stack: ( -- a-addr )
code: DC

A variable containing *true* if the system is in compilation state.

**struct** stack: ( -- 0 )
code: A5
generates: 0

Initializes a `struct...field` structure by leaving a zero on the stack to define the initial offset. See `field` for details.

**suspend-fcode** stack: ( -- )
code: 02 15

Tells the FCode interpreter that the device identification properties for the active package have been declared, and that the interpreter may postpone interpreting the remainder of the package if it so chooses.

If the FCode interpreter postpones (suspends) interpretation, it saves the state of the interpretation process so that interpretation can continue later. Attempts to open a suspended package cause the FCode interpreter to resume and complete the interpretation of that package before executing the package’s `open` method.

For example:

```plaintext
version1
  " SUNW,my-name" name
  " SUNW,my-model" encode-string " model" property
  suspend-fcode
...
end0
```

This feature is intended to save memory space and reduce the system start-up time by preventing the compilation of FCode drivers that are not actually used.
**swap** stack: ( x1 x2 -- x2 x1 )
code: 49
Exchanges the top two stack items.

**2swap** stack: ( x1 x2 x3 x4 -- x3 x4 x1 x2 )
code: 55
Exchanges the top two pairs of stack items.

**then** stack: ( C: orig-sys -- ) ( -- )
code: B2
generates: b(>resolve)
Terminates an if...then or an if...else...then conditional structure. See *if* for more details.

**throw** stack: ( ... error-code -- ?? error-code | ... )
code: 02 18
Transfers control to the most recent dynamically enclosing error handling context, passing the indicated error code to that handler. *error-code* must be nonzero. If the value of *error-code* is zero, the zero is removed from the stack, but no other action is taken.

See *catch* for an example of use.

**to** stack: ( param [old-name< >] -- )
code: C3 old-FCode#
generates: b(to) old-FCode#
Changes the contents of a value or a defer word:

```plaintext
number to name  ( for a value )
xr to name      ( for a defer word )
```
**toggle-cursor**

stack: ( -- )
code: 01  59

toggle-cursor is one of the defer words of the display device interface. The terminal emulator package executes toggle-cursor when it is about to process a character sequence that might involve screen drawing activity, and executes it again after it has finished processing that sequence. The first execution removes the cursor from the screen so that any screen drawing will not interfere with the cursor, and the second execution restores the cursor, possibly at a new position, after the drawing activity related to that character sequence is finished. toggle-cursor is also called once during the terminal emulator initialization sequence.

If the text cursor is on, toggle-cursor turns it off. If the text cursor is off, toggle-cursor turns it on. (On a bitmapped display, a typical implementation of this function inverts the pixels of the character cell to the right of the current cursor position.)

toggle-cursor is initially empty, but must be loaded with an appropriate routine in order for the terminal emulator to function correctly. This can be done with to, or it can be loaded automatically with fb1-install or fb8-install (which load fb1-toggle-cursor or fb8-toggle-cursor, respectively).

If the display device hardware has internal state (for example color map settings) that might have been changed by external software without firmware’s knowledge, that hardware state should be re-established to the state that the firmware driver requires when the cursor is toggled to the “off” state (which indicates that firmware drawing operations are about to begin). This situation can occur, for example, when an operating system is using a display device, but that operating system uses firmware text output services from time to time, e.g. for critical warning messages.

See also: to, fb1-install, fb8-install.
tokenizer[  stack: ( -- )
    code: none

    This is a tokenizer command that ends FCode byte generation and begins
    interpretation of the following text as tokenizer commands (up to the closing
    tokenizer). A tokenizer[...tokenizer sequence may be used
    anywhere in an FCode Program, either in any definition or outside of
    definitions.

    One plausible use for tokenizer[ would be to generate debugging text
during the tokenizing process. (A cr flushes the text from the output buffer
immediately, which is useful if the tokenizer crashes.) For example:

    ...
    tokenizer[ .( step a) cr ]tokenizer
    ...
    tokenizer[ .( step b) cr ]tokenizer
    ...

    emit-byte can be used with tokenizer[ to output a desired byte of FCode.
    This would be useful, for example, if you wished to generate a new FCode
    command that the tokenizer did not understand. For example:

    ...
    tokenizer[  1 emit-byte  27 emit-byte ]tokenizer
    \ manually output finish-device fcode
    ...

]tokenizer  stack: ( -- )
    code: none

    Ends a tokenizer-only command sequence. See tokenizer[.

true  stack: ( -- true )
    code: A4
    generates: -1

    Leaves the value for the true flag (which is -1) on the stack.
**tuck** stack: ( x1 x2 -- x2 x1 x2 )
code: 4C

Copies the top stack item underneath the second stack item.

**type** stack: ( text-str text-len -- )
code: 90

A defer word that transfers text-len characters to the output beginning with the character at address text-str and continuing through text-len consecutive addresses. No action is taken if text-len is zero.

For example:

```cpp
h# 10 buffer: my-name-buff
: hello ( -- )
  ." Enter Your First name " my-name-buff h# 10 expect
  ." Sun Microsystems Welcomes " my-name-buff span @ type cr
;
```

The output will go either to a frame buffer or to a serial port depending on which is enabled.

**u#** stack: ( u1 -- u2 )
code: 99

The remainder of u1 divided by the value of base is converted to an ASCII character and appended to the output string with hold. u2 is the quotient and is maintained for further processing. Typically used between <# and #>.

**u#>** stack: ( u -- str len )
code: 97

Pictured numeric output conversion is ended dropping u. str is the address of the resulting output array. len is the number of characters in the output array. str and len together are suitable for type. See (.) and (u.) for typical usages.
**u#s**  stack: ( u1 -- u2 )
code: 9A

*u1* is converted, appending each resultant character into the pictured numeric output string until the quotient is zero (see: #). A single zero is added to the output string if *u1* was initially zero. Typically used between <# and #>. See (. ) and (u .) for typical usages.

This word is equivalent to calling # repeatedly until the number remaining is zero.

---

**u.**  stack: ( u -- )
code: 9B

Displays *u* as an unsigned number in a free-field format according to the value in base. A trailing space is also displayed.

For example:

```
ok hex -1 u.
ffffffff
```

---

**u<**  stack: ( u1 u2 -- unsigned-less? )
code: 40

Returns true if *u1* is less than *u2* where *u1* and *u2* are treated as unsigned integers.

---

**u<=**  stack: ( u1 u2 -- unsigned-less-or-equal? )
code: 3F

Returns true if *u1* is less than or equal to *u2* where *u1* and *u2* are treated as unsigned integers.

---

**u>**  stack: ( u1 u2 -- unsigned-greater? )
code: 3E

Returns true if *u1* is greater than *u2* where *u1* and *u2* are treated as unsigned integers.
u>eq

stack: ( u1 u2 -- unsigned-greater-or-equal? )
code: 41

Returns true if u1 is greater than or equal to u2 where u1 and u2 are treated as unsigned integers.

(u.)

stack: ( u -- addr len )
code: 96 9A 97
generates: <# u#s u#>

This is a numeric conversion primitive used to implement display words such as u.. It converts an unsigned number into a string according to the value in base.

For example:

\[
\begin{array}{l}
\text{ok hex d# -12 (u.) type} \\
\text{fffffff4}
\end{array}
\]

u2/

stack: ( x1 -- x2 )
code: 58

x2 is the result of x1 logically shifted right one bit. A zero is shifted into the vacated sign bit.

For example:

\[
\begin{array}{l}
\text{ok -2 u2/ .s} \\
\text{7fffffff}
\end{array}
\]

um*

stack: ( u1 u2 -- ud.prod )
code: D4

Multiplies two unsigned 32-bit numbers yielding an unsigned 64-bit product.
For example:

```
ok  hex 3 3 u*x .s
  9 0
ok  4 ffff.ffff u*x .s
     ffffffff 3
```

**um/mod** stack: ( ud u -- urem uquot )
code: D5

Divides an unsigned 64-bit number by an unsigned 32-bit number yielding an unsigned 32-bit remainder and quotient

**u/mod** stack: ( u1 u2 -- urem uquot )
code: 2B

*rem* is the remainder and *quot* is the quotient after dividing *u1* by *u2*. All values and arithmetic are unsigned. All values are 32-bit.

For example:

```
ok  -1 5 u/mod .s
  0 33333333
```

**unloop** stack: ( -- ) ( R: sys -- )
code: 89

Discards loop control parameters.

**until** stack: ( C: dest-sys -- ) ( done? -- )
code: 14 -offset
generates: b?branch -offset

Marks the end of a *begin...until* conditional loop. When *until* is encountered, *done?* is removed and tested. If *done?* is *true*, the loop is terminated and execution continues just after the *until*. If *done?* is *false*, execution jumps back to just after the corresponding *begin*. 
For example:

```lisp
: probe-loop ( addr -- )
  \ generate a tight 'scope loop until a key is pressed.
  begin dup 1@ drop key? until drop
; 
```

**upc** stack: ( char1 -- char2 )

code: 81

`char2` is the upper case version of `char1`. If `char1` is not a lower case letter, it is left unchanged.

For example:

```lisp
: continue? ( -- continue? )
  ." Want to Continue? Enter Y/N" key dup emit
  upc ascii Y =
; 
```

See also: lcc

**u.r** stack: ( u size -- )

code: 9C

`u` is converted according to the value of base and then displayed as an unsigned number right-aligned in a field size digits wide. A trailing space is not displayed.

If the number of digits required to display `u` is greater than `size`, all the digits are displayed with no leading spaces in a field as wide as necessary.

For example:

```lisp
: formatted-output ( -- )
  my-base  h# 8 u.r  ." base" cr 
  my-offset h# 8 u.r  ." offset" cr
; 
```
**user-abort**  
stack: ( ... -- ) ( R: ... -- )  
code: 02 19

Used in an alarm routine to signify that the user has typed an abort sequence. When alarm finishes, instead of returning to the program that was interrupted by the execution of alarm, it enters the OpenBoot command interpreter by calling abort.

For example:

```fcode
: test-dev-status ( -- error? ) ... ;
: my-checker ( -- ) test-dev-status if user-abort then ;
: install-abort ( -- ) ['] my-checker d# 10 alarm ;
```

**value**  
stack: (E: -- x ) ( x "new-name< >" -- )  
code: (header) B8  
generates: new-token|named-token|external-token b(value)

Creates and initializes a value with the name new-name. When later executed, new-name leaves its value on the stack. The value of new-name can be changed with to.

For example:

```
ok 123 value foo  foo .
123
ok 456 to foo  foo .
456
```

In FCode Source, value cannot appear inside a colon definition.

**variable**  
stack: (E: -- a-addr ) ( "new-name< >" -- )  
code: (header) B9  
generates: new-token|named-token|external-token b(variable)

Creates an uninitialized variable named new-name. When later executed, new-name leaves its address on the stack. The alignment of the returned address is system-dependent. The address holds a 32-bit value.
The value of new-name can be changed with ! and fetched with @.

For example:

```
ok variable foo 123 foo ! foo @ .
123
ok 456 foo ! foo ?
456
```

In FCode Source, value cannot appear inside a colon definition.

---

**version1**

stack: ( -- )
code: FD

version1 may only be used as the first byte of an FCode Program.

- Sets the spread value to 1 causing the FCode Evaluator to read successive bytes of the current FCode Program from successive addresses.
- Establishes the use of 8-bit branches.
- Reads an FCode header from the current FCode Program and either discards it or uses it to verify the integrity of the current FCode program in an implementation-dependent manner.

See also: fcode-version2, start0, start1, start2, start4

---

**versionx?**

stack: ( -- flag )
code: ??

A group of tokenizer macros to determine the FCode version of the system running the FCode interpreter. They include:

<table>
<thead>
<tr>
<th>Word</th>
<th>Generates</th>
</tr>
</thead>
<tbody>
<tr>
<td>version1?</td>
<td>version b(lit) 2000.0000 &lt;</td>
</tr>
<tr>
<td>version2?</td>
<td>version b(lit) 2000.0000 &gt;=</td>
</tr>
<tr>
<td></td>
<td>version b(lit) 3000.0000 &lt;</td>
</tr>
<tr>
<td>version2.0?</td>
<td>version b(lit) 2000.0000 =</td>
</tr>
</tbody>
</table>
Each returns true if the named version matches the system running the FCode interpreter.

**w!** ( n adr -- )
code: 74

The low-order 16-bits of n are stored at location adr (through adr+1). The higher byte is stored at adr; the lower byte is stored at adr+1. adr must be on a 16-bit boundary; it must be evenly divisible by 2.

**w,** ( n -- )
code: D1

Compile two bytes into the dictionary. The dictionary pointer must be two-byte-aligned.

See **c,** for limitations.

**w@** ( adr -- n )
code: 6F

Fetch the 16-bit number stored at adr (through adr+1). The higher byte is at adr; the lower byte is at adr+1. The remaining high bytes of n are set to zero. adr must be on a 16-bit boundary; it must be evenly divisible by 2.

**/w** ( -- n )
code: 5B

n is the size in bytes of a 16-bit word: 2.

**/w** stack: ( nu1 -- nu2 )
code: 67

nu2 is the result of multiplying nu1 by /w. This is the portable way to convert an index into a byte offset.
<w@ stack: ( waddr -- n )
code: 70

Fetches the 16-bit number stored at waddr and extends its sign into the upper bytes. waddr must be 16-bit-aligned.

For example:

ok 9123 8000 w! 8000 <w@ .h
ffff9123
ok 8000 w@ .h
9123

wa+ stack: ( addr1 index -- addr2 )
code: 5F

Increments addr1 by index times the value of /w. This is the portable way to increment an address.

wal+ stack: ( addr1 -- addr2 )
code: 63

Increments addr1 by the value of /w. This is the portable way to increment an address.

wbflip stack: ( w1 -- w2 )
code: 80

w2 is the result of exchanging the two low-order bytes of the number w1. The two upper bytes of w1 must be zero, or erroneous results will occur.

wbflips stack: ( waddr len -- )
code: 02 36

Swaps the order of the bytes in each 16-bit word in the memory buffer waddr len.

waddr must be 16-bit-aligned. len must be a multiple /w.
**wbsplit**  
stack: ( w -- b1.lo b2.hi )  
code: AF  

Splits the two lower bytes of \( w \) into two separate bytes (stored as the lower byte of each resulting item on the stack). The upper bytes of \( w \) must be zero.

**while**  
stack: ( C: dest-sys -- orig-sys dest-sys ) ( continue? -- )  
code: 14  +offset  
generates: b?branch +offset  

Tests the exit condition for a \( \text{begin...while...repeat} \) conditional loop. When the \( \text{while} \) is encountered, \( \text{continue?} \) is removed from the stack and tested. If \( \text{continue?} \) is \( \text{true} \), execution continues from just after the \( \text{while} \) through to the \( \text{repeat} \) which then jumps back to just after the \( \text{begin} \). If \( \text{continue?} \) is \( \text{false} \), the loop is exited by causing execution to jump ahead to just after the \( \text{repeat} \).

For example:

```plaintext
: probe-loop ( addr -- )  
\ generate a tight 'scope loop until a key is pressed.  
begin key? 0= while dup l@ drop repeat drop ;
```

**window-left**  
stack: ( -- border-width )  
code: 01  66  

A value, containing the offset (in pixels) of the left edge of the active text area from the left edge of the visible display. The “active text area” is where characters are actually printed. (There is generally a border of unused blank area surrounding it on all sides.) \( \text{window-left} \) contains the size of the left portion of the unused border.

The size of the right portion of the unused border is determined by the difference between \( \text{screen-width} \) and the sum of \( \text{window-left} \) plus the width of the active text area (\#columns times \text{char-width}).

This word is initially set to 0, but should always be set explicitly to an appropriate value if you wish to use \( \text{any fb1- or fb8- utility routines} \). This can be done with \( \text{to} \), or it can be set automatically by calling \( \text{fb1-install} \) or \( \text{fb8-install} \).
When set with `fbx-install`, a calculation is done to set `window-left` so that the available unused border area is split between the left border and the right border. (The calculated value for `window-left` is rounded down to the nearest multiple of 32, though. This allows all pixel-drawing to proceed more efficiently.) If you wish to use `fbx-install` but desire a different value for `window-left`, simply change it with `to` after calling `fbx-install`.

**window-top**  
stack: ( -- border-height )  
code: 01 65

A value, containing the offset (in pixels) of the top of the active text area from the top of the visible display. The “active text area” is where characters are actually printed. (There is generally a border of unused blank area surrounding it on all sides.) `window-top` contains the size of the top portion of the unused border.

The size of the bottom portion of the unused border is determined by the difference between `screen-height` and the sum of `window-top` plus the height of the active text area (`#lines times char-height`).

This word is initially set to 0, but should always be set explicitly to an appropriate value if you wish to use any `fb1-` or `fb8-` utility routines. This can be done with `to`, or it can be set automatically by calling `fb1-install` or `fb8-install`. When set with `fbx-install`, a calculation is done to set `window-top` so that the available unused border area is split between the top border and the bottom border. If you wish to use `fbx-install` but desire a different value for `window-top`, simply change it with `to` after calling `fbx-install`.

**within**  
stack: ( n min max -- min<=n<max? )  
code: 45

`min<=n<max?` is true if `n` is between `min` and `max`, inclusive of `min` and exclusive of `max`.

See also: `between`.
wljoin stack: ( w.lo w.hi -- quad )
code: 7D
Merges two 16-bit numbers into a 32-bit number. The high bytes of w.lo and w.hi must be zero.

wpeek stack: ( waddr -- false | w true )
code: 02 21
Tries to read the 16-bit word at address waddr. Returns w and true if the access was successful. A false return indicates that a read access error occurred. waddr must be 16-bit aligned.

wpoke stack: ( w waddr -- okay? )
code: 02 24
Tries to write the 16-bit word at address waddr. Returns true if the access was successful. A false return indicates that a write access error occurred. waddr must be 16-bit aligned.
Note: wpoke may be unreliable on bus adapters that buffer write accesses.

wxjoin stack: ( w.lo w.2 w.3 w.hi -- o )
code: 02 44
Joins 4 doublets to form an octlet. Combines the sixteen least-significant bits of each operand to form an octlet. Ignores the high-order bits of each operand.

x, stack: ( o -- )
code: 02 45
Compiles an octlet, o, into the dictionary (doubly-aligned).

x@ stack: ( oaddr -- o )
code: 02 46
Fetches and octlet from an octlet-aligned address.
\textbf{x!} stack: (o oaddr -- )  
code: 02 47  
Stores an octlet to an octlet-aligned address.

\textbf{/x} stack: ( -- n )  
code: 02 48  
The number of address units in an octlet, typically eight.

\textbf{xa+} stack: (addr1 index -- addr2)  
code: 02 4a  
.Increments addr1 by index times the value of /x.

\textbf{xa1+} stack: (addr1 -- addr2)  
code: 02 4b  
 Increments addr1 by the value of /x.

\textbf{xbflip} stack: (oct1 -- oct2)  
code: 02 4c  
Reverses the bytes in an octlet.

\textbf{xbflips} stack: (oaddr len --)  
code: 02 4d  
Reverses the bytes in each octlet in the given region. The region begins at oaddr and spans len bytes. The behavior is undefined if len is not a multiple of /x.

\textbf{xbsplit} stack: (o -- b.lo b.2 b.3 b.4 b.5 b.6 b.7 b.hi)  
code: 02 4e  
Splits an octlet into 8 bytes. The bits of greater significance than the eight least-significant bits of each of the eight resulting values are zero.
xlflip stack: ( oct1 -- oct2 )
code: 02 4f
Reverses the quadlets in an octlet. Does not reverse the bytes in each quadlet.

xlflips stack: ( oaddr len -- )
code: 02 50
Reverses the quadlets in each octlet in the given region. Does not reverse the bytes in each quadlet. The region begins at oaddr and spans len bytes.

Refer to previous margin note.

xlsplit stack: ( o -- quad.lo quad.hi )
code: 02 51
Splits an octlet into 2 quadlets. The more-significant-bits than the 32 least-significant bits of each of the two resulting values are zero.

xor stack: ( x1 x2 -- x3 )
code: 25
x3 is the bit-by-bit exclusive-or of x1 with x2.

xwflip stack: ( oct1 -- oct2 )
code: 02 52
Reverses doublets in an octlet. Does not reverse bytes in each doublet.

xwflips stack: ( oaddr len -- )
code: 02 53
Reverses doublets in each octlet in the given region. Does not reverse the bytes in each doublet. The region begins at oaddr and spans len bytes.

Refer to margin note previous page.

xwsplit stack: ( 0 -- w.lo w.2 w.3 w.hi )
code: 02 54
Splits an octlet into four doublets. The more-significant-bits than the 16 least-significant bits of each of the four resulting values are zero.
**FCode Reference**

**FCode Primitives**

This appendix contains three lists:

- FCodes sorted according to functional group
- FCodes sorted by byte value
- FCodes sorted alphabetically by name

**FCodes by Function**

The following tables describe FCodes currently supported by OpenBoot. Both the FCode token values and Forth names are included. A token value entry of `CR` indicates a tokenizer-generated sequence, while `-` indicates that no FCode is generated.

*Table A-1  Stack Manipulation*

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>51</td>
<td>depth</td>
<td>( -- u )</td>
<td>How many items on stack?</td>
</tr>
<tr>
<td>46</td>
<td>drop</td>
<td>( x -- )</td>
<td>Removes x from the stack</td>
</tr>
<tr>
<td>52</td>
<td>2drop</td>
<td>( x1 x2 -- )</td>
<td>Removes 2 items from stack</td>
</tr>
<tr>
<td>CR</td>
<td>3drop</td>
<td>( x1 x2 x3 -- )</td>
<td>Removes 3 items from stack</td>
</tr>
<tr>
<td>47</td>
<td>dup</td>
<td>( x -- x x )</td>
<td>Duplicates x</td>
</tr>
<tr>
<td>53</td>
<td>2dup</td>
<td>( x1 x2 -- x1 x2 x1 x2 )</td>
<td>Duplicates 2 stack items</td>
</tr>
</tbody>
</table>
Table A-1  Stack Manipulation (Continued)

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>CR</td>
<td>3dup</td>
<td>(x1 x2 x3 -- x1 x2 x3 x1 x2 x3)</td>
<td>Copies top 3 stack items</td>
</tr>
<tr>
<td>50</td>
<td>?dup</td>
<td>(x -- 0</td>
<td>x x)</td>
</tr>
<tr>
<td>4D</td>
<td>nip</td>
<td>(x1 x2 -- x2)</td>
<td>Discards the second stack item</td>
</tr>
<tr>
<td>48</td>
<td>over</td>
<td>(x1 x2 -- x1 x2 x1)</td>
<td>Copies second stack item to top of stack</td>
</tr>
<tr>
<td>54</td>
<td>2over</td>
<td>(x1 x2 x3 x4 -- x1 x2 x3 x4 x1 x2)</td>
<td>Copies 2 stack items</td>
</tr>
<tr>
<td>4E</td>
<td>pick</td>
<td>(xu ... x1 x0 u -- xu ... x1 x0 xu)</td>
<td>Copies u-th stack item</td>
</tr>
<tr>
<td>30</td>
<td>&gt;r</td>
<td>(x --) (R: -- x)</td>
<td>Moves a stack item to the return stack</td>
</tr>
<tr>
<td>31</td>
<td>r&gt;</td>
<td>(--) (R: x --)</td>
<td>Moves item from return stack to data stack</td>
</tr>
<tr>
<td>32</td>
<td>r@</td>
<td>(--) (R: x -- x)</td>
<td>Copies the top of the return stack to the data stack</td>
</tr>
<tr>
<td>4F</td>
<td>roll</td>
<td>(xu ... x1 x0 u -- xu-1 ... x1 x0 xu)</td>
<td>Rotates u stack items</td>
</tr>
<tr>
<td>4A</td>
<td>rot</td>
<td>(x1 x2 x3 -- x2 x3 x1)</td>
<td>Rotates 3 stack items</td>
</tr>
<tr>
<td>4B</td>
<td>-rot</td>
<td>(x1 x2 x3 -- x3 x1 x2)</td>
<td>Shuffles top 3 stack items</td>
</tr>
<tr>
<td>56</td>
<td>2rot</td>
<td>(x1 x2 x3 x4 x5 x6 -- x3 x4 x5 x6 x1 x2)</td>
<td>Rotates 3 pairs of stack items</td>
</tr>
<tr>
<td>49</td>
<td>swap</td>
<td>(x1 x2 -- x2 x1)</td>
<td>Exchanges the top 2 stack items</td>
</tr>
<tr>
<td>55</td>
<td>2swap</td>
<td>(x1 x2 x3 x4 -- x3 x4 x1 x2)</td>
<td>Exchanges 2 pairs of stack items</td>
</tr>
<tr>
<td>4C</td>
<td>tuck</td>
<td>(x1 x2 -- x2 x1 x2)</td>
<td>Copies the top stack item below the second item</td>
</tr>
</tbody>
</table>

Table A-2  Arithmetic Operations

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>20</td>
<td>*</td>
<td>(nu1 nu2 -- prod)</td>
<td>Multiplies nu1 times nu2</td>
</tr>
<tr>
<td>1E</td>
<td>+</td>
<td>(nu1 nu2 -- sum)</td>
<td>Adds nu1+nu2</td>
</tr>
<tr>
<td>1F</td>
<td>-</td>
<td>(nu1 nu2 -- diff)</td>
<td>Subtracts nu1-nu2</td>
</tr>
<tr>
<td>21</td>
<td>/</td>
<td>(n1 n2 -- quot)</td>
<td>Divides n1/n2</td>
</tr>
<tr>
<td>CR</td>
<td>1+</td>
<td>(nu1 -- nu2)</td>
<td>Adds one</td>
</tr>
<tr>
<td>CR</td>
<td>1-</td>
<td>(nu1 -- nu2)</td>
<td>Subtracts one</td>
</tr>
<tr>
<td>CR</td>
<td>2+</td>
<td>(nu1 -- nu2)</td>
<td>Adds 2</td>
</tr>
<tr>
<td>CR</td>
<td>2-</td>
<td>(nu1 -- nu2)</td>
<td>Subtracts 2</td>
</tr>
<tr>
<td>59</td>
<td>2*</td>
<td>(x1 -- x2)</td>
<td>Multiplies by 2</td>
</tr>
<tr>
<td>57</td>
<td>2/</td>
<td>(x1 -- x2)</td>
<td>Divides by 2</td>
</tr>
<tr>
<td>27</td>
<td>1shift</td>
<td>(x1 u -- x2)</td>
<td>Left shifts x1 by u places</td>
</tr>
</tbody>
</table>
### Table A-2  Arithmetic Operations (Continued)

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>28</td>
<td>rshift</td>
<td>(x1 u -- x2)</td>
<td>Right shifts x1 by u places</td>
</tr>
<tr>
<td>CR</td>
<td>&lt;&lt;a</td>
<td>(n1 u -- n2)</td>
<td>Arithmetic left shifts (same as lshift)</td>
</tr>
<tr>
<td>29</td>
<td>&gt;&gt;a</td>
<td>(x1 u -- x2)</td>
<td>Arithmetic right shifts n1 by u places</td>
</tr>
<tr>
<td>2D</td>
<td>abs</td>
<td>(n -- u)</td>
<td>Absolute value</td>
</tr>
<tr>
<td>AE</td>
<td>aligned</td>
<td>(n1 -- n1</td>
<td>a-addr)</td>
</tr>
<tr>
<td>23</td>
<td>and</td>
<td>(x1 x2 -- x3)</td>
<td>Logical and</td>
</tr>
<tr>
<td>AC</td>
<td>bounds</td>
<td>(n cnt -- n+cnt n)</td>
<td>Converts start, len to end, start for do loop</td>
</tr>
<tr>
<td>2F</td>
<td>max</td>
<td>(n1 n2 -- n1</td>
<td>n2)</td>
</tr>
<tr>
<td>2E</td>
<td>min</td>
<td>(n1 n2 -- n1</td>
<td>n2)</td>
</tr>
<tr>
<td>22</td>
<td>mod</td>
<td>(n1 n2 -- rem)</td>
<td>Remainder of n1/n2</td>
</tr>
<tr>
<td>CR</td>
<td>*/mod</td>
<td>(n1 n2 n3 -- rem quot)</td>
<td>Remainder, quotient of n1*n2/n3</td>
</tr>
<tr>
<td>2A</td>
<td>/mod</td>
<td>(n1 n2 -- rem quot)</td>
<td>Remainder, quotient of n1/n2</td>
</tr>
<tr>
<td>2C</td>
<td>negate</td>
<td>(n1 -- n2)</td>
<td>Changes the sign of n1</td>
</tr>
<tr>
<td>26</td>
<td>invert</td>
<td>(x1 -- x2)</td>
<td>One’s complement</td>
</tr>
<tr>
<td>CR</td>
<td>not</td>
<td>(x1 -- x2)</td>
<td>Synonym for invert</td>
</tr>
<tr>
<td>24</td>
<td>or</td>
<td>(x1 x2 -- x3)</td>
<td>Logical or</td>
</tr>
<tr>
<td>2B</td>
<td>u/mod</td>
<td>(u1 u2 -- urem uquot)</td>
<td>Unsigned 32-bit divide of u1/u2</td>
</tr>
<tr>
<td>58</td>
<td>u2/</td>
<td>(x1 -- x2)</td>
<td>Logical right shifts 1 bit</td>
</tr>
<tr>
<td>25</td>
<td>xor</td>
<td>(x1 x2 -- x3)</td>
<td>Exclusive or</td>
</tr>
<tr>
<td>D4</td>
<td>um*</td>
<td>(u1 u2 -- ud.prod)</td>
<td>Multiplies two unsigned quadlets, yields an unsigned 64-bit product</td>
</tr>
<tr>
<td>D5</td>
<td>um/mod</td>
<td>(ud u -- urem uquot)</td>
<td>Divides an unsigned 64-bit number by an unsigned quadlet, yields a 32-bit remainder and quotient</td>
</tr>
<tr>
<td>D8</td>
<td>d+</td>
<td>(d1 d2 --d.sum)</td>
<td>Adds two 64-bit numbers</td>
</tr>
<tr>
<td>D9</td>
<td>d−</td>
<td>(d1 d2 -- d.diff)</td>
<td>Subtracts two 64-bit numbers</td>
</tr>
</tbody>
</table>
### Table A-3  Memory Operations

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>72</td>
<td>!</td>
<td>(x a-addr --)</td>
<td>Stores a number into the variable at a-addr</td>
</tr>
<tr>
<td>6C</td>
<td>+!</td>
<td>(nu a-addr --)</td>
<td>Adds nu to the number stored in the variable at a-addr</td>
</tr>
<tr>
<td>77</td>
<td>2!</td>
<td>(x1 x2 a-addr --)</td>
<td>Stores 2 numbers at a-addr; x2 at lower address</td>
</tr>
<tr>
<td>76</td>
<td>2@</td>
<td>(a-addr -- x1 x2)</td>
<td>Fetches 2 numbers from a-addr; x2 from lower address</td>
</tr>
<tr>
<td>6D</td>
<td>@</td>
<td>(a-addr -- x)</td>
<td>Fetches a number from the variable at a-addr</td>
</tr>
<tr>
<td>CR</td>
<td>?</td>
<td>(a-addr --)</td>
<td>Displays the number at a-addr</td>
</tr>
<tr>
<td>75</td>
<td>c!</td>
<td>(byte addr --)</td>
<td>Stores low byte of n at addr</td>
</tr>
<tr>
<td>71</td>
<td>c@</td>
<td>(addr -- byte)</td>
<td>Fetches a byte from addr</td>
</tr>
<tr>
<td>CR</td>
<td>blank</td>
<td>(addr len --)</td>
<td>Sets len bytes of memory to ASCII space, starting at addr</td>
</tr>
<tr>
<td>7A</td>
<td>comp</td>
<td>(addr1 addr2 len -- n)</td>
<td>Compares two byte arrays including case. n=0 if same</td>
</tr>
<tr>
<td>CR</td>
<td>erase</td>
<td>(addr len --)</td>
<td>Sets len bytes of memory to zero, starting at addr</td>
</tr>
<tr>
<td>79</td>
<td>fill</td>
<td>(addr len byte --)</td>
<td>Sets len bytes of memory to value byte</td>
</tr>
<tr>
<td>0228</td>
<td>lbflips</td>
<td>(qaddr len --)</td>
<td>Reverses bytes within each quadlet in given region</td>
</tr>
<tr>
<td>0237</td>
<td>lwflips</td>
<td>(qaddr len --)</td>
<td>Exchanges doublets within quadlets in qaddr len</td>
</tr>
<tr>
<td>73</td>
<td>l!</td>
<td>(quad qaddr --)</td>
<td>Stores the quadlet at qaddr, must be 32-bit aligned</td>
</tr>
<tr>
<td>6E</td>
<td>l@</td>
<td>(qaddr -- quad)</td>
<td>Fetches the quadlet at qaddr, must be 32-bit aligned</td>
</tr>
<tr>
<td>78</td>
<td>move</td>
<td>(src-addr dest-addr len --)</td>
<td>Copies len bytes from src-addr to dest-addr, handles overlap correctly</td>
</tr>
<tr>
<td>6B</td>
<td>off</td>
<td>(a-addr --)</td>
<td>Stores false (32-bit 0) at a-addr</td>
</tr>
<tr>
<td>6A</td>
<td>on</td>
<td>(a-addr --)</td>
<td>Stores true (32-bit -1) at a-addr</td>
</tr>
<tr>
<td>0236</td>
<td>wbflips</td>
<td>(waddr len --)</td>
<td>Exchanges bytes within doublets in the specified region</td>
</tr>
<tr>
<td>74</td>
<td>w!</td>
<td>(w waddr --)</td>
<td>Stores a doublet at waddr, must be 16-bit aligned</td>
</tr>
<tr>
<td>6F</td>
<td>w@</td>
<td>(waddr w)</td>
<td>Fetches the unsigned doublet at waddr, must be 16-bit aligned</td>
</tr>
<tr>
<td>70</td>
<td>&lt;w@</td>
<td>(waddr -- n)</td>
<td>Fetches the signed doublet at waddr, must be 16-bit aligned</td>
</tr>
</tbody>
</table>
### Table A-4  Atomic Access

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0230</td>
<td>rb@</td>
<td>( addr -- byte )</td>
<td>Reads the 8-bit value at the given address, atomically</td>
</tr>
<tr>
<td>0231</td>
<td>rb!</td>
<td>( byte addr -- )</td>
<td>Writes the 8-bit value at the given address, atomically</td>
</tr>
<tr>
<td>0232</td>
<td>rw@</td>
<td>( waddr -- w )</td>
<td>Reads the doublet at the given address, atomically</td>
</tr>
<tr>
<td>0233</td>
<td>rw!</td>
<td>( w waddr -- )</td>
<td>Writes the doublet at the given address, atomically</td>
</tr>
<tr>
<td>0234</td>
<td>rl@</td>
<td>( qaddr -- quad )</td>
<td>Reads the quadlet at the given address, atomically</td>
</tr>
<tr>
<td>0235</td>
<td>rl!</td>
<td>( quad qaddr -- )</td>
<td>Writes the quadlet at the given address, atomically</td>
</tr>
</tbody>
</table>

### Table A-5  Data Exception Tests

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0220</td>
<td>cpeek</td>
<td>( addr -- false</td>
<td>byte true )</td>
</tr>
<tr>
<td>0221</td>
<td>wpeek</td>
<td>( waddr -- false</td>
<td>w true )</td>
</tr>
<tr>
<td>0222</td>
<td>lpeek</td>
<td>( qaddr -- false</td>
<td>quad true )</td>
</tr>
<tr>
<td>0223</td>
<td>cpoke</td>
<td>( byte addr -- okay? )</td>
<td>Writes 8-bit value at given address, returns false if unsuccessful</td>
</tr>
<tr>
<td>0224</td>
<td>wpoke</td>
<td>( w waddr -- okay? )</td>
<td>Writes doublet at given address, returns false if unsuccessful</td>
</tr>
<tr>
<td>0225</td>
<td>lpoke</td>
<td>( quad qaddr -- okay? )</td>
<td>Writes quadlet at given address, returns false if unsuccessful</td>
</tr>
</tbody>
</table>

### Table A-6  Comparison Operations

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>36</td>
<td>0&lt;</td>
<td>( n -- less-than-0? )</td>
<td>True if n &lt; 0</td>
</tr>
<tr>
<td>37</td>
<td>0&lt;=</td>
<td>( n -- less-or-equal-to-0? )</td>
<td>True if n &lt;= 0</td>
</tr>
<tr>
<td>35</td>
<td>0&lt;&gt;</td>
<td>( n -- not-equal-to-0? )</td>
<td>True if n &lt;&gt; 0</td>
</tr>
<tr>
<td>34</td>
<td>0=</td>
<td>( nulflag -- equal-to-0? )</td>
<td>True if n = 0, also inverts any flag</td>
</tr>
<tr>
<td>38</td>
<td>0&gt;</td>
<td>( n -- greater-than-0? )</td>
<td>True if n &gt; 0</td>
</tr>
<tr>
<td>39</td>
<td>0&gt;=</td>
<td>( n -- greater-or-equal-to-0? )</td>
<td>True if n &gt;= 0</td>
</tr>
<tr>
<td>3A</td>
<td>&lt;</td>
<td>( n1 n2 -- less? )</td>
<td>True if n1 &lt; n2</td>
</tr>
<tr>
<td>43</td>
<td>&lt;=</td>
<td>( n1 n2 -- less-or-equal? )</td>
<td>True if n1 &lt;= n2</td>
</tr>
<tr>
<td>3D</td>
<td>&lt;&gt;</td>
<td>( x1 x2 -- not-equal? )</td>
<td>True if x1 &lt;&gt; x2</td>
</tr>
</tbody>
</table>
Table A-6  Comparison Operations (Continued)

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>3C</td>
<td>(=)</td>
<td>( x1 x2 -- equal? )</td>
<td>True if x1 = x2</td>
</tr>
<tr>
<td>3B</td>
<td>(&gt;)</td>
<td>( n1 n2 -- greater? )</td>
<td>True if n1 &gt; n2</td>
</tr>
<tr>
<td>42</td>
<td>(&gt;=)</td>
<td>( n1 n2 -- greater-or-equal? )</td>
<td>True if n1 &gt;= n2</td>
</tr>
<tr>
<td>44</td>
<td>between</td>
<td>( n min max -- min&lt;=n&lt;=max? )</td>
<td>True if min &lt;= n &lt;= max</td>
</tr>
<tr>
<td>CR</td>
<td>false</td>
<td>( -- false )</td>
<td>The value false</td>
</tr>
<tr>
<td>CR</td>
<td>true</td>
<td>( -- true )</td>
<td>The value true</td>
</tr>
<tr>
<td>40</td>
<td>u&lt;</td>
<td>( u1 u2 -- unsigned-less? )</td>
<td>True if u1 &lt; u2, unsigned</td>
</tr>
<tr>
<td>3F</td>
<td>u&lt;=</td>
<td>( u1 u2 -- unsigned-less-or-equal? )</td>
<td>True if u1 &lt;= u2, unsigned</td>
</tr>
<tr>
<td>3E</td>
<td>u&gt;</td>
<td>( u1 u2 -- unsigned-greater? )</td>
<td>True if u1 &gt; u2, unsigned</td>
</tr>
<tr>
<td>41</td>
<td>u&gt;=</td>
<td>( u1 u2 -- unsigned-greater-or-equal? )</td>
<td>True if u1 &gt;= u2, unsigned</td>
</tr>
<tr>
<td>45</td>
<td>within</td>
<td>( n min max -- min&lt;=n&lt;max? )</td>
<td>True if min &lt;= n &lt; max</td>
</tr>
</tbody>
</table>

Table A-7  Text Input

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>-</td>
<td>{</td>
<td>([text&lt;]&gt; --)</td>
<td>Begins a comment (ignored)</td>
</tr>
<tr>
<td>-</td>
<td>\</td>
<td>( -- )</td>
<td>Ignore rest of line (comment)</td>
</tr>
<tr>
<td>CR</td>
<td>ascii</td>
<td>([text&lt; &gt;] -- char)</td>
<td>ASCII value of next character</td>
</tr>
<tr>
<td>CR</td>
<td>control</td>
<td>([text&lt; &gt;] -- char)</td>
<td>Interprets next character as ASCII control character</td>
</tr>
<tr>
<td>8E</td>
<td>key</td>
<td>( -- char )</td>
<td>Reads a character from the keyboard</td>
</tr>
<tr>
<td>8D</td>
<td>key?</td>
<td>( -- pressed? )</td>
<td>True if a key has been typed on the keyboard</td>
</tr>
<tr>
<td>CR</td>
<td>accept</td>
<td>( addr len1 -- len2 )</td>
<td>Gets an edited input line, stores it at addr</td>
</tr>
<tr>
<td>8A</td>
<td>expect</td>
<td>( addr len -- )</td>
<td>Gets a line of edited input from the keyboard; stores it at addr</td>
</tr>
<tr>
<td>88</td>
<td>span</td>
<td>( -- a-addr )</td>
<td>Variable containing the number of characters read by expect</td>
</tr>
</tbody>
</table>
### Table A-8  ASCII Constants

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>A8</td>
<td>bell</td>
<td>( -- 0x07 )</td>
<td>The ASCII code for the bell character; decimal 7</td>
</tr>
<tr>
<td>A9</td>
<td>bl</td>
<td>( -- 0x20 )</td>
<td>The ASCII code for the space character; decimal 32</td>
</tr>
<tr>
<td>AA</td>
<td>bs</td>
<td>( -- 0x08 )</td>
<td>The ASCII code for the backspace character; decimal 8</td>
</tr>
<tr>
<td>CR</td>
<td>carret</td>
<td>( -- 0x0D )</td>
<td>The ASCII code for the carriage return character; decimal 13</td>
</tr>
<tr>
<td>CR</td>
<td>linefeed</td>
<td>( -- 0x0A )</td>
<td>The ASCII code for the linefeed character; decimal 10</td>
</tr>
<tr>
<td>CR</td>
<td>newline</td>
<td>( -- n )</td>
<td>The ASCII code for the newline character; decimal 10</td>
</tr>
</tbody>
</table>

### Table A-9  Numeric Input

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>A4</td>
<td>-1</td>
<td>( -- -1 )</td>
<td>Constant -1</td>
</tr>
<tr>
<td>A5</td>
<td>0</td>
<td>( -- 0 )</td>
<td>Constant 0</td>
</tr>
<tr>
<td>A6</td>
<td>1</td>
<td>( -- 1 )</td>
<td>Constant 1</td>
</tr>
<tr>
<td>A7</td>
<td>2</td>
<td>( -- 2 )</td>
<td>Constant 2</td>
</tr>
<tr>
<td>A8</td>
<td>3</td>
<td>( -- 3 )</td>
<td>Constant 3</td>
</tr>
<tr>
<td>CR</td>
<td>d#</td>
<td>( [number&lt; &gt;] -- n )</td>
<td>Interprets next number in decimal</td>
</tr>
<tr>
<td>-</td>
<td>decimal</td>
<td>( -- )</td>
<td>If outside definition, input text in decimal</td>
</tr>
<tr>
<td>CR</td>
<td>h#</td>
<td>( [number&lt; &gt;] -- n )</td>
<td>Interprets next number in hexadecimal</td>
</tr>
<tr>
<td>-</td>
<td>hex</td>
<td>( -- )</td>
<td>If outside definition, input text in hexadecimal</td>
</tr>
<tr>
<td>CR</td>
<td>o#</td>
<td>( [number&lt; &gt;] -- n )</td>
<td>Interprets next number in octal</td>
</tr>
<tr>
<td>-</td>
<td>octal</td>
<td>( -- )</td>
<td>If outside definition, input text in octal</td>
</tr>
</tbody>
</table>

### Table A-10  Numeric Primitives

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>99</td>
<td>u#</td>
<td>( u1 -- u2 )</td>
<td>Converts a digit in pictured numeric output</td>
</tr>
<tr>
<td>97</td>
<td>u#&gt;</td>
<td>( u -- str len )</td>
<td>Ends pictured numeric output</td>
</tr>
<tr>
<td>96</td>
<td>&lt;#</td>
<td>( -- )</td>
<td>Initializes pictured numeric output</td>
</tr>
<tr>
<td>C7</td>
<td>#</td>
<td>( ud1 -- ud2 )</td>
<td>Converts a digit in pictured numeric output conversion</td>
</tr>
</tbody>
</table>
Table A-10 Numeric Primitives (Continued)

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>C9</td>
<td><code>#</code></td>
<td>(ud -- str len)</td>
<td>Ends pictured numeric output conversion</td>
</tr>
<tr>
<td>A0</td>
<td>base</td>
<td>(-- a-addr)</td>
<td>Variable containing number base</td>
</tr>
<tr>
<td>A3</td>
<td>digit</td>
<td>(char base -- digit true</td>
<td>char false)</td>
</tr>
<tr>
<td>95</td>
<td>hold</td>
<td>(char --)</td>
<td>Inserts the char in the pictured numeric output string</td>
</tr>
<tr>
<td>C8</td>
<td><code>#s</code></td>
<td>(ud -- 0 0)</td>
<td>Converts remaining digits in pictured numeric output</td>
</tr>
<tr>
<td>9A</td>
<td>u#s</td>
<td>(u1 -- u2)</td>
<td>Converts rest of the digits in pictured numeric output</td>
</tr>
<tr>
<td>98</td>
<td>sign</td>
<td>(n --)</td>
<td>Sets sign of pictured output</td>
</tr>
<tr>
<td>A2</td>
<td>$number</td>
<td>(addr len -- true</td>
<td>n false)</td>
</tr>
</tbody>
</table>

Table A-11 Numeric Output

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>9D</td>
<td>.</td>
<td>(nu --)</td>
<td>Displays a number</td>
</tr>
<tr>
<td>CR</td>
<td>.d</td>
<td>(n --)</td>
<td>Displays number in decimal</td>
</tr>
<tr>
<td>CR</td>
<td>decimal</td>
<td>(--)</td>
<td>If inside definition, output in decimal</td>
</tr>
<tr>
<td>CR</td>
<td>.h</td>
<td>(n --)</td>
<td>Displays number in hexadecimal</td>
</tr>
<tr>
<td>CR</td>
<td>hex</td>
<td>(--)</td>
<td>If inside definition, output in hexadecimal</td>
</tr>
<tr>
<td>CR</td>
<td>octal</td>
<td>(--)</td>
<td>If inside definition, output in octal</td>
</tr>
<tr>
<td>9E</td>
<td>.r</td>
<td>(n size --)</td>
<td>Displays a number in a fixed width field</td>
</tr>
<tr>
<td>9F</td>
<td>.s</td>
<td>(... -- ...)</td>
<td>Displays the contents of the data stack</td>
</tr>
<tr>
<td>CR</td>
<td>s.</td>
<td>(n --)</td>
<td>Displays n as a signed number</td>
</tr>
<tr>
<td>9B</td>
<td>u.</td>
<td>(u --)</td>
<td>Displays an unsigned number</td>
</tr>
<tr>
<td>9C</td>
<td>u.r</td>
<td>(u size --)</td>
<td>Prints an unsigned number in a fixed width field</td>
</tr>
</tbody>
</table>

Table A-12 General-purpose Output

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>CR</td>
<td>.(</td>
<td>([text&lt;&gt;] --)</td>
<td>Displays a string now</td>
</tr>
<tr>
<td>91</td>
<td>(cr</td>
<td>(--)</td>
<td>Outputs ASCII CR character; decimal 13</td>
</tr>
<tr>
<td>92</td>
<td>cr</td>
<td>(--)</td>
<td>Starts a new line of display output</td>
</tr>
</tbody>
</table>
Table A-12 General-purpose Output (Continued)

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>8F</td>
<td>emit</td>
<td>(char --)</td>
<td>Displays the character</td>
</tr>
<tr>
<td>CR</td>
<td>space</td>
<td>(-- )</td>
<td>Outputs a single space character</td>
</tr>
<tr>
<td>CR</td>
<td>spaces</td>
<td>(cnt --)</td>
<td>Outputs cnt spaces</td>
</tr>
<tr>
<td>90</td>
<td>type</td>
<td>(text-str text-len --)</td>
<td>Displays n characters</td>
</tr>
</tbody>
</table>

Table A-13 Formatted Output

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>94</td>
<td>#line</td>
<td>(-- a-addr)</td>
<td>Variable holding the line number on the output device</td>
</tr>
<tr>
<td>93</td>
<td>#out</td>
<td>(-- a-addr)</td>
<td>Variable holding the column number on the output device</td>
</tr>
</tbody>
</table>

Table A-14 begin Loops

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>CR</td>
<td>again</td>
<td>(C: dest-sys --)</td>
<td>Ends begin…again (infinite) loop</td>
</tr>
<tr>
<td>CR</td>
<td>begin</td>
<td>(C: -- dest-sys ) (--)</td>
<td>Starts conditional loop</td>
</tr>
<tr>
<td>CR</td>
<td>repeat</td>
<td>(C: orig-sys dest-sys --) (--)</td>
<td>Returns to loop start</td>
</tr>
<tr>
<td>CR</td>
<td>until</td>
<td>(C: dest-sys --) ( done? -- )</td>
<td>If true, exits begin…until loop</td>
</tr>
<tr>
<td>CR</td>
<td>while</td>
<td>(C: dest-sys -- orig-sys dest-sys ) ( continue? -- )</td>
<td>If true, continues begin…while…repeat loop, else exits loop</td>
</tr>
</tbody>
</table>

Table A-15 Conditionals

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>CR</td>
<td>if</td>
<td>(C: -- orig-sys ) (do-next? --)</td>
<td>If true, executes next FCode(s)</td>
</tr>
<tr>
<td>CR</td>
<td>else</td>
<td>(C: orig-sys1 -- orig-sys2 ) (--)</td>
<td>(optional) Executes next FCode(s) if if failed</td>
</tr>
<tr>
<td>CR</td>
<td>then</td>
<td>(C: orig-sys -- ) (--)</td>
<td>Terminates if…else…then</td>
</tr>
</tbody>
</table>
Table A-16 Case Statements

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>CR</td>
<td>case</td>
<td>( C: -- case-sys ) ( sel -- sel )</td>
<td>Begins a case (multiple selection) statement</td>
</tr>
<tr>
<td>CR</td>
<td>endcase</td>
<td>( C: case-sys -- ) ( sel</td>
<td>&lt;nothing&gt; -- )</td>
</tr>
<tr>
<td>CR</td>
<td>of</td>
<td>( C: case-sys1 -- case-sys2 of-sys ) ( sel of-val -- sel</td>
<td>&lt;nothing&gt; )</td>
</tr>
<tr>
<td>CR</td>
<td>endof</td>
<td>( C: case-sys1 of-sys -- case-sys2 ) ( -- )</td>
<td>If true, exits begin…until loop</td>
</tr>
</tbody>
</table>

Table A-17 do Loops

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>CR</td>
<td>do</td>
<td>( C: -- dodest-sys ) ( limit start -- ) ( R: -- sys )</td>
<td>Loops, index start to end-1 inclusive</td>
</tr>
<tr>
<td>CR</td>
<td>?do</td>
<td>( C: -- dodest-sys ) ( limit start -- ) ( R: -- sys )</td>
<td>Like do, but skips loop if end = start</td>
</tr>
<tr>
<td>19</td>
<td>i</td>
<td>( -- index ) ( R: sys -- sys )</td>
<td>Returns current loop index value</td>
</tr>
<tr>
<td>1A</td>
<td>j</td>
<td>( -- index ) ( R: sys -- sys )</td>
<td>Returns value of next outer loop index</td>
</tr>
<tr>
<td>CR</td>
<td>leave</td>
<td>( -- ) ( R: sys -- )</td>
<td>Exits do loop immediately</td>
</tr>
<tr>
<td>CR</td>
<td>?leave</td>
<td>( exit? -- ) ( R: sys -- )</td>
<td>If flag is true, exits do loop</td>
</tr>
<tr>
<td>CR</td>
<td>loop</td>
<td>( C: dodest-sys -- ) ( -- ) ( R: sys1 -- &lt;nothing&gt;</td>
<td>sys2)</td>
</tr>
<tr>
<td>CR</td>
<td>+loop</td>
<td>( C: dodest-sys -- ) ( delta -- ) ( R: sys1 -- &lt;nothing&gt;</td>
<td>sys2 )</td>
</tr>
<tr>
<td>89</td>
<td>unloop</td>
<td>( -- ) ( R: sys -- )</td>
<td>Discards loop control parameters</td>
</tr>
</tbody>
</table>

Table A-18 Control Words

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1D</td>
<td>execute</td>
<td>( ... xt -- ???)</td>
<td>Executes the word whose compilation address is on the stack</td>
</tr>
<tr>
<td>33</td>
<td>exit</td>
<td>( -- ) ( R: sys -- )</td>
<td>Returns from the current word</td>
</tr>
</tbody>
</table>
### Table A-19 Strings

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>CR</td>
<td>&quot;</td>
<td>( [text&lt;&gt;]&lt; &gt; -- text-str text-len )</td>
<td>Collects a string</td>
</tr>
<tr>
<td>CR</td>
<td>s&quot;</td>
<td>( [text&quot;&quot;]&lt; &gt; -- test-str text-len )</td>
<td>Gathers the immediately-following string</td>
</tr>
<tr>
<td>84</td>
<td>count</td>
<td>( pstr -- str len )</td>
<td>Unpacks a packed string</td>
</tr>
<tr>
<td>82</td>
<td>lcc</td>
<td>( char1 -- char2 )</td>
<td>Converts char to lower case</td>
</tr>
<tr>
<td>83</td>
<td>pack</td>
<td>( str len addr -- pstr )</td>
<td>Makes a packed string from addr len, placing it at pstr</td>
</tr>
<tr>
<td>81</td>
<td>upc</td>
<td>( char1 -- char2 )</td>
<td>Converts char to upper case</td>
</tr>
<tr>
<td>0240</td>
<td>left-parse-string</td>
<td>( str len char -- R-str R-len L-str L-len )</td>
<td>Splits a string at the given delimiter (which is discarded)</td>
</tr>
<tr>
<td>011B</td>
<td>parse-2int</td>
<td>( str len -- val.lo val.hi )</td>
<td>Converts a string into a physical address and space</td>
</tr>
</tbody>
</table>

### Table A-20 Defining Words

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>CR</td>
<td>; (colon) name</td>
<td>( -- )</td>
<td>Begins colon definition</td>
</tr>
<tr>
<td>CR</td>
<td>; (semicolon)</td>
<td>( -- )</td>
<td>Ends colon definition</td>
</tr>
<tr>
<td>-</td>
<td>alias</td>
<td>( E: ... -- ???) (&quot;new-name&lt; &gt;old-name&lt; &gt;&quot; -- )</td>
<td>Creates newname with behavior of oldname</td>
</tr>
<tr>
<td>CR</td>
<td>buffer:</td>
<td>( E: -- a-addr ) ( len &quot;new-name&lt; &gt;&quot; -- )</td>
<td>Creates data array of size bytes</td>
</tr>
<tr>
<td>CR</td>
<td>constant</td>
<td>( E: -- x ) ( x &quot;new-name&lt; &gt;&quot; -- )</td>
<td>Creates a constant</td>
</tr>
<tr>
<td>CR</td>
<td>create</td>
<td>( E: -- a-addr ) (&quot;new-name&lt; &gt;&quot; -- )</td>
<td>Generic defining word</td>
</tr>
<tr>
<td>CR</td>
<td>defer</td>
<td>( E: ... -- ??? ) (&quot;new-name&lt; &gt;&quot; -- )</td>
<td>Execution vector (change with is)</td>
</tr>
<tr>
<td>CR</td>
<td>field</td>
<td>( E: addr -- addr+offset ) ( offset size &quot;new-name&lt; &gt;&quot; -- offset+size )</td>
<td>Creates a named offset pointer</td>
</tr>
<tr>
<td>C0</td>
<td>instance</td>
<td>( -- )</td>
<td>Declare a data type to be local</td>
</tr>
<tr>
<td>CR</td>
<td>struct</td>
<td>( -- 0 )</td>
<td>Initializes for field creation</td>
</tr>
<tr>
<td>CR</td>
<td>variable</td>
<td>( E: -- a-addr ) (&quot;new-name&lt; &gt;&quot;-- )</td>
<td>Creates a data variable</td>
</tr>
<tr>
<td>CR</td>
<td>value</td>
<td>( E: -- x ) ( x &quot;new-name&lt; &gt;&quot;-- )</td>
<td>Creates named value-type variable (change with is)</td>
</tr>
</tbody>
</table>
### Table A-21 Dictionary Compilation

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>D3</td>
<td>,</td>
<td>(x -- )</td>
<td>Places a number in the dictionary</td>
</tr>
<tr>
<td>D0</td>
<td>c,</td>
<td>(byte --)</td>
<td>Places a byte in the dictionary</td>
</tr>
<tr>
<td>AD</td>
<td>here</td>
<td>(addr --)</td>
<td>Address of top of dictionary</td>
</tr>
<tr>
<td>D2</td>
<td>l,</td>
<td>(quad --)</td>
<td>Places a quadlet in the dictionary</td>
</tr>
<tr>
<td>D1</td>
<td>w,</td>
<td>(w -- )</td>
<td>Places a doublet in the dictionary</td>
</tr>
<tr>
<td>CR</td>
<td>allot</td>
<td>(len --)</td>
<td>Allocates len bytes in the dictionary</td>
</tr>
<tr>
<td>CR</td>
<td>to</td>
<td>(param [old-name&lt; &gt;] --)</td>
<td>Changes value in a defer word or a value</td>
</tr>
<tr>
<td>DD</td>
<td>compile</td>
<td>(--)</td>
<td>Compiles following command at run time</td>
</tr>
<tr>
<td>DC</td>
<td>state</td>
<td>(a-addr --)</td>
<td>Variable containing true if in compilation state</td>
</tr>
</tbody>
</table>

### Table A-22 Dictionary Search

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>CR</td>
<td>'</td>
<td>(&quot;old-name&lt; &gt;&quot; -- xt)</td>
<td>Finds the word (while executing)</td>
</tr>
<tr>
<td>CR</td>
<td>['] name</td>
<td>(xt --)</td>
<td>Finds word (while compiling)</td>
</tr>
<tr>
<td>CB</td>
<td>name-str</td>
<td>(name-str name-len -- xt true</td>
<td>name-str name-len false)</td>
</tr>
<tr>
<td>CD</td>
<td>eval</td>
<td>(... str len -- ???)</td>
<td>Executes Forth commands within a string</td>
</tr>
<tr>
<td>CD</td>
<td>evaluate</td>
<td>(... str len -- ???)</td>
<td>Interprets Forth text from the given string</td>
</tr>
</tbody>
</table>

### Table A-23 Conversion Operators

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>7F</td>
<td>bljoin</td>
<td>(bl.lo b2 b3 b4.hi -- quad)</td>
<td>Joins four bytes to form a quadlet</td>
</tr>
<tr>
<td>B0</td>
<td>bwjoin</td>
<td>(b.lo b.hi -- w)</td>
<td>Joins two bytes to form a doublet</td>
</tr>
<tr>
<td>5A</td>
<td>/c</td>
<td>(n --)</td>
<td>Address increment for a byte; 1</td>
</tr>
<tr>
<td>-</td>
<td>/c*</td>
<td>(nu1 -- nu2)</td>
<td>Synonym for chars</td>
</tr>
<tr>
<td>66</td>
<td>chars</td>
<td>(nu1 -- nu2)</td>
<td>Multiplies by /c</td>
</tr>
<tr>
<td>5E</td>
<td>ca+</td>
<td>(addr1 index -- addr2)</td>
<td>Increments addr1 by index times /c</td>
</tr>
</tbody>
</table>
### Table A-23 Conversion Operators (Continued)

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>CR</td>
<td>ca1+</td>
<td>(addr1 -- addr2)</td>
<td>Synonym for chars+</td>
</tr>
<tr>
<td>62</td>
<td>char+</td>
<td>(addr1 -- addr2)</td>
<td>Increments addr1 by /c</td>
</tr>
<tr>
<td>80</td>
<td>wbflip</td>
<td>(w1 -- w2)</td>
<td>Swaps the bytes within a doublet</td>
</tr>
<tr>
<td>5C</td>
<td>/l</td>
<td>(n)</td>
<td>Address increment for a quadlet;</td>
</tr>
<tr>
<td>68</td>
<td>/l*</td>
<td>(nu1 -- nu2)</td>
<td>Multiplies by /l</td>
</tr>
<tr>
<td>60</td>
<td>la+</td>
<td>(addr1 index -- addr2)</td>
<td>Increments addr1 by index times /l</td>
</tr>
<tr>
<td>64</td>
<td>lal+</td>
<td>(addr1 -- addr2)</td>
<td>Increments addr1 by /l</td>
</tr>
<tr>
<td>0227</td>
<td>lbflip</td>
<td>(quad1 -- quad2)</td>
<td>Reverses the bytes within a quadlet</td>
</tr>
<tr>
<td>7E</td>
<td>lbsplit</td>
<td>(quad -- b1.lo b2 b3 b4.hi)</td>
<td>Splits a quadlet into four bytes</td>
</tr>
<tr>
<td>7E</td>
<td>lwflip</td>
<td>(quad1 -- quad2)</td>
<td>Swaps the doublets within a quadlet</td>
</tr>
<tr>
<td>7C</td>
<td>lwsplit</td>
<td>(quad -- w1.lo w2.hi)</td>
<td>Splits a quadlet into two doublets</td>
</tr>
<tr>
<td>5D</td>
<td>/n</td>
<td>(n)</td>
<td>Address increment for a normal;</td>
</tr>
<tr>
<td>CR</td>
<td>/n*</td>
<td>(nu1 -- nu2)</td>
<td>Synonym for cells</td>
</tr>
<tr>
<td>69</td>
<td>cells</td>
<td>(nu1 -- nu2)</td>
<td>Multiplies by /n</td>
</tr>
<tr>
<td>61</td>
<td>na+</td>
<td>(addr1 index -- addr2)</td>
<td>Increments addr1 by index times /n</td>
</tr>
<tr>
<td>CR</td>
<td>na1+</td>
<td>(addr1 -- addr2)</td>
<td>Synonym for cell+</td>
</tr>
<tr>
<td>65</td>
<td>cell+</td>
<td>(addr1 -- addr2)</td>
<td>Increments addr1 by /n</td>
</tr>
<tr>
<td>5B</td>
<td>/w</td>
<td>(n)</td>
<td>Address increment for a doublet;</td>
</tr>
<tr>
<td>67</td>
<td>/w*</td>
<td>(nu1 -- nu2)</td>
<td>Multiplies by /w</td>
</tr>
<tr>
<td>5F</td>
<td>wa+</td>
<td>(addr1 index -- addr2)</td>
<td>Increments addr1 by index times /w</td>
</tr>
<tr>
<td>63</td>
<td>wal+</td>
<td>(addr1 -- addr2)</td>
<td>Increments addr1 by /w</td>
</tr>
<tr>
<td>AF</td>
<td>wbsplit</td>
<td>(w -- b1.lo b2.hi)</td>
<td>Splits a doublet into two bytes</td>
</tr>
<tr>
<td>7D</td>
<td>w1join</td>
<td>(w.lo w.hi -- quad)</td>
<td>Joins two doublets to form a quadlet</td>
</tr>
</tbody>
</table>

### Table A-24 64-bit Operations

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>02 2E</td>
<td>rx@</td>
<td>(oaddr -- o)</td>
<td>Reads the 64-bit value at the given address, atomically</td>
</tr>
<tr>
<td>02 2F</td>
<td>rx!</td>
<td>(o oaddr -- )</td>
<td>Writes the 64-bit value at the given address, atomically</td>
</tr>
<tr>
<td>02 41</td>
<td>bxjoin</td>
<td>(b.lo b.2 b.3 b.4 b.5 b.6 b.7 b.hi -- o)</td>
<td></td>
</tr>
</tbody>
</table>
### Table A-24 64-bit Operations (Continued)

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>02 42</td>
<td>&lt;1@</td>
<td>( qaddr -- n )</td>
<td>Fetches a sign-extended quadlet at qaddr</td>
</tr>
<tr>
<td>02 43</td>
<td>lxjoin</td>
<td>( quad.lo quad.hi -- o )</td>
<td>Joins two quadlets to form an octlet</td>
</tr>
<tr>
<td>02 44</td>
<td>wxjoin</td>
<td>( w.lo w.2 w.3 w.hi -- o )</td>
<td>Joins four doublets to form an octlet</td>
</tr>
<tr>
<td>02 45</td>
<td>x</td>
<td>( o -- )</td>
<td>Places an octlet in the dictionary</td>
</tr>
<tr>
<td>02 46</td>
<td>x@</td>
<td>( oaddr -- o )</td>
<td>Fetches the octlet at oaddr, must be 64-bit aligned</td>
</tr>
<tr>
<td>02 47</td>
<td>x!</td>
<td>( o oaddr -- )</td>
<td>Stores an octlet at oaddr, must be 64-bit aligned</td>
</tr>
<tr>
<td>02 48</td>
<td>/x</td>
<td>( -- n )</td>
<td>Address increment for an octlet; 8</td>
</tr>
<tr>
<td>02 49</td>
<td>/x*</td>
<td>( nu1 -- nu2 )</td>
<td></td>
</tr>
<tr>
<td>02 4A</td>
<td>xa+</td>
<td>(addr1 index -- addr2 )</td>
<td>Increments addr1 by index times /x</td>
</tr>
<tr>
<td>02 4B</td>
<td>xal+</td>
<td>( addr1 -- addr2 )</td>
<td>Increments addr1 by /x</td>
</tr>
<tr>
<td>02 4C</td>
<td>xbflip</td>
<td>( oct1 -- oct2 )</td>
<td>Reverse bytes within octlet</td>
</tr>
<tr>
<td>02 4D</td>
<td>xbflips</td>
<td>( oaddr len -- )</td>
<td>Reverse bytes within each octlet in given region</td>
</tr>
<tr>
<td>02 4E</td>
<td>xbsplit</td>
<td>( o -- b.lo b.2 b.3 b.4 b.5 b.6 b.7 b.hi )</td>
<td>Splits an octlet into 8 bytes</td>
</tr>
<tr>
<td>02 4F</td>
<td>xlflip</td>
<td>( oct1 -- oct2 )</td>
<td>Reverse quadlets within octlet</td>
</tr>
<tr>
<td>02 50</td>
<td>xlflips</td>
<td>( oaddr len -- )</td>
<td>Reverse quadlets within each octlet in given region</td>
</tr>
<tr>
<td>02 51</td>
<td>xlsplit</td>
<td>( o -- quad.lo quad.hi )</td>
<td>Splits an octlet into 2 quadlets</td>
</tr>
<tr>
<td>02 52</td>
<td>xwflip</td>
<td>( oct1 -- oct2 )</td>
<td>Reverse doublets within octlet</td>
</tr>
<tr>
<td>02 53</td>
<td>xwflips</td>
<td>( oaddr len -- )</td>
<td>Reverse doublets within each octlet in given region</td>
</tr>
<tr>
<td>02 54</td>
<td>xwsplit</td>
<td>( o -- w.lo w.2 w.3 w.hi )</td>
<td>Splits an octlet into 4 doublets</td>
</tr>
</tbody>
</table>
### Table A-25 Memory Buffers Allocation

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>8B</td>
<td>alloc-mem</td>
<td>( len -- a-addr )</td>
<td>Allocates nbytes of memory and returns its address</td>
</tr>
<tr>
<td>8C</td>
<td>free-mem</td>
<td>( a-addr len -- )</td>
<td>Frees memory allocated by alloc-mem</td>
</tr>
</tbody>
</table>

### Table A-26 Miscellaneous Operators

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>86</td>
<td>&gt;body</td>
<td>( xt -- a-addr )</td>
<td>Finds parameter field address from compilation address</td>
</tr>
<tr>
<td>85</td>
<td>body&gt;</td>
<td>( a-addr -- xt )</td>
<td>Finds compilation address from parameter field address</td>
</tr>
<tr>
<td>DA</td>
<td>get-token</td>
<td>( FCode# -- xt immediate? )</td>
<td>Converts FCode Number to function execution token</td>
</tr>
<tr>
<td>DB</td>
<td>set-token</td>
<td>( xt immediate? FCode# -- )</td>
<td>Assigns FCode Number to existing function</td>
</tr>
<tr>
<td>00</td>
<td>end0</td>
<td>( -- )</td>
<td>Marks the end of FCode</td>
</tr>
<tr>
<td>FF</td>
<td>end1</td>
<td>( -- )</td>
<td>Alternates form for end0 (not recommended)</td>
</tr>
<tr>
<td>CR</td>
<td>fcode-</td>
<td>( -- )</td>
<td>Begins FCode program</td>
</tr>
<tr>
<td></td>
<td>version1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>023E</td>
<td>byte-load</td>
<td>( addr xt -- )</td>
<td>Interprets FCode beginning at location addr</td>
</tr>
<tr>
<td>-</td>
<td>fload</td>
<td>( [filename&lt;cr&gt;] -- )</td>
<td>Begins tokenizing filename</td>
</tr>
<tr>
<td>-</td>
<td>headerless</td>
<td>( -- )</td>
<td>Creates new names with new-token (no name fields)</td>
</tr>
<tr>
<td>-</td>
<td>headers</td>
<td>( -- )</td>
<td>Creates new names with named-token (default)</td>
</tr>
<tr>
<td>7B</td>
<td>noop</td>
<td>( -- )</td>
<td>Does nothing</td>
</tr>
<tr>
<td>CC</td>
<td>offset16</td>
<td>( -- )</td>
<td>All further branches use 16-bit offsets (instead of 8-bit)</td>
</tr>
<tr>
<td>-</td>
<td>tokenizer{</td>
<td>( -- )</td>
<td>Begins tokenizer program commands</td>
</tr>
<tr>
<td>-</td>
<td>tokenizer]</td>
<td>( -- )</td>
<td>Ends tokenizer program commands</td>
</tr>
<tr>
<td>CR</td>
<td>fcode-</td>
<td>( -- )</td>
<td>Begins 2.0 FCode program, compiles start1</td>
</tr>
<tr>
<td></td>
<td>version2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>-</td>
<td>external</td>
<td>( -- )</td>
<td>Creates new names with external-token</td>
</tr>
</tbody>
</table>
Table A-27 Internal Operators, (invalid for program text)

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>01-0F</td>
<td>b(lit)</td>
<td>( -- n ) ( F: /FCode-num32/ -- )</td>
<td>Followed by 32-bit#. Compiled by numeric data</td>
</tr>
<tr>
<td>10</td>
<td>b(')</td>
<td>( -- xt ) ( F: /FCode#/ -- )</td>
<td>Followed by a token (1 or 2-byte code) . Compiled by [ ’ ] or ’</td>
</tr>
<tr>
<td>11</td>
<td>b(*)</td>
<td>( -- str len ) ( F: /FCode-string/ -- )</td>
<td>Followed by count byte, text. Compiled by &quot; or .&quot;</td>
</tr>
<tr>
<td>C3</td>
<td>b(to)</td>
<td>( x -- )</td>
<td>Compiled by to</td>
</tr>
<tr>
<td>FD</td>
<td>version1</td>
<td>( -- )</td>
<td>Followed by reserved byte, checksum (2 bytes) , length (4 bytes). Compiled by fcode-version1, as the first FCode bytes</td>
</tr>
<tr>
<td>13</td>
<td>bbranch</td>
<td>( -- ) ( F: /FCode-offset/ -- )</td>
<td>Followed by offset. Compiled by else or again</td>
</tr>
<tr>
<td>14</td>
<td>b?branch</td>
<td>( don’t-branch? -- ) ( F: /FCode-offset/ -- )</td>
<td>Followed by offset. Compiled by if or until</td>
</tr>
<tr>
<td>15</td>
<td>b(loop)</td>
<td>( -- ) ( F: /FCode-offset/ -- )</td>
<td>Followed by offset. Compiled by loop</td>
</tr>
<tr>
<td>16</td>
<td>b(+loop)</td>
<td>( delta -- ) ( F: /FCode-offset/ -- )</td>
<td>Followed by offset. Compiled by +loop</td>
</tr>
<tr>
<td>17</td>
<td>b(do)</td>
<td>( limit start -- ) ( F: /FCode-offset/ -- )</td>
<td>Followed by offset. Compiled by do</td>
</tr>
<tr>
<td>18</td>
<td>b(?do)</td>
<td>( limit start -- ) ( F: /FCode-offset/ -- )</td>
<td>Followed by offset. Compiled by ?do</td>
</tr>
<tr>
<td>1B</td>
<td>b(leave)</td>
<td>( F: -- )</td>
<td>Compiled by leave or ?leave</td>
</tr>
<tr>
<td>B1</td>
<td>b(&lt;mark)</td>
<td>( F: -- )</td>
<td>Compiled by begin</td>
</tr>
<tr>
<td>B2</td>
<td>b(&gt;resolve)</td>
<td>( -- ) ( F: -- )</td>
<td>Compiled by else or then</td>
</tr>
<tr>
<td>C4</td>
<td>b(case)</td>
<td>( sel -- sel ) ( F: -- )</td>
<td>Compiled by case</td>
</tr>
<tr>
<td>C5</td>
<td>b(endcase)</td>
<td>( sel</td>
<td>&lt;nothing&gt; -- ) ( F: -- )</td>
</tr>
<tr>
<td>C6</td>
<td>b(endif)</td>
<td>( -- ) ( F: /FCode-offset/ -- )</td>
<td>Compiled by endif</td>
</tr>
<tr>
<td>IC</td>
<td>b(of)</td>
<td>( sel of-val -- sel</td>
<td>&lt;nothing&gt; ) ( F: /FCode-offset/ -- )</td>
</tr>
<tr>
<td>B5</td>
<td>new-token</td>
<td>( -- ) ( F: /FCode#/ -- )</td>
<td>Followed by table#, code#, token-type. Compiled by any defining word. Headerless, not used normally.</td>
</tr>
<tr>
<td>B6</td>
<td>named-token</td>
<td>( -- ) ( F: /FCode-string FCode#/ -- )</td>
<td>Followed by packed string (count,text), table#, code#, token-type. Compiled by any defining word (: value constant etc.)</td>
</tr>
</tbody>
</table>
Table A-27 Internal Operators, (invalid for program text) (Continued)

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>B7</td>
<td>b(:)</td>
<td>(E: ... -- ???) (F: -- colon-sys)</td>
<td>Token-type compiled by :</td>
</tr>
<tr>
<td>B8</td>
<td>b(value)</td>
<td>(E: -- x) (F: x --)</td>
<td>Token-type compiled by value</td>
</tr>
<tr>
<td>B9</td>
<td>b(variable)</td>
<td>(E: -- a-addr) (F: --)</td>
<td>Token-type compiled by variable</td>
</tr>
<tr>
<td>BA</td>
<td>b(constant)</td>
<td>(E: -- n) (F: n --)</td>
<td>Token-type compiled by constant</td>
</tr>
<tr>
<td>BB</td>
<td>b(create)</td>
<td>(E: -- a-addr) (F: --)</td>
<td>Token-type compiled by create</td>
</tr>
<tr>
<td>BC</td>
<td>b(defer)</td>
<td>(E: ... -- ???) (F: --)</td>
<td>Token-type compiled by defer</td>
</tr>
<tr>
<td>BD</td>
<td>b(buffer:)</td>
<td>(E: -- a-addr) (F: size --)</td>
<td>Token-type compiled by buffer:</td>
</tr>
<tr>
<td>BE</td>
<td>b(field)</td>
<td>(E: addr -- addr+offset)</td>
<td>Token-type compiled by field</td>
</tr>
<tr>
<td></td>
<td></td>
<td>(F: offset size -- offset+size)</td>
<td></td>
</tr>
<tr>
<td>C2</td>
<td>b(;)</td>
<td>(-- ) (F: colon-sys --)</td>
<td>End a colon definition. Compiled by ;</td>
</tr>
<tr>
<td>CA</td>
<td>external-</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>token</td>
<td>(-- ) (F: /FCode-string FCode#/ --)</td>
<td>Create a new named FCode function.</td>
</tr>
<tr>
<td>F0</td>
<td>start0</td>
<td>(-- )</td>
<td>Like version1, but for version 2.0 FCodes. Uses 16-bit branches. Fetches successive tokens from same address</td>
</tr>
<tr>
<td>F1</td>
<td>start1</td>
<td>(-- )</td>
<td>Like version1, but for version 2.0 FCodes. Uses 16-bit branches. Fetches successive tokens from consecutive addresses. Compiled by fcode-version2</td>
</tr>
<tr>
<td>F2</td>
<td>start2</td>
<td>(-- )</td>
<td>Like version1, but for version 2.0 FCodes. Uses 16-bit branches. Fetches successive tokens from consecutive 16-bit addresses</td>
</tr>
<tr>
<td>F3</td>
<td>start4</td>
<td>(-- )</td>
<td>Like version1, but for version 2.0 FCodes. Uses 16-bit branches. Fetches successive tokens from consecutive 32-bit addresses</td>
</tr>
</tbody>
</table>

Table A-28 Virtual Memory Allocation

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0105</td>
<td>free-virtual</td>
<td>(virt size --)</td>
<td>Frees virtual memory obtained using map-low.</td>
</tr>
<tr>
<td>0130</td>
<td>map-low</td>
<td>(phys-low .... size -- virt)</td>
<td>Allocate virtual memory.</td>
</tr>
</tbody>
</table>
Table A-29 Properties

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0110</td>
<td>property</td>
<td>( prop-addr prop-len name-str name-len -- )</td>
<td>Declares a property with the given value structure, for the given name string.</td>
</tr>
<tr>
<td>021E</td>
<td>delete-property</td>
<td>( nam-str nam-len -- )</td>
<td>Deletes the property with the given name</td>
</tr>
<tr>
<td>0115</td>
<td>encode-bytes</td>
<td>( data-addr data-len -- prop-addr prop-len )</td>
<td>Converts a byte array into an prop-format string</td>
</tr>
<tr>
<td>0111</td>
<td>encode-int</td>
<td>( n -- prop-addr prop-len )</td>
<td>Converts a number into an prop-format string</td>
</tr>
<tr>
<td>0113</td>
<td>encode-phys</td>
<td>( phys.lo ... phys.hi -- prop-addr prop-len )</td>
<td>Converts physical address and space into an prop-format string</td>
</tr>
<tr>
<td>0114</td>
<td>encode-string</td>
<td>( str len -- prop-addr prop-len )</td>
<td>Converts a string into an prop-format string</td>
</tr>
<tr>
<td>0112</td>
<td>encode+</td>
<td>( prop-addr1 prop-len1 prop-addr2 prop-len2 prop-addr3 prop-len3 )</td>
<td>Merges two prop-format strings. They must have been created sequentially</td>
</tr>
<tr>
<td>CR</td>
<td>decode-bytes</td>
<td>( prop-addr1 prop-len1 data-len prop-addr2 prop-len2 data-addr data-len )</td>
<td>Decodes a byte array from a prop-encoded-array</td>
</tr>
<tr>
<td>021B</td>
<td>decode-int</td>
<td>( prop-addr1 prop-len1 prop-addr2 prop-len2 n )</td>
<td>Converts the beginning of an prop-format string to an integer</td>
</tr>
<tr>
<td>021C</td>
<td>decode-string</td>
<td>( prop-addr1 prop-len1 prop-addr2 prop-len2 str len )</td>
<td>Converts the beginning of a prop-format string to a normal string</td>
</tr>
<tr>
<td>0128</td>
<td>decode-phys</td>
<td>( prop-addr1 prop-len1 phys.lo ... phys.hi )</td>
<td>Decode a unit-address from a prop-encoded array</td>
</tr>
<tr>
<td>021A</td>
<td>get-my-property</td>
<td>( nam-str nam-len -- true</td>
<td>prop-addr prop-len false )</td>
</tr>
<tr>
<td>021D</td>
<td>get-inherited-property</td>
<td>( nam-str nam-len -- true</td>
<td>prop-addr prop-len false )</td>
</tr>
<tr>
<td>021F</td>
<td>get-package-property</td>
<td>( name-str name-len phandle -- true</td>
<td>prop-addr prop-len false )</td>
</tr>
</tbody>
</table>
### Table A-30 Commonly-used Properties

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0116</td>
<td>reg</td>
<td>( phys.lo ... phys.hi size -- )</td>
<td>Declares location and size of device registers</td>
</tr>
<tr>
<td>0119</td>
<td>model</td>
<td>( str len -- )</td>
<td>Declares model# for this device, such as &quot;SUNW,501-1623-01&quot;</td>
</tr>
<tr>
<td>011A</td>
<td>device-type</td>
<td>( str len -- )</td>
<td>Declares type of device, e.g. &quot;display&quot;, &quot;block&quot;, &quot;network&quot;, or &quot;byte&quot;</td>
</tr>
<tr>
<td>CR</td>
<td>name</td>
<td>( addr len -- )</td>
<td>Declares SunOS driver name, as in &quot;SUNW,zebra&quot;</td>
</tr>
<tr>
<td>0201</td>
<td>device-name</td>
<td>( str len -- )</td>
<td>Creates the &quot;name&quot; property with the given value</td>
</tr>
</tbody>
</table>

### Table A-31 System Version Information

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>87</td>
<td>fcode-revision</td>
<td>(-- n )</td>
<td>Returns major/minor FCode interface version</td>
</tr>
</tbody>
</table>

### Table A-32 Device Activation Vector Setup

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>011C</td>
<td>is-install</td>
<td>( xt -- )</td>
<td>Identifies &quot;install&quot; routine to allocate a frame buffer</td>
</tr>
<tr>
<td>011D</td>
<td>is-remove</td>
<td>( xt -- )</td>
<td>Identifies &quot;remove&quot; routine, to deallocate a frame buffer</td>
</tr>
<tr>
<td>011E</td>
<td>is-selftest</td>
<td>( xt -- )</td>
<td>Identifies &quot;selftest&quot; routine for this frame buffer</td>
</tr>
<tr>
<td>011F</td>
<td>new-device</td>
<td>( -- )</td>
<td>Opens an additional device, using this driver package</td>
</tr>
<tr>
<td>0127</td>
<td>finish-device</td>
<td>( -- )</td>
<td>Closes out current device, ready for new-device</td>
</tr>
</tbody>
</table>

### Table A-33 Self-test Utility Routines

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0120</td>
<td>diagnostic-mode?</td>
<td>( -- diag? )</td>
<td>Returns &quot;true&quot; if extended diagnostics are desired</td>
</tr>
<tr>
<td>0121</td>
<td>display-status</td>
<td>( n -- )</td>
<td>Obsolete</td>
</tr>
<tr>
<td>0122</td>
<td>memory-test-suite</td>
<td>( addr len -- fail? )</td>
<td>Calls memory tester for given region</td>
</tr>
<tr>
<td>0124</td>
<td>mask</td>
<td>( -- a-addr )</td>
<td>Variable, holds &quot;mask&quot; used by memory-test-suite</td>
</tr>
</tbody>
</table>
### Table A-34 Time Utilities

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0125</td>
<td>get-msecs</td>
<td>( -- n )</td>
<td>Returns the current time, in milliseconds, approx.</td>
</tr>
<tr>
<td>0126</td>
<td>ms</td>
<td>( n -- )</td>
<td>Delays for n milliseconds. Resolution is 1 millisecond</td>
</tr>
<tr>
<td>0213</td>
<td>alarm</td>
<td>( xt n -- )</td>
<td>Periodically execute xt. If n=0, stop.</td>
</tr>
</tbody>
</table>

### Table A-35 Machine-specific Support

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0130</td>
<td>map-low</td>
<td>( phys.lo ... size -- virt )</td>
<td>Maps a region of memory in 'sbus' address space</td>
</tr>
<tr>
<td>0131</td>
<td>sbus-intr&gt;cpu</td>
<td>( sbus-intr# -- cpu-intr# )</td>
<td>Translates SBus interrupt# into CPU interrupt#</td>
</tr>
</tbody>
</table>

### Note – Table A-36 through Table A-42 apply only to display device-types.

### Table A-36 User-set Terminal Emulation Values

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0150</td>
<td>#lines</td>
<td>( -- rows )</td>
<td>Number of lines of text being used for display. This word must be initialized (using is). fbx-install does this automatically, and also properly incorporates the NVRAM parameter &quot;screen-#rows&quot;</td>
</tr>
<tr>
<td>0151</td>
<td>#columns</td>
<td>( -- columns )</td>
<td>Number of columns (chars/line) used for display. This word must be initialized (using is). fbx-install does this automatically, and also properly incorporates the NVRAM parameter &quot;screen-#columns&quot;</td>
</tr>
</tbody>
</table>

### Table A-37 Terminal Emulator-set Terminal Emulation Values

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0152</td>
<td>line#</td>
<td>( -- line# )</td>
<td>Current cursor position (line#). 0 is top line</td>
</tr>
<tr>
<td>0153</td>
<td>column#</td>
<td>( -- column# )</td>
<td>Current cursor position. 0 is left char.</td>
</tr>
<tr>
<td>0154</td>
<td>inverse?</td>
<td>( -- white-on-black? )</td>
<td>True if output is inverted (white-on-black)</td>
</tr>
<tr>
<td>0155</td>
<td>inverse-screen?</td>
<td>( -- black? )</td>
<td>True if screen has been inverted (black background)</td>
</tr>
</tbody>
</table>
### Table A-38 Terminal Emulation Routines*

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0157</td>
<td>draw-character</td>
<td>( char -- )</td>
<td>Paints the given character and advances the cursor</td>
</tr>
<tr>
<td>0158</td>
<td>reset-screen</td>
<td>( -- )</td>
<td>Initializes the display device</td>
</tr>
<tr>
<td>0159</td>
<td>toggle-cursor</td>
<td>( -- )</td>
<td>Draws or erase the cursor</td>
</tr>
<tr>
<td>015A</td>
<td>erase-screen</td>
<td>( -- )</td>
<td>Clears all pixels on the display</td>
</tr>
<tr>
<td>015B</td>
<td>blink-screen</td>
<td>( -- )</td>
<td>Flashes the display momentarily</td>
</tr>
<tr>
<td>015C</td>
<td>invert-screen</td>
<td>( -- )</td>
<td>Changes all pixels to the opposite color</td>
</tr>
<tr>
<td>015D</td>
<td>insert-characters</td>
<td>( n -- )</td>
<td>Inserts n blanks just before the cursor</td>
</tr>
<tr>
<td>015E</td>
<td>delete-characters</td>
<td>( n -- )</td>
<td>Deletes n characters to the right of the cursor</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>Remaining chars slide left</td>
</tr>
<tr>
<td>015F</td>
<td>insert-lines</td>
<td>( n -- )</td>
<td>Inserts n blank lines just before the current line, lower lines are scrolled downward</td>
</tr>
<tr>
<td>0160</td>
<td>delete-lines</td>
<td>( n -- )</td>
<td>Deletes n lines starting with the current line, lower lines are scrolled upward</td>
</tr>
<tr>
<td>0161</td>
<td>draw-logo</td>
<td>( line# addr width height -- )</td>
<td>Draws the logo</td>
</tr>
</tbody>
</table>

*defer-type loadable routines.

### Table A-39 Frame Buffer Parameter Values*

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>016C</td>
<td>char-height</td>
<td>( -- height )</td>
<td>Height (in pixels) of a character (usually 22)</td>
</tr>
<tr>
<td>016D</td>
<td>char-width</td>
<td>( -- width )</td>
<td>Width (in pixels) of a character (usually 12)</td>
</tr>
<tr>
<td>016F</td>
<td>fontbytes</td>
<td>( -- bytes )</td>
<td>Number of bytes/scan line for font entries (usually 2)</td>
</tr>
<tr>
<td>0162</td>
<td>frame-buffer-adr</td>
<td>( -- addr )</td>
<td>Address of frame buffer memory</td>
</tr>
<tr>
<td>0163</td>
<td>screen-height</td>
<td>( -- height )</td>
<td>Total height of the display (in pixels)</td>
</tr>
<tr>
<td>0164</td>
<td>screen-width</td>
<td>( -- width )</td>
<td>Total width of the display (in pixels)</td>
</tr>
<tr>
<td>0165</td>
<td>window-top</td>
<td>( -- border-height )</td>
<td>Distance (in pixels) between display top and text window</td>
</tr>
<tr>
<td>0166</td>
<td>window-left</td>
<td>( -- border-width )</td>
<td>Distance (in pixels) between display left edge and text window left edge</td>
</tr>
</tbody>
</table>

*These must all be initialized before using any fbx- routines.
### Table A-40 Font Operators

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>016A</td>
<td>default-font</td>
<td>(addr width height advance min-char #glyphs)</td>
<td>Returns default font values, plugs directly into set-font</td>
</tr>
<tr>
<td>016B</td>
<td>set-font</td>
<td>(addr width height advance min-char #glyphs)</td>
<td>Sets the character font for text output</td>
</tr>
<tr>
<td>016E</td>
<td>&gt;font</td>
<td>(char -- addr)</td>
<td>Returns font address for given ASCII character</td>
</tr>
</tbody>
</table>

### Table A-41 One-bit Frame Buffer Utilities

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0170</td>
<td>fbl-draw-character</td>
<td>(char --)</td>
<td>Paints the character and advance the cursor</td>
</tr>
<tr>
<td>0171</td>
<td>fbl-reset-screen</td>
<td>(--)</td>
<td>Initializes the display device (noop)</td>
</tr>
<tr>
<td>0172</td>
<td>fbl-toggle-cursor</td>
<td>(--)</td>
<td>Draws or erases the cursor</td>
</tr>
<tr>
<td>0173</td>
<td>fbl-erase-screen</td>
<td>(--)</td>
<td>Clears all pixels on the display</td>
</tr>
<tr>
<td>0174</td>
<td>fbl-blink-screen</td>
<td>(--)</td>
<td>Inverts the screen, twice (slow)</td>
</tr>
<tr>
<td>0175</td>
<td>fbl-invert-screen</td>
<td>(--)</td>
<td>Changes all pixels to the opposite color</td>
</tr>
<tr>
<td>0176</td>
<td>fbl-insert-characters</td>
<td>(n --)</td>
<td>Inserts n blanks just before the cursor</td>
</tr>
<tr>
<td>0177</td>
<td>fbl-delete-characters</td>
<td>(n --)</td>
<td>Deletes n characters, starting at with cursor character, rightward. Remaining chars slide left</td>
</tr>
<tr>
<td>0178</td>
<td>fbl-insert-lines</td>
<td>(n --)</td>
<td>Inserts n blank lines just before the current line, lower lines are scrolled downward</td>
</tr>
<tr>
<td>0179</td>
<td>fbl-delete-lines</td>
<td>(n --)</td>
<td>Deletes n lines starting with the current line, lower lines are scrolled upward</td>
</tr>
<tr>
<td>017A</td>
<td>fbl-draw-logo</td>
<td>(line# addr width height --)</td>
<td>Draws the logo</td>
</tr>
<tr>
<td>017B</td>
<td>fbl-install</td>
<td>(width height #columns #lines --)</td>
<td>Installs the one-bit built-in routines</td>
</tr>
<tr>
<td>017C</td>
<td>fbl-slide-up</td>
<td>(n --)</td>
<td>Like fbl-delete-lines, but doesn’t clear lines at bottom</td>
</tr>
</tbody>
</table>
Table A-42 Eight-bit Frame Buffer Utilities

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0180</td>
<td>fb8-draw-character</td>
<td>( char -- )</td>
<td>Paints the character and advance the cursor</td>
</tr>
<tr>
<td>0181</td>
<td>fb8-reset-screen</td>
<td>( -- )</td>
<td>Initializes the display device (noop)</td>
</tr>
<tr>
<td>0182</td>
<td>fb8-toggle-cursor</td>
<td>( -- )</td>
<td>Draws or erases the cursor</td>
</tr>
<tr>
<td>0183</td>
<td>fb8-erase-screen</td>
<td>( -- )</td>
<td>Clears all pixels on the display</td>
</tr>
<tr>
<td>0184</td>
<td>fb8-blink-screen</td>
<td>( -- )</td>
<td>Inverts the screen, twice (slow)</td>
</tr>
<tr>
<td>0185</td>
<td>fb8-invert-screen</td>
<td>( -- )</td>
<td>Changes all pixels to the opposite color</td>
</tr>
<tr>
<td>0186</td>
<td>fb8-insert-characters</td>
<td>( n -- )</td>
<td>Inserts n blanks just before the cursor</td>
</tr>
<tr>
<td>0187</td>
<td>fb8-delete-characters</td>
<td>( n -- )</td>
<td>Deletes n characters starting with cursor char, rightward. Remaining chars slide left</td>
</tr>
<tr>
<td>0188</td>
<td>fb8-insert-lines</td>
<td>( n -- )</td>
<td>Inserts n blank lines just before the current line, lower lines are scrolled downward</td>
</tr>
<tr>
<td>0189</td>
<td>fb8-delete-lines</td>
<td>( n -- )</td>
<td>Deletes n lines starting with the current line, lower lines are scrolled upward</td>
</tr>
<tr>
<td>018A</td>
<td>fb8-draw-logo</td>
<td>( line# addr width height -- )</td>
<td>Draws the logo</td>
</tr>
<tr>
<td>018B</td>
<td>fb8-install</td>
<td>( width height #columns #lines -- )</td>
<td>Installs the eight-bit built-in routines</td>
</tr>
</tbody>
</table>

Table A-43 Package Support

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>023C</td>
<td>peer</td>
<td>( phandle -- phandle.sibling )</td>
<td>Returns phandle of package that is the next child of the parent of the package</td>
</tr>
<tr>
<td>023B</td>
<td>child</td>
<td>( phandle.parent -- phandle.child )</td>
<td>Returns phandle of the package that is the first child of the package parent-phandle</td>
</tr>
<tr>
<td>0204</td>
<td>find-package</td>
<td>( name-str name-len -- false</td>
<td>phandle true )</td>
</tr>
</tbody>
</table>
Table A-43 Package Support (Continued)

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0205</td>
<td>open-package</td>
<td>( arg-str arg-len phandle -- ihandle</td>
<td>0 )</td>
</tr>
<tr>
<td>020F</td>
<td>$open-package</td>
<td>( arg-str arg-len name-str name-len -- ihandle</td>
<td>0 )</td>
</tr>
<tr>
<td>020A</td>
<td>my-parent</td>
<td>( -- ihandle )</td>
<td>Returns the ihandle of the parent of the current package instance</td>
</tr>
<tr>
<td>0203</td>
<td>my-self</td>
<td>( -- ihandle )</td>
<td>Returns the instance handle of currently-executing package instance</td>
</tr>
<tr>
<td>020B</td>
<td>ihandle&gt;phandle</td>
<td>( ihandle -- phandle )</td>
<td>Converts an ihandle to a phandle</td>
</tr>
<tr>
<td>0206</td>
<td>close-package</td>
<td>( ihandle -- )</td>
<td>Closes an instance of a package</td>
</tr>
<tr>
<td>0207</td>
<td>find-method</td>
<td>( method-str method-len phandle -- false</td>
<td>xt true )</td>
</tr>
<tr>
<td>0208</td>
<td>call-package</td>
<td>( ... xt ihandle -- ??? )</td>
<td>Executes the method “xt” in the instance “ihandle”</td>
</tr>
<tr>
<td>020E</td>
<td>$call-method</td>
<td>( ... method-str method-len ihandle -- ??? )</td>
<td>Executes the method named “method-str” in the instance “ihandle”</td>
</tr>
<tr>
<td>0209</td>
<td>$call-parent</td>
<td>( ... method-str method-len -- ??? )</td>
<td>Executes the method “method-str” in the parent’s package</td>
</tr>
<tr>
<td>0202</td>
<td>my-args</td>
<td>( -- arg-str arg-len )</td>
<td>Returns the argument str passed when this package was opened</td>
</tr>
<tr>
<td>020D</td>
<td>my-unit</td>
<td>( -- phys.lo ... phys.hi )</td>
<td>Returns the physical unit number pair for this package</td>
</tr>
<tr>
<td>0102</td>
<td>my-address</td>
<td>( -- phys.lo ... )</td>
<td>Returns the physical addr of this plug-in device. “phys” is a “magic” number, usable by other routines</td>
</tr>
<tr>
<td>0103</td>
<td>my-space</td>
<td>( -- phys.hi )</td>
<td>Returns address space of plug-in device. “space” is a “magic” number, usable by other routines</td>
</tr>
</tbody>
</table>
Table A-44 Asynchronous Support

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0213</td>
<td>alarm</td>
<td>( xt n -- )</td>
<td>Executes method (command) indicated by &quot;xt&quot; every &quot;n&quot; milliseconds</td>
</tr>
<tr>
<td>0219</td>
<td>user-abort</td>
<td>( ... -- ) ( R: ... -- )</td>
<td>Abort after alarm routine finishes execution</td>
</tr>
</tbody>
</table>

Table A-45 Miscellaneous Operations

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0214</td>
<td>(is-user-word)</td>
<td>( E: ... -- ??? ) ( name-str name-len xt -- )</td>
<td>Creates a new word called &quot;name-str&quot; which executes &quot;xt&quot;</td>
</tr>
<tr>
<td>01A4</td>
<td>mac-address</td>
<td>( -- mac-str mac-len )</td>
<td>Returns the MAC address</td>
</tr>
</tbody>
</table>

Table A-46 Interpretation

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0215</td>
<td>suspend-fcode</td>
<td>( -- )</td>
<td>Suspends execution of FCode, resumes later if an undefined command is required</td>
</tr>
</tbody>
</table>

Table A-47 Error Handling

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0216</td>
<td>abort</td>
<td>( ... -- ) (R: ... -- )</td>
<td>Aborts FCode execution, returns to the &quot;ok&quot; prompt</td>
</tr>
<tr>
<td>0217</td>
<td>catch</td>
<td>( ... xt -- ??? error-code</td>
<td>??? false )</td>
</tr>
<tr>
<td>0218</td>
<td>throw</td>
<td>( ... error-code -- ??? error-code</td>
<td>... )</td>
</tr>
<tr>
<td>FC</td>
<td>ferror</td>
<td>( -- )</td>
<td>Displays &quot;Unimplemented FCode&quot; and stops FCode interpretation</td>
</tr>
</tbody>
</table>
### FCodes by Byte Value

The following table lists, in hexadecimal order, currently-assigned FCode byte values.

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
</tr>
</thead>
<tbody>
<tr>
<td>00</td>
<td>end0</td>
<td>(-- )</td>
</tr>
<tr>
<td>10</td>
<td>b(lit)</td>
<td>(-- n ) ( F: /FCode-num32/ -- )</td>
</tr>
<tr>
<td>11</td>
<td>b(')</td>
<td>(-- xt ) ( F: /FCode#/ -- )</td>
</tr>
<tr>
<td>12</td>
<td>b(*)</td>
<td>(-- str len ) ( F: /FCode-string/ -- )</td>
</tr>
<tr>
<td>13</td>
<td>bbranch</td>
<td>(-- ) ( F: /FCode-offset/ -- )</td>
</tr>
<tr>
<td>14</td>
<td>b?branch</td>
<td>( don’t-branch? -- ) ( F: /FCode-offset/ -- )</td>
</tr>
<tr>
<td>15</td>
<td>b(loop)</td>
<td>(-- ) ( F: /FCode-offset/ -- )</td>
</tr>
<tr>
<td>16</td>
<td>b(+loop)</td>
<td>( delta -- ) ( F: /FCode-offset/ -- )</td>
</tr>
<tr>
<td>17</td>
<td>b(do)</td>
<td>( limit start -- ) ( F: /FCode-offset/ -- )</td>
</tr>
<tr>
<td>18</td>
<td>b(?do)</td>
<td>( limit start -- ) ( F: /FCode-offset/ -- )</td>
</tr>
<tr>
<td>19</td>
<td>i</td>
<td>(-- index ) ( R: sys -- sys )</td>
</tr>
<tr>
<td>1A</td>
<td>j</td>
<td>(-- index ) ( R: sys -- sys )</td>
</tr>
<tr>
<td>1B</td>
<td>b(leave)</td>
<td>( F: -- )</td>
</tr>
<tr>
<td>1C</td>
<td>b(of)</td>
<td>( sel of-val -- sel</td>
</tr>
<tr>
<td>1D</td>
<td>execute</td>
<td>( ... xt -- ??? )</td>
</tr>
<tr>
<td>1E</td>
<td>+</td>
<td>( nu1 nu2 -- sum )</td>
</tr>
<tr>
<td>1F</td>
<td>-</td>
<td>( nu1 nu2 -- diff )</td>
</tr>
<tr>
<td>20</td>
<td>*</td>
<td>( nu1 nu2 -- prod )</td>
</tr>
<tr>
<td>21</td>
<td>/</td>
<td>( n1 n2 -- quot )</td>
</tr>
<tr>
<td>22</td>
<td>mod</td>
<td>( n1 n2 -- rem )</td>
</tr>
<tr>
<td>23</td>
<td>and</td>
<td>( x1 x2 -- x3 )</td>
</tr>
<tr>
<td>24</td>
<td>or</td>
<td>( x1 x2 -- x3 )</td>
</tr>
<tr>
<td>25</td>
<td>xor</td>
<td>( x1 x2 -- x3 )</td>
</tr>
<tr>
<td>26</td>
<td>invert</td>
<td>( x1 -- x2 )</td>
</tr>
<tr>
<td>27</td>
<td>lshift</td>
<td>( x1 u -- x2 )</td>
</tr>
<tr>
<td>28</td>
<td>rshift</td>
<td>( x1 u -- x2 )</td>
</tr>
<tr>
<td>29</td>
<td>&gt;&gt;a</td>
<td>( x1 u -- x2 )</td>
</tr>
</tbody>
</table>
Table A-48 FCodes by Byte Value (Continued)

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
</tr>
</thead>
<tbody>
<tr>
<td>2A</td>
<td>/mod</td>
<td>( n1 n2 -- rem quot )</td>
</tr>
<tr>
<td>2B</td>
<td>u/mod</td>
<td>( u1 u2 -- urem uquot )</td>
</tr>
<tr>
<td>2C</td>
<td>negate</td>
<td>( n1 -- n2 )</td>
</tr>
<tr>
<td>2D</td>
<td>abs</td>
<td>( n -- u )</td>
</tr>
<tr>
<td>2E</td>
<td>min</td>
<td>( n1 n2 -- n1</td>
</tr>
<tr>
<td>2F</td>
<td>max</td>
<td>( n1 n2 -- n1</td>
</tr>
<tr>
<td>30</td>
<td>&gt;r</td>
<td>( x -- ) ( R: -- x )</td>
</tr>
<tr>
<td>31</td>
<td>r&gt;</td>
<td>( -- x ) ( R: x -- )</td>
</tr>
<tr>
<td>32</td>
<td>r@</td>
<td>( -- x ) ( R: x -- x )</td>
</tr>
<tr>
<td>33</td>
<td>exit</td>
<td>( -- ) ( R: sys -- )</td>
</tr>
<tr>
<td>34</td>
<td>0=</td>
<td>( nulflag -- equal-to-0? )</td>
</tr>
<tr>
<td>35</td>
<td>0&lt;&gt;</td>
<td>( n -- not-equal-to-0? )</td>
</tr>
<tr>
<td>36</td>
<td>0&lt;</td>
<td>( n -- less-than-0? )</td>
</tr>
<tr>
<td>37</td>
<td>0&lt;=</td>
<td>( n -- less-or-equal-to-0? )</td>
</tr>
<tr>
<td>38</td>
<td>0&gt;</td>
<td>( n -- greater-than-0? )</td>
</tr>
<tr>
<td>39</td>
<td>0&gt;=</td>
<td>( n -- greater-or-equal-to-0? )</td>
</tr>
<tr>
<td>3A</td>
<td>&lt;</td>
<td>( n1 n2 -- less? )</td>
</tr>
<tr>
<td>3B</td>
<td>&gt;</td>
<td>( n1 n2 -- greater? )</td>
</tr>
<tr>
<td>3C</td>
<td>=</td>
<td>( x1 x2 -- equal? )</td>
</tr>
<tr>
<td>3D</td>
<td>&lt;&gt;</td>
<td>( x1 x2 -- not-equal? )</td>
</tr>
<tr>
<td>3E</td>
<td>u&gt;</td>
<td>( u1 u2 -- unsigned-greater? )</td>
</tr>
<tr>
<td>3F</td>
<td>u&lt;=</td>
<td>( u1 u2 -- unsigned-less-or-equal? )</td>
</tr>
<tr>
<td>40</td>
<td>u&lt;</td>
<td>( u1 u2 -- unsigned-less? )</td>
</tr>
<tr>
<td>41</td>
<td>u&gt;=</td>
<td>( u1 u2 -- unsigned-greater-or-equal? )</td>
</tr>
<tr>
<td>42</td>
<td>&gt;=</td>
<td>( n1 n2 -- greater-or-equal? )</td>
</tr>
<tr>
<td>43</td>
<td>&lt;=</td>
<td>( n1 n2 -- less-or-equal? )</td>
</tr>
<tr>
<td>44</td>
<td>between</td>
<td>( n min max -- min&lt;=n&lt;=max? )</td>
</tr>
<tr>
<td>45</td>
<td>within</td>
<td>( n min max -- min&lt;=n&lt;max? )</td>
</tr>
<tr>
<td>46</td>
<td>drop</td>
<td>( x -- )</td>
</tr>
<tr>
<td>47</td>
<td>dup</td>
<td>( x -- x x )</td>
</tr>
<tr>
<td>48</td>
<td>over</td>
<td>( x1 x2 -- x1 x2 x1 )</td>
</tr>
</tbody>
</table>
Table A-48 FCodes by Byte Value (Continued)

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
</tr>
</thead>
<tbody>
<tr>
<td>49</td>
<td>swap</td>
<td>( x1 x2 -- x2 x1 )</td>
</tr>
<tr>
<td>4A</td>
<td>rot</td>
<td>( x1 x2 x3 -- x2 x3 x1 )</td>
</tr>
<tr>
<td>4B</td>
<td>-rot</td>
<td>( x1 x2 x3 -- x3 x1 x2 )</td>
</tr>
<tr>
<td>4C</td>
<td>tuck</td>
<td>( x1 x2 -- x2 x1 x2 )</td>
</tr>
<tr>
<td>4D</td>
<td>nip</td>
<td>( x1 x2 -- x2 )</td>
</tr>
<tr>
<td>4E</td>
<td>pick</td>
<td>( xu ... x1 x0 u -- xu ... x1 x0 xu )</td>
</tr>
<tr>
<td>4F</td>
<td>roll</td>
<td>( xu ... x1 x0 u -- xu-1 ... x1 x0 xu )</td>
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<td>50</td>
<td>?dup</td>
<td>( x -- 0</td>
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<tr>
<td>51</td>
<td>depth</td>
<td>( -- u )</td>
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<td>52</td>
<td>2drop</td>
<td>( x1 x2 -- )</td>
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<tr>
<td>53</td>
<td>2dup</td>
<td>( x1 x2 -- x1 x2 x1 x2 )</td>
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<td>54</td>
<td>2over</td>
<td>( x1 x2 x3 x4 -- x1 x2 x3 x4 x1 x2 )</td>
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<td>55</td>
<td>2swap</td>
<td>( x1 x2 x3 x4 -- x3 x4 x1 x2 )</td>
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<td>56</td>
<td>2rot</td>
<td>( x1 x2 x3 x4 x5 x6 -- x3 x4 x5 x6 x1 x2 )</td>
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<tr>
<td>57</td>
<td>/</td>
<td>( x1 -- x2 )</td>
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<tr>
<td>58</td>
<td>u2/</td>
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<tr>
<td>59</td>
<td>2*</td>
<td>( x1 -- x2 )</td>
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<tr>
<td>5A</td>
<td>/c</td>
<td>( -- n )</td>
</tr>
<tr>
<td>5B</td>
<td>/w</td>
<td>( -- n )</td>
</tr>
<tr>
<td>5C</td>
<td>/l</td>
<td>( -- n )</td>
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<td>5D</td>
<td>/n</td>
<td>( -- n )</td>
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<tr>
<td>5E</td>
<td>ca+</td>
<td>( addr1 index -- addr2 )</td>
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<tr>
<td>5F</td>
<td>wa+</td>
<td>( addr1 index -- addr2 )</td>
</tr>
<tr>
<td>60</td>
<td>la+</td>
<td>( addr1 index -- addr2 )</td>
</tr>
<tr>
<td>61</td>
<td>na+</td>
<td>( addr1 index -- addr2 )</td>
</tr>
<tr>
<td>62</td>
<td>char+</td>
<td>( addr1 -- addr2 )</td>
</tr>
<tr>
<td>63</td>
<td>wai+</td>
<td>( addr1 -- addr2 )</td>
</tr>
<tr>
<td>64</td>
<td>lai+</td>
<td>( addr1 -- addr2 )</td>
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<tr>
<td>65</td>
<td>cell+</td>
<td>( addr1 -- addr2 )</td>
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<tr>
<td>66</td>
<td>chars</td>
<td>( nu1 -- nu2 )</td>
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<tr>
<td>67</td>
<td>/w*</td>
<td>( nu1 -- nu2 )</td>
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<td>/1*</td>
<td>(nu1 -- nu2)</td>
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<tr>
<td>69</td>
<td>cells</td>
<td>(nu1 -- nu2)</td>
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<td>6A</td>
<td>on</td>
<td>(a-addr --)</td>
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<td>6B</td>
<td>off</td>
<td>(a-addr --)</td>
</tr>
<tr>
<td>6C</td>
<td>+!</td>
<td>(nu a-addr --)</td>
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<tr>
<td>6D</td>
<td>@</td>
<td>(a-addr -- x)</td>
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<tr>
<td>6E</td>
<td>l@</td>
<td>(qaddr -- quad)</td>
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<tr>
<td>6F</td>
<td>w@</td>
<td>(waddr -- w)</td>
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<tr>
<td>70</td>
<td>&lt;w@</td>
<td>(waddr -- n)</td>
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<tr>
<td>71</td>
<td>c@</td>
<td>(addr -- byte)</td>
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<tr>
<td>72</td>
<td>!</td>
<td>(x a-addr --)</td>
</tr>
<tr>
<td>73</td>
<td>l!</td>
<td>(quad qaddr --)</td>
</tr>
<tr>
<td>74</td>
<td>w!</td>
<td>(w waddr --)</td>
</tr>
<tr>
<td>75</td>
<td>c!</td>
<td>(byte addr --)</td>
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<tr>
<td>76</td>
<td>z@</td>
<td>(a-addr -- x1 x2)</td>
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<td>77</td>
<td>z!</td>
<td>(x1 x2 a-addr --)</td>
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<td>move</td>
<td>(src-addr dest-addr len --)</td>
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<td>79</td>
<td>fill</td>
<td>(addr len byte --)</td>
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<td>7A</td>
<td>comp</td>
<td>(addr1 addr2 len -- n)</td>
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<td>7B</td>
<td>noop</td>
<td>(--)</td>
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<td>7C</td>
<td>lwsplit</td>
<td>(quad -- w1.lo w2.hi)</td>
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<td>7D</td>
<td>wlojoin</td>
<td>(w.lo w.hi -- quad)</td>
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<tr>
<td>7E</td>
<td>lbsplit</td>
<td>(quad -- b.lo b2 b3 b4.hi)</td>
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<td>b4join</td>
<td>(b1.lo b2 b3 b4.hi -- quad)</td>
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<td>wbflip</td>
<td>(w1 -- w2)</td>
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<td>81</td>
<td>upc</td>
<td>(char1 -- char2)</td>
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<tr>
<td>82</td>
<td>lcc</td>
<td>(char1 -- char2)</td>
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<tr>
<td>83</td>
<td>pack</td>
<td>(str len addr -- pstr)</td>
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<td>84</td>
<td>count</td>
<td>(pstr -- str len)</td>
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<tr>
<td>85</td>
<td>body&gt;</td>
<td>(a-addr -- xt)</td>
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<tr>
<td>86</td>
<td>&gt;body</td>
<td>(xt -- a-addr)</td>
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### Table A-48 FCodes by Byte Value (Continued)

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<td>87</td>
<td>fcode-revision</td>
<td>( -- n )</td>
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<td>88</td>
<td>span</td>
<td>( -- a-addr )</td>
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<tr>
<td>89</td>
<td>unloop</td>
<td>( -- ) ( R: sys -- )</td>
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<td>8A</td>
<td>expect</td>
<td>( addr len -- )</td>
</tr>
<tr>
<td>8B</td>
<td>alloc-mem</td>
<td>( len -- a-addr )</td>
</tr>
<tr>
<td>8C</td>
<td>free-mem</td>
<td>( a-addr len -- )</td>
</tr>
<tr>
<td>8D</td>
<td>key?</td>
<td>( -- pressed? )</td>
</tr>
<tr>
<td>8E</td>
<td>key</td>
<td>( -- char )</td>
</tr>
<tr>
<td>8F</td>
<td>emit</td>
<td>( char -- )</td>
</tr>
<tr>
<td>90</td>
<td>type</td>
<td>( text-str text-len -- )</td>
</tr>
<tr>
<td>91</td>
<td>(cr</td>
<td>( -- )</td>
</tr>
<tr>
<td>92</td>
<td>cr</td>
<td>( -- )</td>
</tr>
<tr>
<td>93</td>
<td>#out</td>
<td>( -- a-addr )</td>
</tr>
<tr>
<td>94</td>
<td>#line</td>
<td>( -- a-addr )</td>
</tr>
<tr>
<td>95</td>
<td>hold</td>
<td>( char -- )</td>
</tr>
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<td>96</td>
<td>&lt;#</td>
<td>( -- )</td>
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<tr>
<td>97</td>
<td>u#&gt;</td>
<td>( u -- str len )</td>
</tr>
<tr>
<td>98</td>
<td>sign</td>
<td>( n -- )</td>
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<tr>
<td>99</td>
<td>u#</td>
<td>( u1 -- u2 )</td>
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<td>9A</td>
<td>u#s</td>
<td>( u1 -- u2 )</td>
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<td>u.</td>
<td>( u -- )</td>
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<td>9C</td>
<td>u.r</td>
<td>( u size -- )</td>
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<td>9D</td>
<td>.</td>
<td>( nu -- )</td>
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<tr>
<td>9E</td>
<td>.r</td>
<td>( n size -- )</td>
</tr>
<tr>
<td>9F</td>
<td>.s</td>
<td>( ... -- ... )</td>
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<tr>
<td>A0</td>
<td>base</td>
<td>( -- a-addr )</td>
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<tr>
<td>A2</td>
<td>$number</td>
<td>( addr len -- true</td>
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<tr>
<td>A3</td>
<td>digit</td>
<td>( char base -- digit true</td>
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<tr>
<td>A4</td>
<td>-1</td>
<td>( -- -1 )</td>
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<tr>
<td>A5</td>
<td>0</td>
<td>( -- 0 )</td>
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<tr>
<td>A6</td>
<td>1</td>
<td>( -- 1 )</td>
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Table A-48 FCodes by Byte Value (Continued)

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<tr>
<th>Value</th>
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<th>Stack</th>
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<tr>
<td>A7</td>
<td>2</td>
<td>( ~ 2 )</td>
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<tr>
<td>A8</td>
<td>3</td>
<td>( ~ 3 )</td>
</tr>
<tr>
<td>A9</td>
<td>bl</td>
<td>( ~ 0x20 )</td>
</tr>
<tr>
<td>AA</td>
<td>bs</td>
<td>( ~ 0x08 )</td>
</tr>
<tr>
<td>AB</td>
<td>bell</td>
<td>( ~ 0x07 )</td>
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<tr>
<td>AC</td>
<td>bounds</td>
<td>( n cnt -- n+cnt n )</td>
</tr>
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<td>AD</td>
<td>here</td>
<td>( ~ addr )</td>
</tr>
<tr>
<td>AE</td>
<td>aligned</td>
<td>( n1 -- n1</td>
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<tr>
<td>AF</td>
<td>wbsplit</td>
<td>( w -- b1.lo b2.hi )</td>
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<tr>
<td>B0</td>
<td>bwjoin</td>
<td>( b.lo b.hi -- w )</td>
</tr>
<tr>
<td>B1</td>
<td>b(&lt;mark)</td>
<td>( F: ~ )</td>
</tr>
<tr>
<td>B2</td>
<td>b(&gt;resolve)</td>
<td>( ~ ) ( F: ~ )</td>
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<tr>
<td>B5</td>
<td>new-token</td>
<td>( ~ ) ( F: /FCode#/ ~ )</td>
</tr>
<tr>
<td>B6</td>
<td>named-token</td>
<td>( ~ ) ( F: /FCode-string FCode#/ ~ )</td>
</tr>
<tr>
<td>B7</td>
<td>b(;)</td>
<td>( E: ... ~ ??? ) ( F: -- colon-sys )</td>
</tr>
<tr>
<td>B8</td>
<td>b(value)</td>
<td>( E: -- x ) ( F: x -- )</td>
</tr>
<tr>
<td>B9</td>
<td>b(variable)</td>
<td>( E: -- a-addr ) ( F: ~ )</td>
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<tr>
<td>BA</td>
<td>b(constant)</td>
<td>( E: -- n ) ( F: n -- )</td>
</tr>
<tr>
<td>BB</td>
<td>b(create)</td>
<td>( E: -- a-addr ) ( F: ~ )</td>
</tr>
<tr>
<td>BC</td>
<td>b(defer)</td>
<td>( E: ... ~ ??? ) ( F: ~ )</td>
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<tr>
<td>BD</td>
<td>b(buffer:)</td>
<td>( E: -- a-addr ) ( F: size -- )</td>
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<tr>
<td>BE</td>
<td>b(field)</td>
<td>( E: addr -- addr+offset ) ( F: offset size -- offset+size )</td>
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<tr>
<td>C0</td>
<td>instance</td>
<td>( ~ )</td>
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<tr>
<td>C2</td>
<td>b(;)</td>
<td>( ~ ) ( F: colon-sys -- )</td>
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<tr>
<td>C3</td>
<td>b(to)</td>
<td>( x ~ )</td>
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<tr>
<td>C4</td>
<td>b(case)</td>
<td>( sel -- sel ) ( F: ~ )</td>
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<td>C5</td>
<td>b(endcase)</td>
<td>( sel</td>
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<tr>
<td>C6</td>
<td>b(endif)</td>
<td>( ~ ) ( F: /FCode-offset/ ~ )</td>
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<tr>
<td>C7</td>
<td>#</td>
<td>( ud1 -- ud2 )</td>
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<tr>
<td>C8</td>
<td>#s</td>
<td>( ud -- 0 0 )</td>
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<td>C9</td>
<td>#&gt;</td>
<td>( ud -- str len )</td>
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Table A-48 FCodes by Byte Value (Continued)

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<td>CA</td>
<td>external-token</td>
<td>⟨ ⟨ ⟩ ( F: /FCode-string FCode#/ -- )</td>
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<td>CB</td>
<td>$find</td>
<td>⟨ name-str name-len -- xt true</td>
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<tr>
<td>CC</td>
<td>offset16</td>
<td>⟨ ⟩</td>
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<td>CD</td>
<td>evaluate</td>
<td>⟨ ... str len -- ??? ⟩</td>
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<td>D0</td>
<td>c,</td>
<td>⟨ byte -- ⟩</td>
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<td>w,</td>
<td>⟨ w ~ ⟩</td>
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<tr>
<td>D2</td>
<td>l,</td>
<td>⟨ quad -- ⟩</td>
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<td>D3</td>
<td>,</td>
<td>⟨ x ~ ⟩</td>
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<td>D4</td>
<td>um*</td>
<td>⟨ u1 u2 -- ud.prod ⟩</td>
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<td>D5</td>
<td>um/mod</td>
<td>⟨ ud u -- urem uquot ⟩</td>
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<td>D8</td>
<td>d+</td>
<td>⟨ d1 d2 --d.sum ⟩</td>
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<tr>
<td>D9</td>
<td>d-</td>
<td>⟨ d1 d2 -- d.diff ⟩</td>
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<td>get-token</td>
<td>⟨ fcode# -- xt immediate? ⟩</td>
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<td>DB</td>
<td>set-token</td>
<td>⟨ xt immediate? fcode# -- ⟩</td>
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<td>state</td>
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<td>compile,</td>
<td>⟨ xt ~ ⟩</td>
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<td>behavior</td>
<td>⟨ defer-xt -- contents-xt ⟩</td>
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<td>start0</td>
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<td>start1</td>
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<td>start2</td>
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<td>start4</td>
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<td>version1</td>
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<td>my-address</td>
<td>⟨ -- phys.lo ... ⟩</td>
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<td>0103</td>
<td>my-space</td>
<td>⟨ -- phys.hi ⟩</td>
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<td>free-virtual</td>
<td>⟨ virt size -- ⟩</td>
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<td>0110</td>
<td>property</td>
<td>⟨ prop-addr prop-len name-str name-len -- ⟩</td>
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<td>encode-int</td>
<td>⟨ n -- prop-addr prop-len ⟩</td>
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<td>0112</td>
<td>encode+</td>
<td>⟨ prop-addr1 prop-len1 prop-addr2 prop-len2 -- prop-addr3 prop-len3 ⟩</td>
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<td>0113</td>
<td>encode-phys</td>
<td>⟨ phys.lo ... phys.hi -- prop-addr prop-len ⟩</td>
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<td><code>(str len -- prop-addr prop-len)</code></td>
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<td>0015</td>
<td><code>encode-bytes</code></td>
<td><code>(data-addr data-len -- prop-addr prop-len)</code></td>
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<td>0016</td>
<td><code>reg</code></td>
<td><code>(phys.lo ... phys.mi size --)</code></td>
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<td>0019</td>
<td><code>model</code></td>
<td><code>(str len --)</code></td>
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<td>001A</td>
<td><code>device-type</code></td>
<td><code>(str len --)</code></td>
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<td>001B</td>
<td><code>parse-2int</code></td>
<td><code>(str len -- val.lo val.mi)</code></td>
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<td>001C</td>
<td><code>is-install</code></td>
<td><code>(xt --)</code></td>
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<td>001D</td>
<td><code>is-remove</code></td>
<td><code>(xt --)</code></td>
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<td><code>is-selftest</code></td>
<td><code>(xt --)</code></td>
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<td>001F</td>
<td><code>new-device</code></td>
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<td><code>diagnostic-mode?</code></td>
<td><code>()</code></td>
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<td>0021</td>
<td><code>display-status</code></td>
<td><code>(n --)</code></td>
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<td><code>memory-test-suite</code></td>
<td><code>(addr len -- fail?)</code></td>
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<td><code>mask</code></td>
<td><code>()</code></td>
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<td>0025</td>
<td><code>get-msecs</code></td>
<td><code>(n --)</code></td>
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<td>0026</td>
<td><code>ms</code></td>
<td><code>(n --)</code></td>
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<td>0027</td>
<td><code>finish-device</code></td>
<td><code>()</code></td>
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<td>0028</td>
<td><code>decode-phys</code></td>
<td><code>(prop-addr1 prop-len1 -- prop-addr2 prop-len2 phys.lo ... phys.mi)</code></td>
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<td><code>push-package</code></td>
<td><code>(phandle --)</code></td>
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<td>002A</td>
<td><code>pop-package</code></td>
<td><code>()</code></td>
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<td>002B</td>
<td><code>interpose</code></td>
<td><code>(adr len phandle --)</code></td>
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<td><code>reset-screen</code></td>
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<td>my-self</td>
<td>(– ihandle)</td>
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<td>0206</td>
<td>close-package</td>
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<td>find-method</td>
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<td>call-package</td>
<td>(… xt ihandle -- ???)</td>
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<td>ihandle&gt;phandle</td>
<td>(ihandle -- phandle)</td>
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<td>my-unit</td>
<td>(– phys.lo … phys.hi)</td>
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<td>(… method-str method-len ihandle -- ???)</td>
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<td>(arg-str arg-len name-str name-len -- ihandle</td>
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<td>alarm</td>
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<td>0214</td>
<td>(is-user-word)</td>
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<td>throw</td>
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<td>child</td>
<td>( phandle.parent -- phandle.child )</td>
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<td>( phandle -- phandle.sibling )</td>
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<td>(&quot;old-name&quot;&quot; &lt; &gt; xt )</td>
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<td>( n -- str len )</td>
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<td>control</td>
<td>([text&lt; &gt;] -- char )</td>
</tr>
<tr>
<td>CR</td>
<td>create</td>
<td>( E: -- a-addr ) ( &quot;new-name&lt; &gt;&quot; -- )</td>
</tr>
<tr>
<td>CR</td>
<td>d#</td>
<td>([number&lt; &gt;] -- n )</td>
</tr>
<tr>
<td>CR</td>
<td>.d</td>
<td>( n -- )</td>
</tr>
<tr>
<td>CR</td>
<td>decimal</td>
<td>( -- )</td>
</tr>
<tr>
<td>CR</td>
<td>decode-bytes</td>
<td>( prop-addr1 prop-len1 data-len -- prop-addr2 prop-len2 data-addr data-len )</td>
</tr>
<tr>
<td>CR</td>
<td>defer</td>
<td>( E: ... -- ??? ) ( &quot;new-name&lt; &gt;&quot; -- )</td>
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<tr>
<td>CR</td>
<td>do</td>
<td>( C: -- dodest-sys ) ( limit start -- ) ( R: -- sys )</td>
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<td>CR</td>
<td>?do</td>
<td>( C: -- dodest-sys ) ( limit start -- ) ( R: -- sys )</td>
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<tr>
<td>CR</td>
<td>3drop</td>
<td>( x1 x2 x3 -- )</td>
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<tr>
<td>CR</td>
<td>3dup</td>
<td>( x1 x2 x3 -- x1 x2 x3 x1 x2 x3 )</td>
</tr>
<tr>
<td>CR</td>
<td>else</td>
<td>( C: orig-sys1 -- orig-sys2 ) ( -- )</td>
</tr>
<tr>
<td>CR</td>
<td>endcase</td>
<td>( C: case-sys -- ) ( sel</td>
</tr>
<tr>
<td>CR</td>
<td>endif</td>
<td>( C: case-sys1 of-sys -- case-sys2 ) ( -- )</td>
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<tr>
<td>CR</td>
<td>erase</td>
<td>( addr len -- )</td>
</tr>
<tr>
<td>CR</td>
<td>eval</td>
<td>( ... str len -- ??? )</td>
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<tr>
<td>CR</td>
<td>false</td>
<td>( -- false )</td>
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<td>CR</td>
<td>fcode-version2</td>
<td>( -- )</td>
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<td>CR</td>
<td>field</td>
<td>( E: addr -- addr+offset ) ( offset size &quot;new-name&lt; &gt;&quot; -- offset+size )</td>
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<tr>
<td>CR</td>
<td>h#</td>
<td>([number&lt; &gt;] -- n )</td>
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<td>CR</td>
<td>.h</td>
<td>( n -- )</td>
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<tr>
<td>CR</td>
<td>hex</td>
<td>( -- )</td>
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<tr>
<td>CR</td>
<td>if</td>
<td>( C: -- orig-sys ) ( do-next? -- )</td>
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<tr>
<td>CR</td>
<td>leave</td>
<td>( -- ) ( R: sys -- )</td>
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<tr>
<td>CR</td>
<td>?leave</td>
<td>( exit? -- ) ( R: sys -- )</td>
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<td>CR</td>
<td>linefeed</td>
<td>( -- 0x0A )</td>
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<td>CR</td>
<td>loop</td>
<td>( C: dodest-sys -- ) ( -- ) ( R: sys1 -- &lt;nothing&gt;</td>
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<td>+loop</td>
<td>( C: dodest-sys -- ) ( delta -- ) ( R: sys1 -- &lt;nothing&gt;</td>
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<td>Value</td>
<td>Function</td>
<td>Stack</td>
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<td>-------</td>
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<td>-------</td>
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<tr>
<td>CR</td>
<td>/n*</td>
<td>( nu1 -- nu2 )</td>
</tr>
<tr>
<td>CR</td>
<td>nal+</td>
<td>( addr1 -- addr2 )</td>
</tr>
<tr>
<td>CR</td>
<td>not</td>
<td>( x1 -- x2 )</td>
</tr>
<tr>
<td>CR</td>
<td>o#</td>
<td>( [number&lt; &gt;] -- n )</td>
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<tr>
<td>CR</td>
<td>octal</td>
<td>( -- )</td>
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<tr>
<td>CR</td>
<td>of</td>
<td>( C: case-sys1 -- case-sys2 of-sys ) ( sel of-val -- sel</td>
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<tr>
<td>CR</td>
<td>repeat</td>
<td>( C: orig-sys dest-sys -- ) ( -- )</td>
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<td>CR</td>
<td>s&quot;</td>
<td>( [text&lt;&quot;&gt;] -- test-str text-len )</td>
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<tr>
<td>CR</td>
<td>s.</td>
<td>( n -- )</td>
</tr>
<tr>
<td>CR</td>
<td>space</td>
<td>( -- )</td>
</tr>
<tr>
<td>CR</td>
<td>spaces</td>
<td>( cnt -- )</td>
</tr>
<tr>
<td>CR</td>
<td>struct</td>
<td>( -- 0 )</td>
</tr>
<tr>
<td>CR</td>
<td>then</td>
<td>( C: orig-sys -- ) ( -- )</td>
</tr>
<tr>
<td>CR</td>
<td>to</td>
<td>( param [old-name&lt; &gt;] -- )</td>
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<tr>
<td>CR</td>
<td>true</td>
<td>( -- true )</td>
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<tr>
<td>CR</td>
<td>(u,)</td>
<td>( u -- str len )</td>
</tr>
<tr>
<td>CR</td>
<td>until</td>
<td>( C: dest-sys -- ) ( done? -- )</td>
</tr>
<tr>
<td>CR</td>
<td>value</td>
<td>( E: -- x) ( x &quot;new-name&lt; &gt;&quot;-- )</td>
</tr>
<tr>
<td>CR</td>
<td>variable</td>
<td>( E: -- a-addr ) (&quot;new-name&lt; &gt;&quot;-- )</td>
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<tr>
<td>CR</td>
<td>while</td>
<td>( C: dest-sys -- orig-sys dest-sys ) ( continue? -- )</td>
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## FCodes by Name

The following table lists, in alphabetic order, currently-assigned FCodes.

Table A-49 Fcodes by Name

<table>
<thead>
<tr>
<th>Value</th>
<th>Function</th>
<th>Stack</th>
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<tr>
<td>72</td>
<td>!</td>
<td>( x a-addr -- )</td>
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<tr>
<td>CR</td>
<td>&quot;</td>
<td>( [text&lt;&quot;&gt;]&lt; &gt;] -- text text-len )</td>
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<tr>
<td>C7</td>
<td>#</td>
<td>( ud1 -- ud2 )</td>
</tr>
<tr>
<td>C9</td>
<td>#&gt;</td>
<td>( ud -- str-len )</td>
</tr>
<tr>
<td>CR</td>
<td>'</td>
<td>( &quot;old-name&lt; &gt;&quot; -- xt )</td>
</tr>
<tr>
<td>-</td>
<td></td>
<td>( [text&lt;]&gt; -- )</td>
</tr>
<tr>
<td>CR</td>
<td>( . )</td>
<td>( n -- str-len )</td>
</tr>
<tr>
<td>20</td>
<td>*</td>
<td>( nu1 nu2 -- prod )</td>
</tr>
<tr>
<td>1E</td>
<td>+</td>
<td>( nu1 nu2 -- sum )</td>
</tr>
<tr>
<td>6C</td>
<td>+!</td>
<td>( nu a-addr -- )</td>
</tr>
<tr>
<td>D3</td>
<td>,</td>
<td>( x -- )</td>
</tr>
<tr>
<td>1F</td>
<td>-</td>
<td>( nu1 nu2 -- diff )</td>
</tr>
<tr>
<td>9D</td>
<td>.</td>
<td>( nu -- )</td>
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<tr>
<td>CR</td>
<td>:&quot;</td>
<td>( [text&lt;]&gt; ] -- )</td>
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<tr>
<td>CR</td>
<td>.(.)</td>
<td>( [text&lt;]&gt; ] -- )</td>
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<td>21</td>
<td>/</td>
<td>( n1 n2 -- quot )</td>
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<tr>
<td>CR</td>
<td>: (colon)</td>
<td>( &quot;new-name&lt; &gt;&quot; -- colon-sys ) ( E: ... -- ?? )</td>
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<tr>
<td>CR</td>
<td>; (semicolon)</td>
<td>( -- )</td>
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<tr>
<td>3A</td>
<td>&lt;</td>
<td>( n1 n2 -- less? )</td>
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<tr>
<td>96</td>
<td>&lt;=</td>
<td>( n1 n2 -- less-or-equal? )</td>
</tr>
<tr>
<td>CR</td>
<td>&lt;&lt;</td>
<td>( x1 u -- x2 )</td>
</tr>
<tr>
<td>43</td>
<td>&lt;=</td>
<td>( n1 n2 -- less-or-equal? )</td>
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<tr>
<td>3D</td>
<td>=&gt;</td>
<td>30</td>
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<tr>
<td>3C</td>
<td>=</td>
<td>( n1 n2 -- greater? )</td>
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<tr>
<td>0B</td>
<td>&gt;</td>
<td>( n1 n2 -- greater? )</td>
</tr>
<tr>
<td>42</td>
<td>&gt;=</td>
<td>( n1 n2 -- greater-or-equal? )</td>
</tr>
<tr>
<td>CR</td>
<td>&gt;&gt;</td>
<td>( x1 u -- x2 )</td>
</tr>
<tr>
<td>CR</td>
<td>?</td>
<td>( addr -- )</td>
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<td>Value</td>
<td>Function</td>
<td>Stack</td>
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<td>-------</td>
<td>----------</td>
<td>-------</td>
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<tr>
<td>6D</td>
<td>@</td>
<td>( a-addr -- x )</td>
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<tr>
<td>CR</td>
<td>[']</td>
<td>( [old-name&lt; &gt;] -- xt )</td>
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<tr>
<td>-</td>
<td>\</td>
<td>( -- )</td>
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<tr>
<td>-</td>
<td>tokenizer</td>
<td>( -- )</td>
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<tr>
<td>A5</td>
<td>0</td>
<td>( -- 0 )</td>
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<tr>
<td>36</td>
<td>0&lt;</td>
<td>( n -- less-than-0? )</td>
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<tr>
<td>37</td>
<td>0&lt;=</td>
<td>( n -- less-or-equal-to-0? )</td>
</tr>
<tr>
<td>35</td>
<td>0&lt;&gt;</td>
<td>( n -- not-equal-to-0? )</td>
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<tr>
<td>34</td>
<td>0=</td>
<td>( nulflag -- equal-to-0? )</td>
</tr>
<tr>
<td>38</td>
<td>0&gt;</td>
<td>( n -- greater-than-0? )</td>
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<tr>
<td>39</td>
<td>0&gt;=</td>
<td>( n -- greater-or-equal-to-0? )</td>
</tr>
<tr>
<td>A6</td>
<td>1</td>
<td>( -- 1 )</td>
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<tr>
<td>CR</td>
<td>1+</td>
<td>( nu1 -- nu2 )</td>
</tr>
<tr>
<td>CR</td>
<td>1-</td>
<td>( nu1 -- nu2 )</td>
</tr>
<tr>
<td>A4</td>
<td>-1</td>
<td>( -- -1 )</td>
</tr>
<tr>
<td>A7</td>
<td>2</td>
<td>( -- 2 )</td>
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<tr>
<td>77</td>
<td>2!</td>
<td>( x1 x2 a-addr -- )</td>
</tr>
<tr>
<td>59</td>
<td>2*</td>
<td>( x1 -- x2 )</td>
</tr>
<tr>
<td>CR</td>
<td>2+</td>
<td>( nu1 -- nu2 )</td>
</tr>
<tr>
<td>CR</td>
<td>2-</td>
<td>( nu1 -- nu2 )</td>
</tr>
<tr>
<td>57</td>
<td>2/</td>
<td>( x1 -- x2 )</td>
</tr>
<tr>
<td>76</td>
<td>2@</td>
<td>( a-addr -- x1 x2 )</td>
</tr>
<tr>
<td>A8</td>
<td>3</td>
<td>( -- 3 )</td>
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<tr>
<td>29</td>
<td>&gt;&gt;a</td>
<td>( x1 u -- x2 )</td>
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<tr>
<td>0216</td>
<td>abort</td>
<td>( ... -- ) (R:... -- )</td>
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<tr>
<td>2D</td>
<td>abs</td>
<td>( n -- u )</td>
</tr>
<tr>
<td>CR</td>
<td>accept</td>
<td>( addr len1 -- len2 )</td>
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<tr>
<td>CR</td>
<td>again</td>
<td>( C: dest-sys -- )</td>
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<tr>
<td>0213</td>
<td>alarm</td>
<td>( xt n -- )</td>
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<tr>
<td>-</td>
<td>alias</td>
<td>( E: ... -- ???) ( “new-name&lt; &gt;old-name&lt; &gt;” -- )</td>
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<tr>
<td>AE</td>
<td>aligned</td>
<td>( n1 -- n1</td>
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### Table A-49 Fcodes by Name (Continued)

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<td>alloc-mem</td>
<td>( len -- a-addr )</td>
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<td>CR</td>
<td>allot</td>
<td>( len -- )</td>
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<tr>
<td>23</td>
<td>and</td>
<td>( x1 x2 -- x3 )</td>
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<tr>
<td>CR</td>
<td>ascii</td>
<td>( [text&lt;&gt;] -- char )</td>
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<tr>
<td>12</td>
<td>b(*)</td>
<td>( -- str len ) ( F: /FCode-string/ -- )</td>
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<tr>
<td>11</td>
<td>b(')</td>
<td>( -- xt ) ( F: /FCode#/ -- )</td>
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<tr>
<td>B7</td>
<td>b(:)</td>
<td>( E: ... = ??? ) ( F: -- colon-sys )</td>
</tr>
<tr>
<td>C2</td>
<td>b(; )</td>
<td>( ) ( F: colon-sys -- )</td>
</tr>
<tr>
<td>A0</td>
<td>base</td>
<td>( -- a-addr )</td>
</tr>
<tr>
<td>13</td>
<td>b?branch</td>
<td>( -- ) ( F: /FCode-offset/ -- )</td>
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<tr>
<td>14</td>
<td>b?branch</td>
<td>( don't-branch? -- ) ( F: /FCode-offset/ -- )</td>
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<tr>
<td>BD</td>
<td>b(buffer:)</td>
<td>( E: -- a-addr ) ( F: size -- )</td>
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<tr>
<td>C4</td>
<td>b(case)</td>
<td>( sel -- sel ) ( F: -- )</td>
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<tr>
<td>BA</td>
<td>b(constant)</td>
<td>( E: -- n ) ( F: n -- )</td>
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<tr>
<td>BB</td>
<td>b(create)</td>
<td>( E: -- a-addr ) ( F: -- )</td>
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<tr>
<td>BC</td>
<td>b(defer)</td>
<td>( E: ... = ??? ) ( F: -- )</td>
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<td>17</td>
<td>b(do)</td>
<td>( limit start -- ) ( F: /FCode-offset/ -- )</td>
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<tr>
<td>18</td>
<td>b(?do)</td>
<td>( limit start -- ) ( F: /FCode-offset/ -- )</td>
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<tr>
<td>CR</td>
<td>begin</td>
<td>( C: -- dest-sys ) ( -- )</td>
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<tr>
<td>DE</td>
<td>behavior</td>
<td>( defer-xt -- contents-xt )</td>
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<td>AB</td>
<td>bell</td>
<td>( -- 0x07 )</td>
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<td>C5</td>
<td>b(endcase)</td>
<td>( sel</td>
</tr>
<tr>
<td>C6</td>
<td>b(endof)</td>
<td>( -- ) ( F: /FCode-offset/ -- )</td>
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<td>44</td>
<td>between</td>
<td>( n min max -- min&lt;=n&lt;=max? )</td>
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<tr>
<td>BE</td>
<td>b(field)</td>
<td>( E: addr -- addr+offset ) ( F: offset size -- offset+size )</td>
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<td>A9</td>
<td>b1</td>
<td>( -- 0x20 )</td>
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<tr>
<td>CR</td>
<td>blank</td>
<td>( addr len -- )</td>
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<tr>
<td>1B</td>
<td>b(leave)</td>
<td>( F: -- )</td>
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<tr>
<td>015B</td>
<td>blink-screen</td>
<td>( -- )</td>
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<td>10</td>
<td>b(lit)</td>
<td>( -- n ) ( F: /FCode-num32/ -- )</td>
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<td>7F</td>
<td>bljoin</td>
<td>( bl,lo b2 b3 b4.hi -- quad )</td>
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### Table A-49 Fcodes by Name (Continued)

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<td>b(loop)</td>
<td>( -- ) ( F: /FCode-offset/ -- )</td>
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<td>16</td>
<td>b(+loop)</td>
<td>( delta -- ) ( F: /FCode-offset/ -- )</td>
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<td>B1</td>
<td>b(&lt;mark)</td>
<td>( F: -- )</td>
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<tr>
<td>85</td>
<td>body&gt;</td>
<td>( a-addr -- xt )</td>
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<tr>
<td>86</td>
<td>&gt;body</td>
<td>( xt -- a-addr )</td>
</tr>
<tr>
<td>1C</td>
<td>b(of)</td>
<td>( sel of-val -- sel</td>
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<tr>
<td>AC</td>
<td>bounds</td>
<td>( n cnt -- n+cnt n )</td>
</tr>
<tr>
<td>B2</td>
<td>b(resolve)</td>
<td>( -- ) ( F: -- )</td>
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<tr>
<td>AA</td>
<td>bs</td>
<td>( -- 0x08 )</td>
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<tr>
<td>C3</td>
<td>b(to)</td>
<td>( x -- )</td>
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<td>CR</td>
<td>buffer:</td>
<td>( E: -- a-addr ) ( len &quot;new-name&lt; &gt;&quot; -- )</td>
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<td>B8</td>
<td>b(value)</td>
<td>( E: -- x ) ( F: x -- )</td>
</tr>
<tr>
<td>B9</td>
<td>b(variable)</td>
<td>( E: -- a-addr ) ( F: -- )</td>
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<td>B0</td>
<td>bwjoin</td>
<td>( b.lo b.hi -- w )</td>
</tr>
<tr>
<td>02 41</td>
<td>bxjoin</td>
<td>( b.lo b.2 b.3 b.4 b.5 b.6 b.7 b.hi -- o )</td>
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<td>023E</td>
<td>byte-load</td>
<td>( addr xt -- )</td>
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<td>75</td>
<td>c!</td>
<td>( byte addr -- )</td>
</tr>
<tr>
<td>D0</td>
<td>c,</td>
<td>( byte -- )</td>
</tr>
<tr>
<td>5A</td>
<td>/c</td>
<td>( -- n )</td>
</tr>
<tr>
<td>-</td>
<td>/c*</td>
<td>( nu1 -- nu2 )</td>
</tr>
<tr>
<td>71</td>
<td>c@</td>
<td>( addr -- byte )</td>
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<tr>
<td>5E</td>
<td>ca+</td>
<td>( addr1 index -- addr2 )</td>
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<td>CR</td>
<td>cal+</td>
<td>( addr1 -- addr2 )</td>
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<td>62</td>
<td>char+</td>
<td>( addr1 -- addr2 )</td>
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<td>020E</td>
<td>$call-method</td>
<td>( ... method-str method-len ihandle -- ???? )</td>
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<td>0208</td>
<td>call-package</td>
<td>( ... xt ihandle -- ???? )</td>
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<tr>
<td>0209</td>
<td>$call-parent</td>
<td>( ... method-str method-len -- ???? )</td>
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<td>carret</td>
<td>( -- 0x0D )</td>
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<td>( d1 d2 -- d.diff )</td>
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<td>( prop-addr1 prop-len1 -- prop-addr2 prop-len2 n )</td>
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<td>( prop-addr1 prop-len1 -- prop-addr2 prop-len2 phys.lo ... phys.hi )</td>
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<td>( prop-addr1 prop-len1 -- prop-addr2 prop-len2 str len )</td>
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<td>( E: ... -- ??? ) ( &quot;new-name&lt; &gt;&quot; -- )</td>
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Table A-49 Fcodes by Name (Continued)

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<td>( char base -- digit true</td>
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<td>draw-logo</td>
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<td>drop</td>
<td>( x -- )</td>
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<td>2drop</td>
<td>( x1 x2 -- )</td>
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<td>3drop</td>
<td>( x1 x2 x3 -- )</td>
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<td>( x -- x x )</td>
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<td>( x1 x2 -- x1 x2 x1 x2 )</td>
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<td>( data-addr data-len -- prop-addr prop-len )</td>
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### Table A-49 Fcodes by Name (Continued)

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<td><code>( ... str len -- ???)</code></td>
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<td><code>evaluate</code></td>
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<td><code>( -- )</code></td>
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<td><code>external-token</code></td>
<td><code>( -- ) ( F: /FCode-string FCode#/ -- )</code></td>
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Table A-49 Fcodes by Name (Continued)

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## Table A-49 Fcodes by Name (Continued)

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<td>leave</td>
<td>( -- ) ( R: sys -- )</td>
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<td>?leave</td>
<td>( exit? -- ) ( R: sys -- )</td>
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<td>( str len char -- R-str R-len L-str L-len )</td>
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<td>( -- rows )</td>
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<td>( quad -- w1.lo w2.hi )</td>
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<td>( -- mac-str mac-len )</td>
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<td>my-args</td>
<td>( -- arg-str arg-len )</td>
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Table A-49 Fcodes by Name (Continued)

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<td>my-parent</td>
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<td>my-self</td>
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<td>my-unit</td>
<td>( -- phys.lo ... phys.hi )</td>
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<td>/n</td>
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<td>/n*</td>
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<td>( addr1 index -- addr2 )</td>
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<td>nal+</td>
<td>( addr1 -- addr2 )</td>
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<td>negate</td>
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<td>( -- )</td>
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<td>B5</td>
<td>new-token</td>
<td>( -- ) ( F: /FCode#/ -- )</td>
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<td>next-property</td>
<td>( previous-str previous-len phandle -- false</td>
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<td>nip</td>
<td>( x1 x2 -- x2 )</td>
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<td>noop</td>
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<td>( x1 -- x2 )</td>
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<td>open-package</td>
<td>( arg-str arg-len phandle -- ihandle</td>
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<td>$open-package</td>
<td>( arg-str arg-len name-str name-len -- ihandle</td>
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<td>24</td>
<td>or</td>
<td>( x1 x2 -- x3 )</td>
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<td>93</td>
<td>#out</td>
<td>( -- a-addr )</td>
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<td>48</td>
<td>over</td>
<td>( x1 x2 -- x1 x2 x1 )</td>
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<td>54</td>
<td>2over</td>
<td>( x1 x2 x3 x4 -- x1 x2 x3 x4 x1 x2 )</td>
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Table A-49 Fcodes by Name (Continued)

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<td>parse-2int</td>
<td>( str len -- val.lo val.hi )</td>
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<td>peer</td>
<td>( phandle -- phandle.sibling )</td>
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<td>4E</td>
<td>pick</td>
<td>( xu ... x1 x0 u -- xu ... x1 x0 xu )</td>
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<td>0110</td>
<td>property</td>
<td>( prop-addr prop-len name-str name-len -- )</td>
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<td>01 2A</td>
<td>pop-package</td>
<td>( -- )</td>
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<tr>
<td>01 29</td>
<td>push-package</td>
<td>( phandle -- )</td>
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<td>31</td>
<td>r&gt;</td>
<td>( -- x ) ( R: x -- )</td>
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<td>32</td>
<td>r@</td>
<td>( -- x ) ( R: x -- x )</td>
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<td>9E</td>
<td>.r</td>
<td>( n size -- )</td>
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<td>&gt;r</td>
<td>( x -- ) ( R: -- x )</td>
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<td>rb!</td>
<td>( byte addr -- )</td>
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<td>0230</td>
<td>rb@</td>
<td>( addr -- byte )</td>
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<td>0116</td>
<td>reg</td>
<td>( phys.lo ... phys.hi size -- )</td>
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<td>repeat</td>
<td>( C: orig-sys dest-sys -- ) ( -- )</td>
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<td>0158</td>
<td>reset-screen</td>
<td>( -- )</td>
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<td>rl!</td>
<td>( quad qaddr -- )</td>
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<td>0234</td>
<td>rl@</td>
<td>( qaddr -- quad )</td>
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<td>roll</td>
<td>( xu ... x1 x0 u -- xu-1 ... x1 x0 xu )</td>
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<td>rot</td>
<td>( x1 x2 x3 -- x2 x3 x1 )</td>
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<td>-rot</td>
<td>( x1 x2 x3 -- x3 x1 x2 )</td>
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<td>2rot</td>
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<td>rw@</td>
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<td>rx@</td>
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<td>s&quot;</td>
<td>( [text&lt;&quot;] -- test-str text-len )</td>
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<td>( ud -- 0 0 )</td>
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<td>.s</td>
<td>( ... -- ... )</td>
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### Table A-49 Fcodes by Name (Continued)

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<td><code>( sbus-intr# -- cpu-intr# )</code></td>
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<td><code>screen-height</code></td>
<td><code>( -- height )</code></td>
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<td><code>screen-width</code></td>
<td><code>( -- width )</code></td>
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<td><code>set-args</code></td>
<td><code>( arg-str arg-len unit-str unit-len -- )</code></td>
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<td><code>set-font</code></td>
<td><code>( addr width height advance min-char #glyphs -- )</code></td>
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<td><code>set-token</code></td>
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<td><code>( -- )</code></td>
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<td><code>start2</code></td>
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<td><code>start4</code></td>
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<td><code>state</code></td>
<td><code>( -- a-addr )</code></td>
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<td><code>struct</code></td>
<td><code>( -- 0 )</code></td>
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<td><code>suspend-fcode</code></td>
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<td><code>then</code></td>
<td><code>( C: orig-sys -- ) ( -- )</code></td>
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<td><code>( param [old-name&lt; &gt;] -- )</code></td>
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<td><code>( u -- str len )</code></td>
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<td><code>u#s</code></td>
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<td><code>u.</code></td>
<td><code>( u -- )</code></td>
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<td>( u1 u2 -- unsigned-less-or-equal? )</td>
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<td>( x1 -- x2 )</td>
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<td>um*</td>
<td>( u1 u2 -- ud.prod )</td>
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<td>um/mod</td>
<td>( ud u -- urem uquot )</td>
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<td>( u1 u2 -- urem uquot )</td>
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<td>u.r</td>
<td>( u size -- )</td>
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<td>w@</td>
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<td>( w -- b1.lo b2.hi )</td>
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<td>( C: dest-sys -- orig-sys dest-sys ) ( continue? -- )</td>
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<td>window-top</td>
<td>( -- border-height )</td>
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Table A-49 Fcodes by Name (Continued)

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<td>wpoke</td>
<td>( w waddr -- okay? )</td>
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<td>wxjoin</td>
<td>( w.lo w.2 w.3 w.hi -- o )</td>
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<td>x,</td>
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<td>x@</td>
<td>( xaddr -- o )</td>
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<td>x!</td>
<td>( o xaddr -- )</td>
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<td>/x</td>
<td>( -- n )</td>
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<td>/x*</td>
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<td>xal+</td>
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<td>xbflip</td>
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<td>xbflips</td>
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<td>xsxsplit</td>
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<td>xor</td>
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<td>( oct1 -- oct2 )</td>
</tr>
<tr>
<td>02 53</td>
<td>xwflips</td>
<td>( xaddr len -- )</td>
</tr>
<tr>
<td>02 54</td>
<td>xwsplit</td>
<td>( o -- w.lo w.2 w.3 w.hi )</td>
</tr>
</tbody>
</table>
FCode Memory Allocation

To get general-purpose memory, use buffer: or alloc-mem. Use free-mem to de-allocate memory obtained with alloc-mem.

To map in portions of your SBus device for ordinary access, use "$\text{map-in}$ $\text{call-parent}$ as in:

\[
\text{my-address offset + my-space size } \text{"map-in" } \text{call-parent (virt)}
\]

To later map out those portions of your device, use "$\text{map-out}$ $\text{call-parent}$ as in:

\[
(\text{virt }) \text{ size } \text{"map-out" } \text{call-parent}
\]
To use a region of system memory for DMA (for example, for both direct CPU access and DMA access from a device), first define the following mapping and allocation routines, then follow the steps below to ensure data coherency.

```
: dma-alloc ( size -- virt )  " dma-alloc" $call-parent ;
: dma-free   ( virt size -- )  " dma-free" $call-parent ;
: dma-map-in  ( virt size cache? -- devaddr )
   " dma-map-in" $call-parent
   ;
: dma-map-out ( virt devaddr size -- )  " dma-map-out" $call-parent ;
: dma-sync ( virt devaddr size -- ) \ Correct even if "dma-sync" missing
   " dma-sync" ['] $call-parent catch if
   2drop 3drop
   then
   ;
```

1. Allocate the DMA region with:
   a. dma-alloc
   b. dma-map-in

2. CPU accesses the region using virt from dma-alloc, then perform dma-sync.

3. Start DMA operation, using devaddr from dma-map-in.
   a. Wait for DMA complete status.
   b. Repeat DMA as needed, then perform dma-sync

4. Repeat Steps 2 and 3 as needed

5. Deallocate the region when completed, with:
   a. dma-map-out
   b. dma-free
Coding Style

This appendix describes the coding style used in some OpenBoot implementations. These guidelines are a living document that first came into existence in 1985. By following these guidelines in your own code development, you will produce code that is similar in style to a large body of existing OpenBoot work. This will make your code more easily understood by others in the OpenBoot community.

Typographic Conventions

The following typographic conventions are used in this document:

- The symbol ✜ is used to represent space characters (i.e. ASCII 0x20).
- The symbol … is used to represent an arbitrary amount of Forth code.
- Within prose descriptions, Forth words are show in this font.

Use of Spaces

Since Forth code can be very terse, use spaces to increase readability.

Two consecutive spaces are used to separate a definition’s name from the beginning of the stack diagram, another two consecutive spaces (or a newline) are used to separate the stack diagram from the word’s definition, and two consecutive spaces (or a newline) separate the last word of a definition from the closing semi-colon. For example:
Forth words are usually separated by one space. If a phrase consisting of several words performs some function, that phrase should be separated from other words/phrases by two consecutive spaces or a newline.

When creating multiple line definitions, all lines except the first and last should be indented by three (3) spaces. If additional indentation is needed with control structures, the left margin of each additional level of indentation should start three (3) spaces to the right of the preceding level.

if...then...else

In if...then or if...else...then control structures that occupy no more than one line, two spaces should be used both before and after each if, else or then.

Longer constructs should be structured like this:

<code to generate flag>if
  <true clause>
then
<code to generate flag>if
  <true clause>
else
  <false clause>
then
do...loop

In do...loop constructs that occupy no more than one line, two spaces should be used both before and after each do or loop.

```c
<code to calculate limits>
		do
			<body>
		loop

Longer constructs should be structured like this:

```c
<code to calculate limits>
		do
			<body>
		loop

The longer +loop construct should be structured like this:

```c
<code to calculate limits>
		do
			<body>
		+loop

begin...while...repeat

In begin...while...repeat constructs that occupy no more than one line, two spaces should be used both before and after each begin, while or repeat).

```c
-begin-
	<flag code>-while-
	<body>-repeat-

Longer constructs:

```c
-begin-
	<short flag code>-while-
	<body>-repeat-

```

```c
-begin-
	<long flag code>-while-
	<body>-repeat-
```
begin...until...again

In begin...until and begin...again constructs that occupy no more than one line, two spaces should be used both before and after each begin, until or again.

```c
begin <body> until
begin <body> again
```

Longer constructs:

```c
begin <body> until
begin <body> again
```

Block Comments

Block comments begin with `//`. All text following the space is ignored until after the next newline. While it would be possible to delimit block comments with parentheses, the use of parentheses is reserved by convention for stack comments.

Precede each non-trivial definition with a block comment giving a clear and concise explanation of what the word does. Put more comments at the very beginning of the file to describe external words which could be used from the User Interface.

Stack Comments

Stack comments begin with `( and end with ). Use stack comments liberally in definitions. Try to structure each definition so that, when you put stack comments at the end of each line, the stack picture makes a nice pattern.

```c
: name ( stack before -- stack after )
```
Return Stack Comments

Return stack comments are also delimited with parentheses. In addition, the notation \( r: \) is used at the beginning of the return stack comment to differentiate it from a parameter stack comment.

Place return stack comments on any line that contains one or more words that cause the return stack to change. (This limitation is a practical one; it is often difficult to do otherwise due to lack of space.) The words \( r> \) and \( r\geq \) must be paired inside colon definitions and inside do...loop constructs.

```plaintext
: name ( stack before -- stack after )

✦✦✦ dup ( stack condition after the execution of dup )

✦✦✦ ;
```

Numbers

Hexadecimal numbers should be typed in lower case. If a given number contains more than 4 digits, the number may be broken into groups of four digits with periods. For example:

dead.beef

Since the default number base is hexadecimal, the convention is not to precede hexadecimal numbers with \( h\# \).
This appendix discusses the FCodes and macros that have changed between FCode 2.x and FCode 3.x. The 3.x tokenizer will still tokenize code correctly using FCode 2.x names (excepting old #>, #, and #s). The function of each of the equivalent FCodes is unchanged. The existing tokenized FCode programs using 2.x FCodes will not be affected on 3.x OpenBoot PROMs. The only functional exception is in the FCode 2.x names #>, #, and #s. FCode 3.x has the same names associated with functionally different FCodes and different byte values. If you have tokenized FCode using the 2.x tokenizer with these FCodes (for instance, #), you will get the same response (since the operation of old # is equivalent to new u# and tokenized code has 99 as byte value for your old #).

Table D-1  FCode Names Changed in Version 3.x

<table>
<thead>
<tr>
<th>FCode 2.x</th>
<th>FCode 3.x (equivalent)</th>
<th>Byte Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>not</td>
<td>invert</td>
<td>26</td>
</tr>
<tr>
<td>&lt;&lt;</td>
<td>lshift</td>
<td>27</td>
</tr>
<tr>
<td>&gt;&gt;</td>
<td>rshift</td>
<td>28</td>
</tr>
<tr>
<td>ca1+</td>
<td>char+</td>
<td>62</td>
</tr>
<tr>
<td>na1+</td>
<td>cell+</td>
<td>65</td>
</tr>
<tr>
<td>/c*</td>
<td>chars</td>
<td>66</td>
</tr>
<tr>
<td>/n*</td>
<td>cells</td>
<td>69</td>
</tr>
<tr>
<td>flip</td>
<td>wbflip</td>
<td>80</td>
</tr>
<tr>
<td>version</td>
<td>FCode-revision</td>
<td>37</td>
</tr>
<tr>
<td>FCode 2.x</td>
<td>FCode 3.x (equivalent)</td>
<td>Byte Value</td>
</tr>
<tr>
<td>----------</td>
<td>------------------------</td>
<td>------------</td>
</tr>
<tr>
<td>b(is)</td>
<td>b(to)</td>
<td>C3</td>
</tr>
<tr>
<td>eval</td>
<td>evaluate</td>
<td>CD</td>
</tr>
<tr>
<td>u*x</td>
<td>um*</td>
<td>D4</td>
</tr>
<tr>
<td>xu/mod</td>
<td>um/mod</td>
<td>D5</td>
</tr>
<tr>
<td>x+</td>
<td>d+</td>
<td>D8</td>
</tr>
<tr>
<td>x-</td>
<td>d-</td>
<td>D9</td>
</tr>
<tr>
<td>attribute</td>
<td>property</td>
<td>01 10</td>
</tr>
<tr>
<td>xdrint</td>
<td>encode-int</td>
<td>01 11</td>
</tr>
<tr>
<td>xdr+</td>
<td>encode+</td>
<td>01 12</td>
</tr>
<tr>
<td>xdrphys</td>
<td>encode-phys</td>
<td>01 13</td>
</tr>
<tr>
<td>xdrstring</td>
<td>encode-string</td>
<td>01 14</td>
</tr>
<tr>
<td>xdrbytes</td>
<td>encode-bytes</td>
<td>01 15</td>
</tr>
<tr>
<td>decode-2int</td>
<td>parse-2int</td>
<td>01 1B</td>
</tr>
<tr>
<td>map-sbus</td>
<td>map-low</td>
<td>01 30 (stack diag. enhanced)</td>
</tr>
<tr>
<td>get-my-attribute</td>
<td>get-my-property</td>
<td>02 1A</td>
</tr>
<tr>
<td>xdrtoint</td>
<td>decode-int</td>
<td>02 1B</td>
</tr>
<tr>
<td>xdrtostring</td>
<td>decode-string</td>
<td>02 1C</td>
</tr>
<tr>
<td>get-inherited-attribute</td>
<td>get-inherited-property</td>
<td>02 1D</td>
</tr>
<tr>
<td>delete-attribute</td>
<td>delete-property</td>
<td>02 1E</td>
</tr>
<tr>
<td>get-package-attribute</td>
<td>get-package-property</td>
<td>02 1F</td>
</tr>
<tr>
<td>wflips</td>
<td>wbflips</td>
<td>02 36</td>
</tr>
<tr>
<td>lflips</td>
<td>lwflips</td>
<td>02 37</td>
</tr>
<tr>
<td>is</td>
<td>to</td>
<td></td>
</tr>
</tbody>
</table>
Note – The following 2.x FCodes have changed names. The new 3.x FCodes with the same names function differently.

Table D-2  FCode 2.x Changed Names and Equivalent FCode 3.x Names

<table>
<thead>
<tr>
<th>FCode 2.x</th>
<th>FCode 3.x (equivalent)</th>
<th>Byte Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>&gt;#</td>
<td>u#&gt;</td>
<td>97</td>
</tr>
<tr>
<td>#</td>
<td>u#</td>
<td>99</td>
</tr>
<tr>
<td>#s</td>
<td>u#s</td>
<td>9A</td>
</tr>
</tbody>
</table>

So if you are using the 3.x tokenizer to elicit the old response from >#, #, and #s, the source code must be changed so that the commands are replaced by u#>, u# and u#s respectively. For code previously tokenized using the 2.x tokenizer, the result is the same on both OpenBoot 2.x and 3.xPROMs.

Table D-3  FCode 2.x Commands Deleted in FCode 3.x

<table>
<thead>
<tr>
<th>FCode 2.x</th>
<th>Byte Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>4-byte-id</td>
<td>FE</td>
</tr>
<tr>
<td>dma-alloc</td>
<td>01 01</td>
</tr>
<tr>
<td>memmap</td>
<td>01 04</td>
</tr>
<tr>
<td>&gt;physical</td>
<td>01 06</td>
</tr>
<tr>
<td>my-params</td>
<td>01 0F</td>
</tr>
<tr>
<td>intr</td>
<td>01 17</td>
</tr>
<tr>
<td>driver</td>
<td>01 18</td>
</tr>
<tr>
<td>group-code</td>
<td>01 23</td>
</tr>
<tr>
<td>processor-type</td>
<td>02 10</td>
</tr>
<tr>
<td>firmware-version</td>
<td>02 11</td>
</tr>
<tr>
<td>fcode-version</td>
<td>02 12</td>
</tr>
<tr>
<td>probe</td>
<td>02 38</td>
</tr>
<tr>
<td>probe-virtual</td>
<td>02 39</td>
</tr>
</tbody>
</table>

To access the functionality provided by dma-alloc, do:

: my-dma-alloc (size -- addr) " dma-alloc" $call-parent;

and use my-dma-alloc.
To access the functionality provided by `memmap`, use `map-low` appropriately.

To replace `intr`, create “interrupts” and “intr” properties using `property`.

To access the functionality provided by `firmware-version` or `fcode-version`, use `firmware-revision`.

**Table D-4**  New FCodes Added in 3.x

<table>
<thead>
<tr>
<th>FCode 3.x</th>
<th>ByteValue</th>
</tr>
</thead>
<tbody>
<tr>
<td>unloop</td>
<td>89</td>
</tr>
<tr>
<td>get-token</td>
<td>DA</td>
</tr>
<tr>
<td>set-token</td>
<td>DB</td>
</tr>
<tr>
<td>state</td>
<td>DC</td>
</tr>
<tr>
<td>compile,</td>
<td>DD</td>
</tr>
<tr>
<td>behavior</td>
<td>DE</td>
</tr>
<tr>
<td>decode-phys</td>
<td>01 28</td>
</tr>
<tr>
<td>push-package</td>
<td>01 29</td>
</tr>
<tr>
<td>pop-package</td>
<td>01 2A</td>
</tr>
<tr>
<td>interpose</td>
<td>01 2B</td>
</tr>
<tr>
<td>lwflip</td>
<td>02 26</td>
</tr>
<tr>
<td>lbflip</td>
<td>02 27</td>
</tr>
<tr>
<td>lbflips</td>
<td>02 28</td>
</tr>
<tr>
<td>next-property</td>
<td>02 3D</td>
</tr>
<tr>
<td>byte-load</td>
<td>02 3E</td>
</tr>
<tr>
<td>set-args</td>
<td>02 3F</td>
</tr>
</tbody>
</table>

**Table D-5**  Differently Functioning 3.x FCodes With Changed Byte Values

<table>
<thead>
<tr>
<th>FCode 3.x</th>
<th>Byte Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>#</td>
<td>C7</td>
</tr>
<tr>
<td>#s</td>
<td>C8</td>
</tr>
<tr>
<td>#&gt;</td>
<td>C9</td>
</tr>
</tbody>
</table>
### Table D-6  3.x FCodes Related to 64-bit Operations

<table>
<thead>
<tr>
<th>3.x FCodes</th>
<th>Stack Diagrams</th>
<th>Byte Value</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>rx@</code></td>
<td>( o.addr -- o )</td>
<td>02 2E</td>
</tr>
<tr>
<td><code>rx!</code></td>
<td>( o -- o.addr )</td>
<td>02 2F</td>
</tr>
<tr>
<td><code>bxjoin</code></td>
<td>( b.lo b.2 b.3 b.4 b.5 b.6 b.7 b.hi -- o )</td>
<td>02 41</td>
</tr>
<tr>
<td><code>&lt;l@</code></td>
<td>( q.addr -- n )</td>
<td>02 42</td>
</tr>
<tr>
<td><code>lxjoin</code></td>
<td>( quad.lo quad.hi -- o )</td>
<td>02 43</td>
</tr>
<tr>
<td><code>wxjoin</code></td>
<td>( w.lo w.2 w.3 w.hi -- o )</td>
<td>02 44</td>
</tr>
<tr>
<td><code>x,</code></td>
<td>( o -- )</td>
<td>02 45</td>
</tr>
<tr>
<td><code>x@</code></td>
<td>( o.addr -- o )</td>
<td>02 46</td>
</tr>
<tr>
<td><code>x!</code></td>
<td>( o -- o.addr )</td>
<td>02 47</td>
</tr>
<tr>
<td><code>/x</code></td>
<td>( -- n )</td>
<td>02 48</td>
</tr>
<tr>
<td><code>/x*</code></td>
<td>( nu1 -- nu2 )</td>
<td>02 49</td>
</tr>
<tr>
<td><code>xa+</code></td>
<td>( addr1 index -- addr2 )</td>
<td>02 4A</td>
</tr>
<tr>
<td><code>xa1+</code></td>
<td>( addr1 -- addr2 )</td>
<td>02 4B</td>
</tr>
<tr>
<td><code>xbflip</code></td>
<td>( oct1 -- oct2 )</td>
<td>02 4C</td>
</tr>
<tr>
<td><code>xbflips</code></td>
<td>( o.addr len -- )</td>
<td>02 4D</td>
</tr>
<tr>
<td><code>xbsplit</code></td>
<td>( o -- b.lo b.2 b.3 b.4 b.5 b.6 b.7 b.hi )</td>
<td>02 4E</td>
</tr>
<tr>
<td><code>xlflip</code></td>
<td>( oct1 -- oct2 )</td>
<td>02 4F</td>
</tr>
<tr>
<td><code>xlflips</code></td>
<td>( o.addr len -- )</td>
<td>02 50</td>
</tr>
<tr>
<td><code>xsplit</code></td>
<td>( o -- quad.lo quad.hi )</td>
<td>02 51</td>
</tr>
<tr>
<td><code>xwflip</code></td>
<td>( oct1 -- oct2 )</td>
<td>02 52</td>
</tr>
<tr>
<td><code>xwflips</code></td>
<td>( o.addr len -- )</td>
<td>02 53</td>
</tr>
<tr>
<td><code>xwsplit</code></td>
<td>( o -- w.lo w.2 w.3 w.hi )</td>
<td>02 54</td>
</tr>
</tbody>
</table>
The following device-handling-related user interface commands have changed between OpenBoot 2.x and OpenBoot 3.x. Their functional behavior is the same. Determine your system’s OpenBoot PROM version by entering .version at the ok> prompt, then using the appropriate commands from the following table.

<table>
<thead>
<tr>
<th>OpenBoot 2.x Command</th>
<th>OpenBoot 3.x Command</th>
</tr>
</thead>
<tbody>
<tr>
<td>.attributes</td>
<td>.properties</td>
</tr>
<tr>
<td>cd</td>
<td>dev</td>
</tr>
<tr>
<td>reset (to reset full system)</td>
<td>reset-all</td>
</tr>
</tbody>
</table>
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0<>, 222
0=, 222
0>, 222
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-1, 223
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2, 223
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2*, 224

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