



# SunVTS™ 6.2 Test Reference Manual for x86 Platforms

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# Preface

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SunVTS™ is the Sun Microsystems™ Validation Test Suite. SunVTS is a comprehensive software diagnostic package that tests and validates Sun SPARC and x86 hardware by verifying the configuration and functionality of controllers, devices, and platforms.

SunVTS is primarily used from a graphical user interface (GUI). This book describes SunVTS tests that run on machines with SPARC® and x86 architectures. The descriptions include specific test options, procedures, and error messages.

This book is primarily written as a reference for SunVTS test specific information. Refer to the *SunVTS User's Guide* for overall SunVTS information.

Developers or experienced users who want to run the SunVTS diagnostic application will find these documents useful.

---

**Note** – The Solaris release with which this version of SunVTS is delivered supports systems that use the SPARC and x86 families of processor architectures: UltraSPARC®, SPARC64, AMD64, Pentium, and Xeon EM64T. The supported systems appear in the *Solaris 10 Hardware Compatibility List* at <http://www.sun.com/bigadmin/hcl>. This document cites any implementation differences between the platform types.

In this document the term `x86` refers to 64-bit and 32-bit systems manufactured using processors compatible with the AMD64 or Intel Xeon/Pentium product families. For supported systems, see the *Solaris 10 Hardware Compatibility List*.

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## Before You Read This Book

To fully use the information in this document, you must have thorough knowledge of the topics discussed in these books:

- *SunVTS User's Guide*
- *SunVTS Quick Reference Card*

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## How This Book Is Organized

This book is organized as follows:

[Chapter 1](#) describes SunVTS requirements, test modes, user interfaces, the collection of tests, and how to run a test from the command line.

The remaining chapters describe the individual SunVTS tests, their options, applicable test modes, and command-line syntax. These chapters are arranged in alphabetical order according to each test name.

[Appendix A](#) provides information about the serial and parallel port loopback connectors that are required by some of the SunVTS tests.

---

## Using UNIX Commands

This document might not contain information on basic UNIX® commands and procedures such as shutting down the system, booting the system, and configuring devices.

Refer to one or more of the following for this information:

- *Solaris Handbook for Sun Peripherals*
- Software documentation that you received with your system
- Solaris™ Operating System documentation, which is at  
<http://docs.sun.com>

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# Shell Prompts

Shell	Prompt
C shell	<i>machine-name%</i>
C shell superuser	<i>machine-name#</i>
Bourne shell and Korn shell	\$
Bourne shell and Korn shell superuser	#

---

# Typographic Conventions

Typeface or Symbol	Meaning	Examples
AaBbCc123	The names of commands, files, and directories; on-screen computer output	Edit your <code>.login</code> file. Use <code>ls -a</code> to list all files. % You have mail.
<b>AaBbCc123</b>	What you type, when contrasted with on-screen computer output	% <b>su</b> Password:
<i>AaBbCc123</i>	Book titles, new words or terms, words to be emphasized. Replace command-line variables with real names or values.	Read Chapter 6 in the <i>User's Guide</i> . These are called <i>class</i> options. To delete a file, type <b>rm</b> <i>filename</i> .

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# Related Documentation

This manual covers SunVTS tests. It serves as a reference companion to the SunVTS documents listed below.

Application	Title	Part Number
Installation and Navigation	<i>SunVTS 6.2 User's Guide</i>	819-6454-10
Quick Reference Card	<i>SunVTS Quick Reference Card</i>	819-6458-10

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*SunVTS 6.2 Test Reference Manual for x86 Platforms*, part number 819-6456-10





# Introduction

---

This manual describes SunVTS Version 6.2 tests that are distributed on the Solaris 10 6/06 (Solaris 10 Update 2) Software DVDs.

This chapter contains the following topics:

- [“x86 Platform Support” on page 2](#)
- [“Test Requirements” on page 3](#)
- [“Collection of SunVTS Tests” on page 4](#)
- [“SunVTS User Interfaces” on page 5](#)

The Sun™ Validation and Test Suite (SunVTS) software performs multiple diagnostic hardware tests from a single user interface. SunVTS verifies the connectivity, functionality, and reliability of controllers and devices.

SunVTS contains many individual tests that support testing of a wide range of products and peripherals. Most of the tests are capable of testing devices in a 32-bit or 64-bit Solaris environment.

Such flexibility means that the proper test modes and options need to be selected to maximize its effectiveness. This book covers the individual test options, modes, and requirements. For overall test configuration modes and options refer to the *SunVTS User's Guide*.

---

**Note** – 64-bit tests are located in this directory: `/opt/SUNWvts/bin/64/`, or the relative path to which you installed SunVTS. If a test is not present in this directory, then it might be available as a 32-bit test only. For more information, see [“32-Bit and 64-Bit Tests” on page 4](#).

---

The default installation directory for SunVTS is `/opt/SUNWvts`. However, when you are installing SunVTS, you can specify a different directory. Refer to the *SunVTS User's Guide* for installation information.

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**Note** – SunVTS does not support processor sets. If processor sets are defined, you must first delete the processor sets before running SunVTS.

---

## SunVTS Version Information

The standard command line argument, *testname* -v, displays the SunVTS version and release date of the test if available.

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## x86 Platform Support

Starting with Solaris 10, the SunVTS infrastructure and core diagnostics are available for x86 Solaris platforms.

SunVTS is supported and tested on the following Sun x86 platforms:

- Sun Fire V20z
- Sun Fire V40z
- Sun Fire B100
- Sun Fire B200
- Sun Fire x4100
- Sun Fire x4200
- Netra CP3020

---

**Note** – If you perform SunVTS on an unsupported platform, a warning message appears and SunVTS stops.

---

You must install the x86 version of the SunVTS packages to perform SunVTS on x86 platforms. The software packages use the same names as in the SPARC environment. The SunVTS packages delivered separately for both x86 and SPARC Solaris platforms are as follows:

- `SUNWvts` – Contains the SunVTS core framework that includes the kernel and user interface.
- `SUNWvtsmn` – Contains the SunVTS online manual pages
- `SUNWvtshr` – Contains the SunVTS framework configuration files in the root partition (Superuser).
- `SUNWvtsts` – Contains the SunVTS test binaries.

The SunVTS components available for x86 Solaris platforms are as follows.

#### Infrastructure:

- sunvts
- vtsk
- vts\_cmd
- vtstty
- vtsui
- vtsprobe

#### SunVTS Tests:

- BMC Environment Test (bmcenvironment)
- Optical Disk Drive Test (cddvdtest)
- CPU Test (cputest)
- Cryptographics Test (cryptotest)
- Disk and Floppy Drives Test (disktest)
- Data Translation Look-aside Buffer Test (dtlbtest)
- Emulex HBA Test (emlxtest)
- InfiniBand Host Channel Adapter Test (ibhcatest)
- Level 1 Data Cache Test (l1dcachetest)
- Level 2 SRAM Test (l2sramtest)
- Ethernet Loopback Test (netlbtest)
- Network Hardware Test (nettest)
- Physical Memory Test (pmemtest)
- Qlogic Host Bus Adapter Test (qlctest)
- RAM Test (ramtest)
- Serial Ports Test (serialtest)
- System Test (systest)
- Tape Test (tapetest)
- Virtual Memory Test (vmemtest)

---

## Test Requirements

SunVTS Version 6.2 was first introduced and designed to run in the Solaris 10 6/06 (Solaris 10 Update 2) operating system (OS) and subsequent compatible releases. The operating system kernel must be configured to support all peripherals that are to be tested.

Some SunVTS tests have special requirements such as the connection of loopback connectors, installation of test media, or the availability of disk space. These requirements are listed for each test in the corresponding chapter in this book.

---

# Collection of SunVTS Tests

Many individual tests make up the collection of tests in the SunVTS application. Each test is a separate process from the SunVTS kernel. Each test can be run individually from the command line or from the SunVTS user interface.

When SunVTS is started, the SunVTS kernel automatically probes the system kernel to determine the hardware devices. The devices are then displayed on the SunVTS control panel with the appropriate tests and test options. This provides a quick check of your hardware configuration, and no time is wasted trying to run tests that are not applicable to your configuration.

During testing, the hardware tests send the test status and messages to the SunVTS kernel through interprocess communication (IPC) protocols. The kernel passes the status to the user interface and logs the messages.

SunVTS has a shared object library that contains test-specific probing routines. At runtime, the SunVTS kernel dynamically links in and calls these probing routines to initialize its data structure with test-specific information. You can add new tests into the SunVTS environment without recompiling the SunVTS source code.

## 32-Bit and 64-Bit Tests

Because each test is a separate program, you can run individual tests directly from the command line. When this is done, care must be taken to run the appropriate test (32-bit or 64-bit) that corresponds to the operating system that is running (32-bit or 64-bit). This is done by running tests from specific directories as follows:

- 32-bit tests—`/opt/SUNWvts/bin/testname`
- 64-bit tests—`/opt/SUNWvts/bin/64/testname`
  - The test is an actual 64-bit binary test if *testname* is a binary file.
  - The test is a 32-bit test capable of running in the 64-bit environment if *testname* is a symbolic link.

---

**Note** – For information on SunVTS packages and installation procedures, refer to the *SunVTS User's Guide*.

---

If you use the `sunvts` command to run SunVTS, SunVTS automatically allocates 32-bit or 64-bit tests based on the 32-bit or 64-bit Solaris operating environment that is running. Therefore, the only time that you need to be concerned with the 32-bit or 64-bit operation is when you run the SunVTS kernel or SunVTS tests from the command line.

If you are not sure which operating system is running, refer to the Solaris System Administration manuals. In Solaris 10, you can use the following command to identify the application support of your system.

```
# isainfo -v
```



# SunVTS User Interfaces

You can run SunVTS tests from multiple interfaces: The graphical user interfaces or the TTY interface. SunVTS tests can also be run individually from a shell tool command line, using the command-line syntax for each test (refer to [“Running a Test From the Command Line” on page 8](#)). [TABLE 1-1](#) describes the various SunVTS user interfaces. Refer to the *SunVTS User’s Guide* for more information on these interfaces.

**TABLE 1-1** SunVTS System Interfaces

SunVTS System Interfaces	Description
Graphical user interfaces (GUIs)	Users can select tests and test options by pointing and clicking with a mouse button in the CDE interface.
TTY interface	Users can run SunVTS from a terminal or modem attached to a serial port. This feature requires that users use the keyboard instead of the mouse, and it displays one screen of information at a time.
Command-line execution	Users can run each of the SunVTS tests individually from a shell tool command line using the command-line syntax. Each test description in this book contains the corresponding command-line syntax.

**Note –** To increase or decrease a numeric value in a SunVTS CDE dialog box, you can use either the up or down arrows, or type a new value in the text box and press Return. Select Apply to apply all dialog box changes.

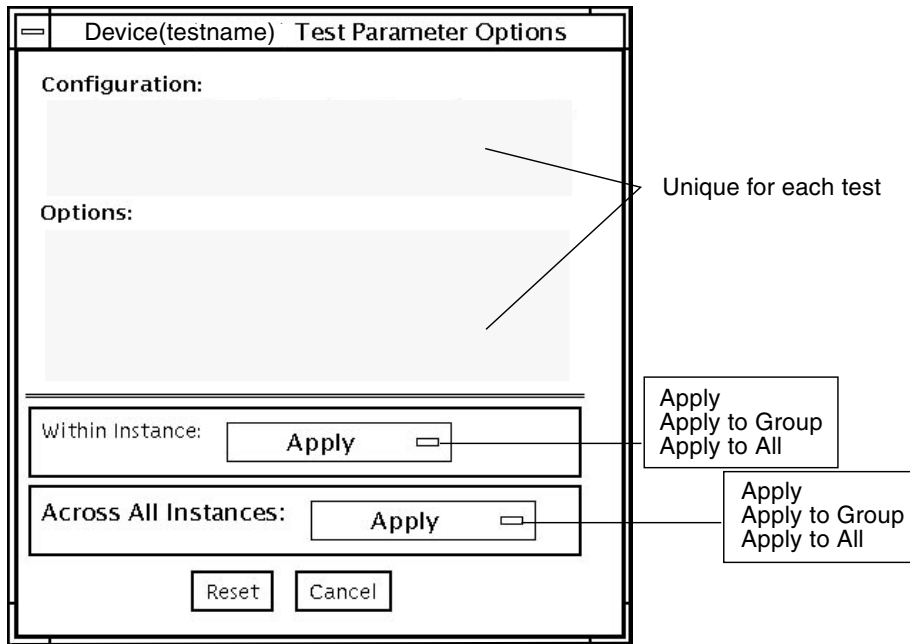
# Running a Test From a User Interface

The common way to run SunVTS testing is through a SunVTS graphical user interface or the TTY interface.

Test configuration, control, and results are easily accessed through buttons and dialog boxes. These buttons and dialog boxes are covered in the *SunVTS User's Guide*. However, the Test Parameter Options dialog box is unique for each test, and is therefore covered in this manual.

## Test Parameter Options Dialog Box

The options displayed in this menu differ for each test, but the lower set of buttons are generic and are described below.



**FIGURE 1-1** Test Parameter Options Dialog Box

**TABLE 1-2** Test Parameter Options Dialog Box Items

Menu Item	Description
Configuration	Information such as device type, capacity, revision, and serial numbers for the selected device. This information cannot be changed.
Options	A list of test options that are used to customize the testing of the selectable device, group, or all devices. The options are specific for each test and are covered in the test specific-chapters in this manual.
Within Instance	Provides the means to apply the settings: <ul style="list-style-type: none"> <li>• to this device only with Apply, or</li> <li>• to all devices within this group with Apply to Group, or</li> <li>• to all devices (of the <i>same device type</i> for <i>all controllers</i>) with Apply to All.</li> </ul> The option settings are only applied to one instance of the test.

**TABLE 1-2** Test Parameter Options Dialog Box Items (*Continued*)

Menu Item	Description
Across All Instances	Provides the means to apply the settings globally: <ul style="list-style-type: none"><li>• to this device only with Apply, or</li><li>• to all devices within this group with Apply to Group, or</li><li>• to all devices (of the <i>same device type</i> for all controllers) with Apply to All.</li></ul> The option settings are applied to all instances.
Reset	Returns the option values to their default settings and closes the test parameter option menu.
Cancel	Ignores any changes made to option values and closes the test parameter option menu.

**Note** – The Test Parameter Options Dialog Box descriptions also apply to the Test Parameter Options menu in the TTY interface.

## Running a Test From the Command Line

In some cases it may be more convenient to run a single SunVTS test from the command line rather than through a SunVTS user interface. The following information describes how to do this.

Unless specified, the test runs without the SunVTS kernel (`vtstk`). All events and errors are sent to `stdout` or `stderr` and are not logged in the log files.

When you run a test in this way, you must specify all test options in the form of command-line arguments.

There are two types of command-line arguments:

- Standard arguments—common to all tests. Refer to [TABLE 1-3](#) for details.
- Test specific arguments—unique to a specific test. Refer to the test-specific chapters in this book for details.

The standard syntax for all SunVTS tests is:

```
testname [-scruvdtelnf] [-i number] [-w number] [-o test_specific_arguments]
```



## Standard Command-Line Arguments

The following table defines the standard SunVTS command-line arguments:

**TABLE 1-3** Standard Command-Line Arguments

Argument	Description
-s	Runs a test as though it were invoked from the SunVTS kernel (vtsk). The default is to send the output to <code>stdout</code> or <code>stderr</code> .
-c	Enables a core image of the test process to be created in the current working directory upon receipt of certain signals, otherwise those signals are caught and handled to prevent a core from being generated. The default is to disable the creation of a core image.
-r	Enables run on error so that when an error occurs, the test continues with the next test sequence instead of exiting. The default is false.
-v	Runs the test in Verbose mode and displays messages with more detailed information about the testing process. The default is false.
-V	Displays the SunVTS version and release date of the test.
-d	Runs the test in debug mode and displays messages to help programmers debug their test code. The default is false.
-t	Runs the test in Trace mode and displays messages that track function calls and sequences currently in use by the test code. The default is false.
-l	Runs the test in Online Functional mode. This is the same mode that tests run in when executed with the <code>vtsui.online</code> command. It is a non-intrusive version that will not significantly affect other applications. See the note below. The default is true.
-x	Runs the test in Exclusive mode.
-n	Runs the test in Connection mode. See the note below. The default is false.
-f	Runs the test in full Functional mode. This mode assumes that the test has complete control of the device under test. See the note below. The default is false.
-p <i>number</i>	Defines the number of passes for scalable tests. The default is 1.
-i <i>number</i>	Defines the number of instances for scalable tests. The default is 1.
-w <i>number</i>	Defines to which instance the test is assigned; this option is for scalable tests. The default is 0.
-o	Indicates that the options and arguments that follow are test specific.

---

**Note** – Separate each test-specific argument by commas, with no space after each comma.

---

---

**Note** – If you choose to specify a test mode with the `l`, `n`, or `f` option, specify only one option at a time because only one test mode can be selected at a time.

---

## Test-Specific Arguments

There are test-specific arguments, as described in [TABLE 1-4](#). Test-specific arguments follow the format specified in the `getsubopt(3C)` man page. For information about test-specific arguments refer to the specific test chapter in this book.

**TABLE 1-4** SunVTS Test-Specific Arguments

Argument	Description
<code>-o</code>	Separate each test-specific argument by commas, with no space after the comma. For example: <code># ./sample -v -o dev=device-name,count=count-number,test=testlist,size=file-size,retain=mode</code>  The test option format is specified by the man page <code>getsubopt(3C)</code> .

## BMC Environment Test (bmcenvironment)

---

The `bmcenvironment` test scans the sensors of the Base Management Controller (BMC) and reports failures on sensors that report readings outside of their reported ranges.



---

**Caution** – Ensure that no other applications, such as `ipmitool`, are using the BMC when performing the `bmcenvironment` test.

---



---

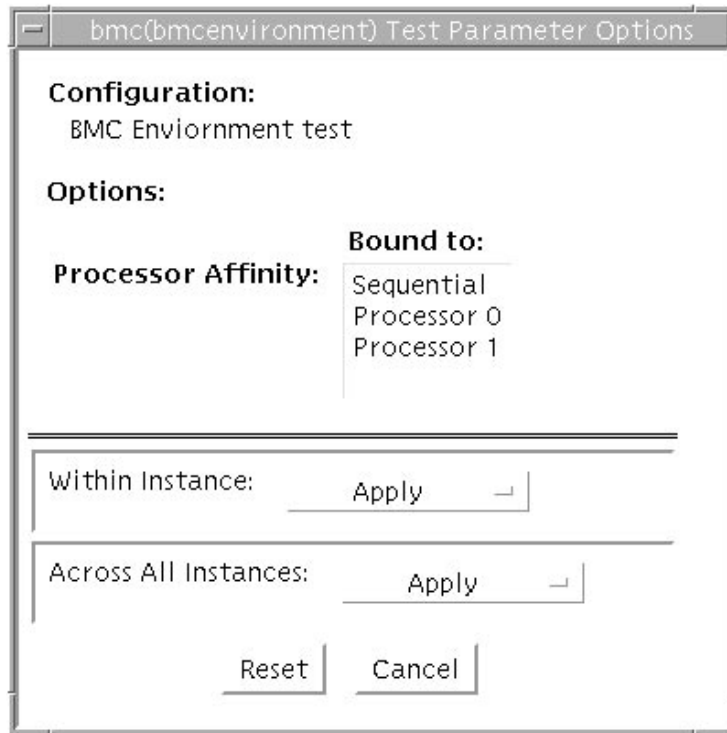
**Caution** – Ensure that a service processor daughter card is installed and seated correctly.

---

---

### bmcenvironment Options

To reach the following dialog box, right-click on the test name in the System Map and select Test Parameter Options. If you do not see this test in the System Map, you might need to expand the collapsed groups, or your system might not include the device appropriate to this test. Refer to the *SunVTS User's Guide* for more details.



**FIGURE 2-1** bmcenvironment Test Parameter Options Dialog Box

---

**Note** – Only the standard options are supported for the bmcenvironment test.

---

## bmcenvironment Test Modes

bmcenvironment supports the test modes listed in the following table.

**TABLE 2-1** bmcenvironment Supported Test Modes

Test Mode	Description
Connection	Runs the entire test.

**TABLE 2-1**    `bmcenvironment` Supported Test Modes

Test Mode	Description
Functional	Runs the entire test.
Online	Runs the entire test.
Exclusive	Runs the entire test.

---

# bmcenvironment Command-Line Syntax

`/opt/SUNWvts/bin/bmcenvironment` *standard\_arguments*

---

**Note** – Only the standard SunVTS command line arguments are supported for the `bmcenvironment` test.

---



## Optical Disk Drive Test (`cddvdtest`)

- 
- [“`cddvdtest` Description” on page 15](#)
  - [“`cddvdtest` Hardware and Software Requirements” on page 16](#)
  - [“`cddvdtest` Subtests” on page 18](#)
  - [“`cddvdtest` Options” on page 18](#)
  - [“`cddvdtest` Test Modes” on page 27](#)
  - [“`cddvdtest` Command-Line Syntax” on page 29](#)
- 

### `cddvdtest` Description

`cddvdtest` verifies the functionality of optical disk drives. The `disktest` probe detects the media type in the drive and shows the test options for the media found. If inserted media is not supported by the drive, the `disktest` probe shows an error and registers CD-ROM options as default.

---

**Note** – `cddvdtest` is a newly consolidated test which is used to test the optical media drives such as CD-ROM, DVD-ROM, DVD-RAM, and CD-DVD-RW drives.

---

### Volume Management

`cddvdtest` tests the optical drive(s) even if the volume manager is not running. If the volume manager is running and no media is installed in the drive(s), SunVTS prompts you to install media in the drive before selecting the test. The test fails if you try to run it without media in the drive.

---

**Note** – When testing rewritable media, the media can be blank or can contain the SunVTS test data. When testing write-once media, the media (such as CD-R) must be blank before starting the write test. Such media could still run multiple passes of the test, because after the first write test, the subsequent tests treat the media as read only and perform the test accordingly.

---

For CD-ROM and DVD-ROM drives, the test checks the unit by reading either the CD or DVD. For CD-ROMs, each track is classified as follows:

- Mode 1 uses error detection/correction code (288 bytes).
- Mode 2 uses that space for auxiliary data or as an audio track.

For rewritable CD media, the test can write one or more tracks in one test pass. `cddvdtest` writes tracks on next available space on the media. If the media is full, `cddvdtest` automatically erases the whole and starts from the beginning of the media..

For rewritable DVD media, the test writes only one track in one test pass (because there is only one track in DVD format). `cddvdtest` blanks the media when starting the test, if the media is not already blank.

For rewritable media types, the test verifies write, read, and other supporting functions of CD and DVD RW drives. The supported media include the following:

- CD-R (must be blank)
- CD-RW (can either be blank or contain the SunVTS test data)
- DVD-R (must be blank)
- DVD+R (must be blank)
- DVD-RW
- DVD+RW
- DVD-RAM

---

**Note** – `cddvdtest` is not a scalable test.

---

---

## `cddvdtest` Hardware and Software Requirements

Requirements for `cddvdtest` differ based on the media type.



## CD-ROM and DVD-ROM

The drive must have the appropriate CD-ROM or the DVD-ROM media before performing the test.

When a CD-ROM is loaded in the drive, `cddvdtest` uses CD-ROM specific options to test the drive. When a DVD-ROM is loaded, the test uses DVD-ROM specific options. Whenever you change the media in the drive, you must perform a reprobe (refer to the *SunVTS User's Guide* for details) so that the SunVTS kernel associates the correct test options with the media.

## CD-RW and DVD-RW

When testing rewritable media, reprobing is required if the media is changed. To prevent accidentally erasing useful data on a media used for testing, `cddvdtest` accepts the test media only if it is blank or it contains SunVTS test data (data that is written by `cddvdtest` itself). The test checks these conditions at probe time and at the start of the test.

In case of nonblank media or media with nonSunVTS test data, the media must be blanked first by using the `cdrw` utility. Because -R or +R media can be written only once, only blank media should be used for write testing. Such media could still run multiple passes of the test because after the first write test, the subsequent tests treat the media as read only and perform the test accordingly.

To prevent media corruption, the test posts a Warning message if it stops during the write, finalize, format, or erase phases and continues until the current operation is completed. Wait until the test completes before doing any operation on the drive.

Do not stop the test in the middle of a writing operation. Doing so may cause damage to the media in some cases. Set a limited number of passes for `cddvdtest`, set Max Passes=0 (unlimited) and stop the test manually. If a media is damaged, blank the media with `cdrw` command.

---

**Note** – DVD+RW media cannot be blanked.

---

The default delay between two passes for the read write media is three minutes. This setting enables the test to preserve the media by running fewer passes during long test runs. This setting also allows for stopping the test between passes.

# cddvdtest Subtests

cddvdtest has different subtests for each media type.

## CD-RW and DVD-RW

**TABLE 3-1** cddvdtest Subtests for CD-RW and DVD-RW Media

Subtest	Description
Blank	Erases data on DVD-RW media and CD-RW media if not blank. This subtest does not apply to DVD+RW media because this media type can not be blanked. This subtest does not apply to DVD+RW media because this media type does not support blank and always starts from the beginning of the track by overwriting earlier data (sunvts data) on the track.
Simulation	Performs a write track with laser turned off. This mode tests writing function without data written to the media.
Write	Writes to media with predefined data patterns of 0..ff hex. For CD-RW media, track can be specified as data or audio track.
Read	Reads the written data.
Compare	Compares write/read data, reports failure if miscompared.
Read Disk/Track	Reads and shows Disk Table of Contents (TOC).
Eject	Ejects media.

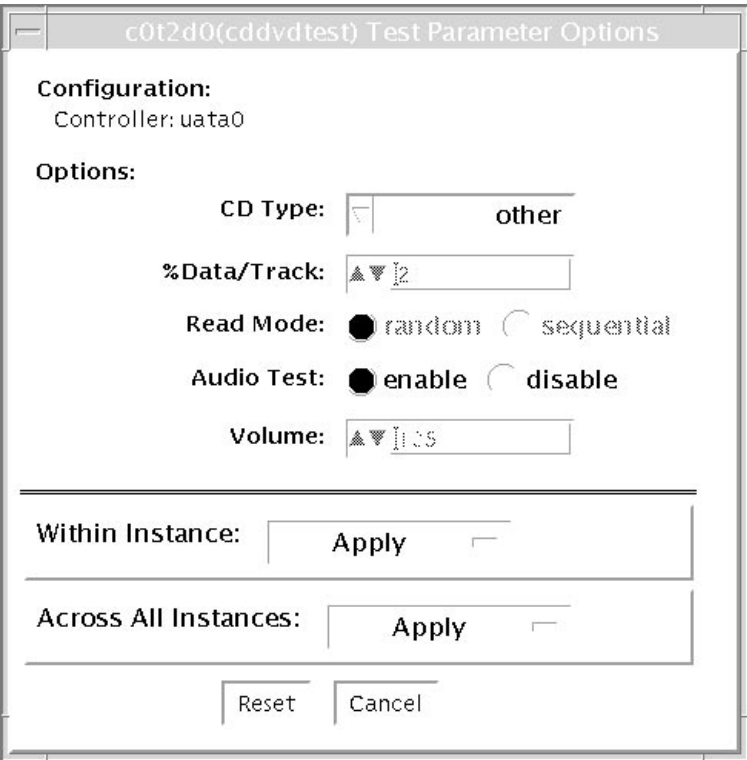
## cddvdtest Options

To reach the following dialog box, right-click on the test name in the System Map and select Test Parameter Options. If you do not see this test in the System Map, you might need to expand the collapsed groups, or your system may not include the device appropriate to this test. Refer to the *SunVTS User's Guide* for more details.

cddvdtest has different test options for each media type.

# CD-ROM Test Options

This section describes the `cddvdtest` options for CD-ROMs.



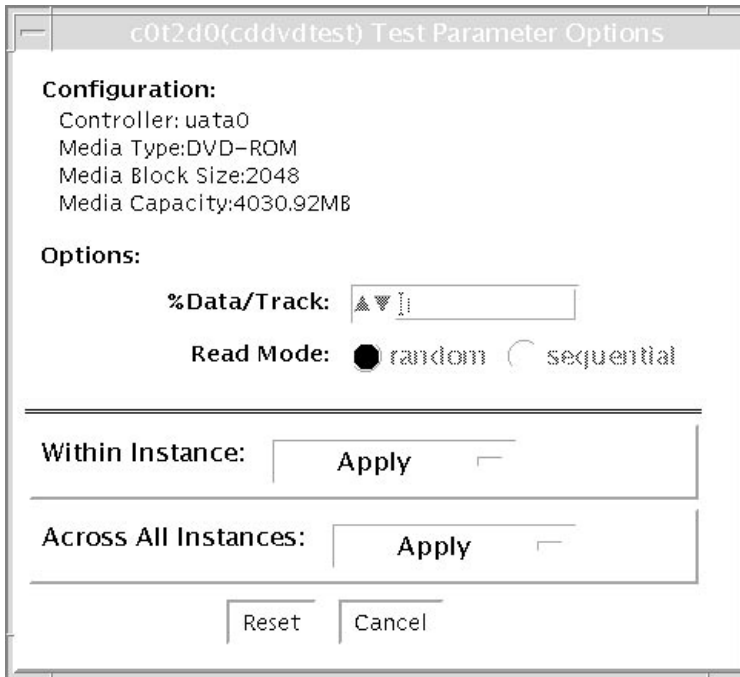
**FIGURE 3-1** `cddvdtest` Test Parameter Options Dialog Box for CD-ROM

**TABLE 3-2** `cddvdtest` Options for CD-ROM

Option	Description
CD Type	<p>The CD Type menu lists the types of compact discs that can be tested. The choices are: Pdo, Multi-session, or Other (the default CD type is Other). In Connection test mode, this option has a default value of Other.</p> <p>Note - Your choice must correspond with the disc used for testing.</p>
% Data/Track	<p>Tests a percentage of data on each track. Type a value between 0 and 100 in this field to indicate the percentage. In the online and connection tests this option has a default value of 2%.</p>
Read Mode	<p><code>cddvdtest</code> reads the CD either in Random or Sequential mode. In Random mode, data blocks are read from random track positions; in Sequential mode, data blocks are read in sequence. For both modes, the total number of blocks read is determined by the <code>%_of_data</code> option. In the online and Connection tests this option has a default value of random.</p>
Audio Test	<p>Enables or disables the audio test. You must connect headphones or a speaker to the audio jack on the CD player to hear audio output. In the Connection test, this option has a default value of disable.</p>
Volume	<p>Adjusts the volume. Type a value between 0 and 255 in this field. In the online and connection tests this option has a default value of 125.</p>

# DVD-ROM Test Options

This section describes the `cddvdtest` options for DVD-ROMs.



**FIGURE 3-2** `cddvdtest` Test Parameter Dialog Box for DVD-ROM

**TABLE 3-3** `cddvdrwtest` options for DVD-ROM

Option	Description
% Data/Track	Specifies a percentage of data to test for each track. Use a number between 0 and 100.
Read Mode	Specifies either Random or Sequential mode. Random mode reads data blocks from random track positions. Sequential mode reads data blocks in sequence. For both modes, the total number of blocks read is determined by the % Data/Track value.

## CD-RW Test Options

This section describes the test options for CD-RWs.

c0t2d0s2(cddvdrttest) Test Parameter Option

**Configuration:**  
Controller:sd1  
Device:/vol/dev/rdisk/c0t2d0/audio\_cd  
Device:ASUS DRW-0402P/D 1.05 03/05/23 P  
IONEER  
Block size = 2048  
Disk size:737126400 bytes  
Blocks:359925  
Control:Streaming control  
Write speed: 529 k/s  
Read speed: 5468 k/s  
Digital Audio Support :succeeded  
Digital Audio Accuracy :succeeded  
Write Mode:  
TAO Mode:DATA AUDIO  
SAO Mode:DATA AUDIO  
DAO Mode:DATA AUDIO

**Options:**  

Media type: ☒ CD-RW

SimulationWrite: ☒ Enable ☐ Disable

WriteSpeed:

WriteDataTrack: ☒ Enable ☐ Disable

WriteAudioTrack: ☒ Enable ☐ Disable

NumberOfLoop:

Close: ☐ Enable ☒ Disable

Erase:

Eject: ☐ Enable ☒ Disable

Within Instance:

Across All Instances:

Reset

Cancel

FIGURE 3-3 cddvdrttest Test Parameter Options Dialog Box for CD-RW

**TABLE 3-4** `cddvdtest` Options for CD-RW

Option	Description
SimulationWrite	Disables or enables simulation write.
WriteSpeed	Speed entered in terms of <i>nX</i> . Speed is set to closest approximation of <i>nX</i> allowed by the device.
WriteDataTrack	Writes a data track in one test loop.
WriteAudioTrack	Writes an audio track in one test loop.
NumberOfLoop	Number of loops in one test pass comprising data and audio track if enabled.
Close	Closes after writing. Cannot write more tracks on the media.
Erase None	Does not erase. The All option erases the whole disk.
Eject	Ejects the disk when the test is completed. Do not enable this option in a multiple pass test session.

## DVD-RW Test Options

This section describes the `cddvdtest` options for DVD-RWs.

**c0t2d0s2(cddvdrttest) Test Parameter Option**

**Configuration:**  
Controller:sd1  
Device:/vol/dev/rdisk/c0t2d0/unknown\_format  
Device:ASUS DRW-0402P/D 1.05 03/05/23 P  
IONEER  
Block size = 2048  
Control:Streaming control  
Write speed: 1235 k/s  
Read speed: 8291 k/s  
Digital Audio Support :succeeded  
Digital Audio Accuracy :succeeded  
Write Mode:  
TAO Mode:DATA AUDIO  
SAO Mode:DATA AUDIO  
DAO Mode:DATA AUDIO

**Options:**

**Media type:** DVD-RW

**Simulation Write:** ☒ Enable ☐ Disable

**Write Speed:** 2

**Image Size:** 2MB

**Erase:** none

**Eject:** ☐ Enable ☒ Disable

---

**Within Instance:** Apply

---

**Across All Instances:** Apply

Reset Cancel

**FIGURE 3-4** `cddvdtest` Test Parameter Options Dialog Box for DVD-RW

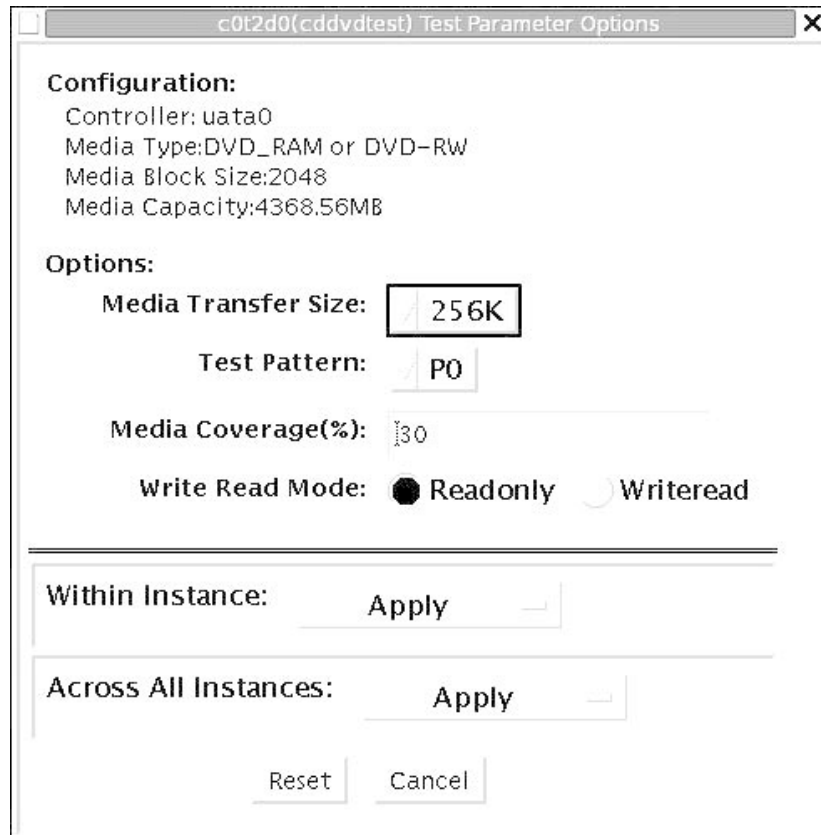


**TABLE 3-5** `cddvdtest` Options for DVD-RW

Options	Description
SimulationWrite	Disables or enables simulation write.
WriteSpeed	Speed entered in terms of <i>nX</i> . Speed is set to closest approximation of <i>nX</i> allowed by the device.
ImageSize	Specifies track size, how much data is written: 2 MB, 10 MB, 2 GB, 4 GB.
Erase None	Do not erase the media.
Fast	Erases with Fast option. Only the Table of Contents is erased.
All	Erases with All option. Erases whole disk surface.
Eject	Ejects the disk when the test is completed. Do not enable this option in a multiple pass test session.

## DVD-RAM Test Options

This section describes the `cddvdtest` options for DVD-RAM.



**FIGURE 3-5** `cddvdtest` Test Parameter Options Dialog Box for DVD-RAM

**TABLE 3-6** `cddvdtest` Options for DVD-RAM

Options	Description
Media Transfer Size	Specifies the media test size in Kbytes.
Test Pattern	Specifies the test pattern: P0, P1, or P2 (Default P0). The following options are supported: <ul style="list-style-type: none"><li>• P0 = 0000000000000000</li><li>• P1 = ff00ff00ff00ff00</li><li>• p2 = ffffffffffffff</li></ul>
Media Coverage	Specifies the percentage of media coverage.
Write Read Mode	Specifies either Readonly or Writeread.

## cddvdtest Test Modes

This test supports Connection and Functional test modes for all media types. The test behavior in each mode for the different media types is explained in the following tables.

### CD-ROM Test Modes

[TABLE 3-7](#) describes the test modes for CD-ROMs.

**TABLE 3-7** Supported Test Modes for CD-ROM

Test Mode	Description
Connection	<code>cddvdtest</code> verifies that a CD-ROM drive is connected to and configured in the system.
Functional	The test registers a failure if the device is found to be busy. This is because SunVTS tests make the assumption that all the resources will be available for testing in the Functional test and the unavailability of a device is interpreted as an indication of a fault condition.

# DVD-ROM Test Modes

TABLE 3-8 describes the test modes for DVD-ROMs.

**TABLE 3-8** Supported Test Modes for DVD-ROM

Test Mode	Description
Connection	Requests and displays information from the drive and reads two blocks of data from the media to confirm connectivity. An error is reported if no media is loaded in the drive.
Functional	Requests and displays information from the drive, then reads data from the media based on the options that are set in the Test Parameter Options dialog box. An error is reported if no media is loaded in the drive.

# CD-RW and DVD-RW Test Modes

TABLE 3-9 describes the test modes for DVD-RWs.

**TABLE 3-9** Supported Test Modes for CD-RW and DVD-RW

Test Mode	Description
Connection	Shows basic drive information and supporting modes. Shows disk Table of Contents (TOC).
Functional	<p>The following subtests are done in sequence. Erase - Erases data if DVD-RW media is not blank, or CD-RW media if full.</p> <ul style="list-style-type: none"><li>• Simulation (optional) - Performs a write track with Laser turned off. This mode tests writing function without data written to the media.</li><li>• Write - Writes to media with predefined data patterns of 0..ff hex. For CD media track can be specified as data or audio track.</li><li>• Read - Reads the written data.</li><li>• Compare - Compares write/read data, reports failure if miscompared.</li><li>• Blank media (optional).</li><li>• Read Disk/Track - Reads and shows disk table of contents.</li><li>• Ejects (optional).</li></ul>

# DVD-RAM Test Modes

TABLE 3-10 describes the test modes for DVD-RAM.

TABLE 3-10 Supported Test Modes for DVD-RAM

Test Mode	Description
Connection	Requests and displays information from the drive and reads two blocks of data from the media to confirm connectivity. An error is reported if no media is loaded in the drive.
Functional	Requests and displays information from the drive, then reads data from the media based on the options that are set in the Test Parameter Options dialog box. An error is reported if no media is loaded in the drive.

## cddvdtest Command-Line Syntax

cddvdtest has different command-line syntax for each media type.

## CD-ROM Command-Line Syntax

```
/opt/SUNWvts/bin/cddvdtest standard_arguments -o dev=  
raw_device_name,mode=mode,read=random|sequential,data=%_of_data,vol=  
volume,audio=enable|disable,type=CD_type
```

TABLE 3-11 CD-ROM Command-Line Syntax

Argument	Description
dev= <i>raw-device-name</i>	Specifies the name of the raw device to be tested.
read= <i>random sequential</i>	Indicates random or sequential read access.
data= <i>%-of-data</i>	Sets the percentage of data to be tested. You can specify 0 to 100 percent.

**TABLE 3-11** CD-ROM Command-Line Syntax (*Continued*)

Argument	Description
<code>vol=<i>volume</i></code>	Controls the audio volume. You can specify 0 through 255. The default is 255.
<code>audio=<i>enable disable</i></code>	Enables or disables the audio test. You must connect headphones or a speaker to the audio jack on the CD player to hear audio output.
<code>type=<i>CD-type</i></code>	Specifies the type of CD used for the test. The choices are <code>pdo</code> , <code>multi-session</code> , <code>sunos</code> and <code>other</code> . The default is <code>other</code> .

## DVD-ROM Command-Line Syntax

`/opt/SUNWvts/bin/cddvdtest` *standard\_arguments* **-o** **dev**=*device\_name*,**read**=*random|sequential*,**data**=%\_of\_data

**TABLE 3-12** DVD-ROM Command-Line Syntax

Argument	Description
<b>dev</b> = <i>device_name</i>	Specifies the name of the device to test, for example <code>/dev/rdisk/cntndn</code> .
<b>read</b> = <i>random sequential</i>	Indicates random or sequential read access.
<b>data</b> =%_of_data	Sets the percentage of data to test. Use a number from 0 to 100.

## CD-RW Command-Line Syntax

`/opt/SUNWvts/bin/cddvdtest` *standard\_arguments* **-o** **dev**=*cntndnsn*,**media**=*CD-RW,nosim*,**speed**=*n*,**nodata**,**noaudio**,**loop**=*n*,**close**,**erase**=*{none,all}*,**eject**

**TABLE 3-13** CD-RW Command-Line Syntax

Argument	Description
<b>dev</b> = <i>cntndnsn</i>	Specifies the device under test.
<b>media</b> = <i>CD-RW</i>	Specifies the media.
<b>nosim</b>	Disables simulation write.
<b>speed</b> = <i>n</i>	Specifies the speed. Enter the speed in terms of <i>nX</i> .
<b>nodata</b>	Disables data track test.

**TABLE 3-13** CD-RW Command-Line Syntax *(Continued)*

Argument	Description
<b>noaudio</b>	Disables audio track test.
<b>loop=<i>n</i></b>	Specifies 1 to 40, the number of loops in one test pass.
<b>close</b>	Closes track after the test, no track can be added.
<b>erase={none, all}</b>	none - Does not erase media after test complete. all - Erases entire disk.
<b>eject</b>	Ejects disk after test completed.

## DVD-RW Command-Line Syntax

**/opt/SUNWvts/bin/cddvdtest** *standard\_arguments* **-o dev=cntndnsn,media={DVD-RW,DVD+RW},nosim,speed=*n*,imagesize={2MB,10MB,2GB,4GB},erase={none,fast,all},eject**

**TABLE 3-14** DVD-RW Command-Line Syntax

Argument	Description
<b>dev=cntndnsn</b>	Specifies the device under test.
<b>media={DVD-RW, DVD+RW}</b>	Specifies the media.
<b>nosim</b>	Disables Simulation Write.
<b>speed=<i>n</i></b>	Specifies the speed. Enter the speed in terms of <i>nX</i> .
<b>imagesize={2MB, 10MB, 2GB, 4GB}</b>	Specifies the image size used in the write/read test.
<b>erase={none, fast, all}</b>	.none - Does not erase media after test complete. fast - Erases the last track added. all - Erases entire disk.
<b>eject</b>	Ejects the media.

# DVD-RAM Command-Line Syntax

`/opt/SUNWvts/bin/cddvdtest standard_arguments -o dev=cntndnsn,  
rawiosize={32K,64K,128K,256K,512K},rawpattern={P0,P1,P2},rawcover={1% -  
100%},rawrw={Readonly, Writeread}`

TABLE 3-15 DVD-RAM Command-Line Syntax

Argument	Description
<b>dev</b> = <i>cntndnsn</i>	Specifies the device under test.
<b>rawiosize</b> = <i>32K,64K,128K,256K,512K</i>	Specifies the <i>iosize</i> for write and read operation. Default is 256K.
<b>rawpattern</b> = <i>P0,P1,P2</i>	The following options are supported. Default is P0. P0 = 0000000000000000 P1 = ff00ff00ff00ff00 p2 = ffffffffffffffffff
<b>rawcover</b> = <i>1-100</i>	Specifies the media coverage in percent. Default is 30.
<b>rawrw</b> = <i>Readonly, Writeread</i>	Selects the Read and Write operations. Default is Readonly.



## CPU Test (cputest)

- 
- [“cputest Description” on page 33](#)
  - [“cputest Options” on page 34](#)
  - [“cputest Test Modes” on page 37](#)
  - [“cputest Command-Line Syntax” on page 37](#)
- 

### cputest Description

The `cputest` checks specific aspects of processor datapath functionality.

The `cputest` comprises two subtests:

- `g0` subtest – This subtest is not supported on x86 platforms.
- CUC subtest – Tests a processor’s ability to correctly execute a Compress/Uncompress/Compare (`cmp`) command sequence. As the CUC subtest runs, it creates four files in the `/tmp/sunvts` directory. The CUC subtest uses the following file naming conventions where *nnn* represents the processor unit number and *x* represents a random character string appended to the file name:
  - `PnnnPx` – original pattern file (size determined by the `cputest` File Size option)
  - `PnnnZx` – Compressed version of the pattern file
  - `PnnnUx` – Uncompressed data from the `PnnnZx` file
  - `PnnnCx` – Comparison data between the `PnnnPx` and `PnnnUx` files

With the `cputest` file retention mode, you can control whether these files are deleted or not, so that in the event of a miscompare, you can view the contents of the files to analyze the miscompared data. The exact names of the files are displayed in the SunVTS message window whenever the files are saved, such as when there is a compression miscompare or when the file retention mode is set to save. Refer to [“cputest Options” on page 34](#) for more details.



---

**Caution** – Do not run the CUC subtest with the retention mode set to *save* for numerous passes, otherwise the files that are saved in `/tmp/sunvts` can fill the `/tmp` capacity. If `/tmp` is mounted to the swap area, the swap space may become filled to capacity.

---

---

**Note** – Only one instance of `cputest` per processor is possible.

---

---

**Note** – When `cputest` is run with other tests, it may give the error message `exec'd program compress failed with code 1`. This can be an indication of a failed compression program, not necessarily a failed CPU. If this occurs, stop all other tests and run `cputest` alone. If the message occurs again, the CPU is failing.

---

---

**Note** – SunVTS does not support processor sets. If processor sets are defined, you must first delete the processor sets before running SunVTS.

---

---

## cputest Options

To reach the following dialog box, right-click on the test name in the System Map and select Test Parameter Options. If you do not see this test in the System Map, you might need to expand the collapsed groups, or your system might not include the device appropriate to this test. Refer to the *SunVTS User's Guide* for more details.

**cpu-unit4(cputest) Test Parameter Options**

**Configuration:**  
Type:SPARC V9 based FPU  
clock-frequency: 336 MHz.

**Options:**

Iterations: ▲▼ 200

Test List:  
☒ CUC  
☒ g0

File Size (KBytes): ▲▼ 1024

File Retention Mode: ▼ OnError

Bound to:  
Processor Affinity: Processor 4  
Processor 5

---

Within Instance: Apply ▢

---

Across All Instances: Apply ▢

Reset Cancel

**FIGURE 4-1** cputest Test Parameter Options Dialog Box

**TABLE 4-1** `cputest` Option Dialog Box Descriptions

Option	Description
Iterations	Specifies the number of times to loop on the selected subtests. Use the up and down arrow keys to select a value from 1 to 8192. The default varies depending on the SunVTS test mode.
Test List	Specifies which subtests to run. The choices are: <ul style="list-style-type: none"><li>• CUC – The compress/uncompress/compare subtest.</li></ul> Refer to the general test description at the beginning of this chapter for subtest descriptions. If no subtest is selected, both subtests run.
File Size (KBytes)	Specifies the size of the CUC pattern file in KBytes. Select a value from 1 to 8192. The default varies depending on the SunVTS test mode.
File Retention Mode	Specifies whether the <code>cputest</code> removes the CUC pattern files or not. The choices are: <ul style="list-style-type: none"><li>• Purge – Unconditionally removes the four subtest files.</li><li>• Save – Does not remove any of the four subtest files.</li><li>• OnError – Removes the four subtest files unless the CUC resulted in a miscompare. In this case, do not remove the files.</li></ul> The default is OnError. Refer to the Caution at the beginning of this chapter regarding the Save value.
Processor Affinity	Although the Test Parameter Options dialog box displays the processor affinity Bound To selection box, the processor that corresponds to this instance of the <code>cputest</code> is determined when the SunVTS kernel probes for devices. Therefore, switching processor affinity in this dialog box is not supported.

---

# cputest Test Modes

The following table describes how the `cputest` functions in the different test modes.

**TABLE 4-2** `cputest` Supported Test Modes

Test Mode	Description
Connection	Both subtests are selected. The test options are fixed with the following values: <ul style="list-style-type: none"><li>• Iterations=5</li><li>• File Size=64 KBytes</li><li>• File retention=OnError</li></ul>
Functional	Both subtests are selectable, and all the test options are available to scale the <code>cputest</code> as needed.
Online	Supported.

---

# cputest Command-Line Syntax

`/opt/SUNWvts/bin/cputest standard-arguments -o dev=device-name,count=count-number,test=testlist,size=file-size,retain=mode`

**TABLE 4-3** `cputest` Command-Line Syntax

Argument	Description
<b>dev</b> =device-name	Specifies the name of the device to test, for example, <code>cpu-unit5</code>
<b>count</b> =count-number	Defines the number of times to loop on the subtests. Use a number from 1 to 8192. The default is 200.

**TABLE 4-3** `cputest` Command-Line Syntax (*Continued*)

Argument	Description
<b>test</b> = <i>testlist</i>	Specifies which subtests to run. The choices are: <ul style="list-style-type: none"><li>• CUC</li></ul>
<b>size</b> = <i>file-size</i>	Specifies the size of the CUC pattern file in KBytes. Select a value from 1 to 8192. The default is 1024.
<b>retain</b> = <i>mode</i>	<p>Specifies whether the <code>cputest</code> removes the CUC pattern files or not. The choices are:</p> <ul style="list-style-type: none"><li>• Purge – unconditionally remove the four subtest files</li><li>• Save – do not remove any of the four subtest files</li><li>• OnError – remove the four subtest files unless the CUC resulted in a miscompare. In this case do not remove the files.</li></ul> <p>The default is OnError.</p> <p>Refer to the Caution at the beginning of this chapter regarding the Save value.</p>

# Cryptographics Test (cryptotest)

- 
- “cryptotest Description” on page 39
  - “cryptotest Subtests” on page 40
  - “cryptotest Options” on page 40
  - “cryptotest Test Modes” on page 46
  - “cryptotest Command-Line Syntax for mcatest” on page 46
  - “cryptotest Command-Line Syntax for vcatest” on page 47
  - “cryptotest Command-Line Syntax for dcatest” on page 47
- 

## cryptotest Description

The Encryption Framework in Solaris 10 provides a user level API for access to cryptographic accelerators. This API is based on the PKCS#11 standard. Cryptographic accelerators are referred to as PKCS#11 tokens, and each cryptographic algorithm that the token accelerates is referred to as a mechanism.

`cryptotest` tests the mechanism supported by PKCS#11 tokens in the Solaris OS.

`cryptotest` supports the Sun Crypto Accelerator 500, 1000, 4000, 6000, and the UltraSPARC T1 (high-end processor with chip multithreading [CMT]) Crypto Provider. PKCS documentation is available at:  
<http://www.rsasecurity.com/rsalabs/PKCS>

**TABLE 5-1** Definitions of the Mechanisms Tested by `cryptotest`

Algorithm	Description
AES	CTR and CBC modes
Diffie-Hellman	Crypto mechanism
DSA	Digital signature algorithm
DES	Data encryption standard as defined in FIPS PUB 46-3

**TABLE 5-1** Definitions of the Mechanisms Tested by `cryptotest` (Continued)

Algorithm	Description
MD5 RSA	Data Security MD5 message-digest algorithm
RSA	Public key cryptosystem
SHA1	Secure hash algorithm
RNG	Random number generator algorithm

## cryptotest Subtests

**TABLE 5-2** `cryptotest` Subtests

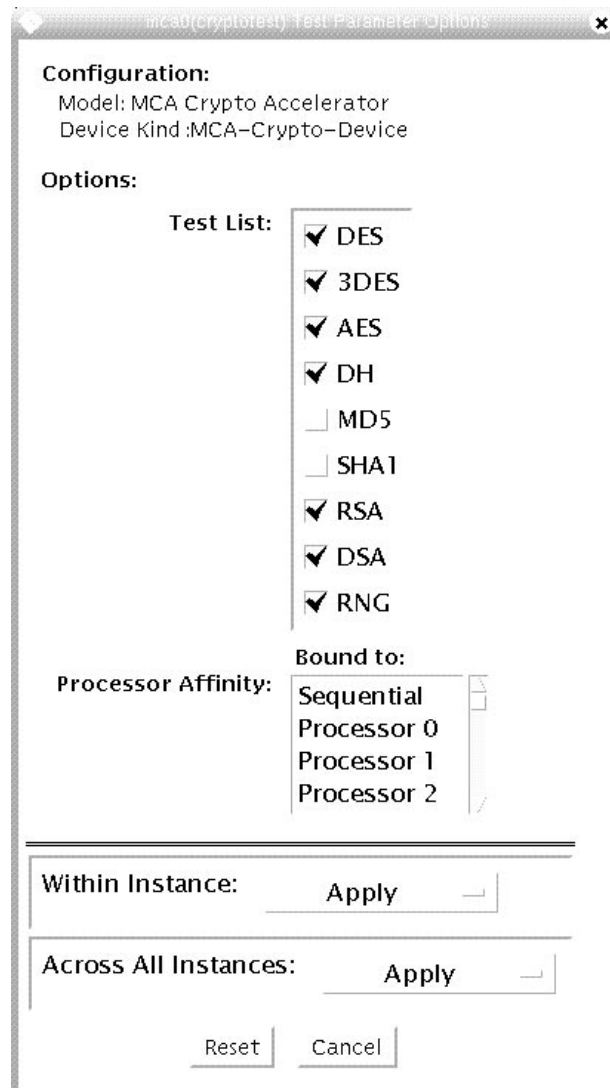
Subtest	Description
AES	Tests CTR and CBC modes
Diffie-Hellman	Tests the crypto mechanism
DES	Tests DES bulk encryption
3DES	Tests 3DES bulk encryption
RSA	Tests RSA public and private keys
DSA	Tests DSA signature verification
RNG	Tests random number generation

## cryptotest Options

To reach the following dialog boxes, right-click on the test name in the System Map and select Test Parameter Options. Because graphics tests can test multiple types of frame buffers, the test name that is displayed will correspond to the particular framebuffer being tested. If you do not see this test in the System Map, you might need to expand the collapsed groups, or your system might not include the device appropriate to this test. Refer to the *SunVTS User's Guide*.



## mcatest Options

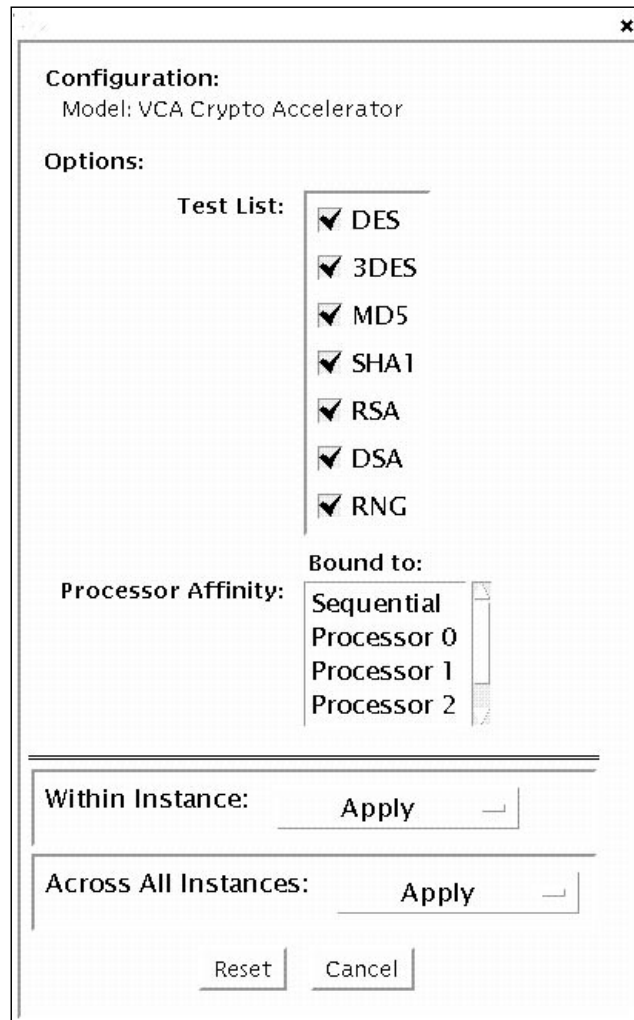


**FIGURE 5-1** mcatest Test Parameter Options Dialog Box

**TABLE 5-3** `mcatest` Options

Option	Description
AES	Tests CTR and CBC modes
Diffie-Hellman	Tests the crypto mechanism
DES	Tests DES bulk encryption
3DES	Tests 3DES bulk encryption
MD5	Data security MD5 message-digest algorithm.
SHA1	Secure hash algorithm.
RSA	Tests RSA public and private keys

## vcatest Options

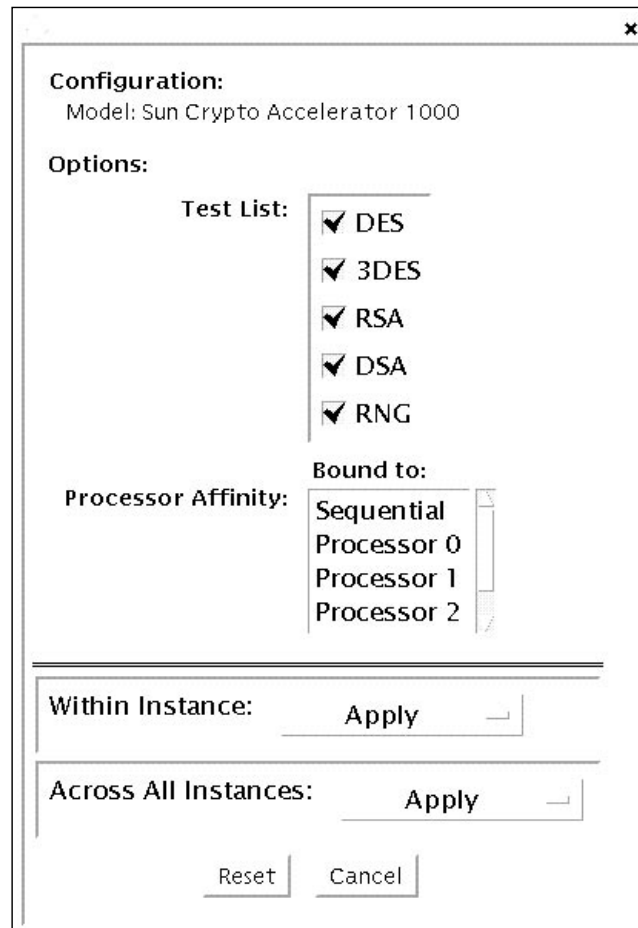


**FIGURE 5-2** vcatest Test Parameter Options Dialog Box

**TABLE 5-4** `vcatest` Options

Option	Description
DES	Tests DES bulk encryption
3DES	Tests 3DES bulk encryption
MD5	Data security MD5 message-digest algorithm.
SHA1	Secure hash algorithm.
RSA	Tests RSA public and private keys
DSA	Tests DSA signature verification
RNG	Tests random number generation

# dcatest Options



**FIGURE 5-3** dcatest Test Parameter Options Dialog Box

**TABLE 5-5** dcatest Options

Option	Description
DES	Tests DES bulk encryption
3DES	Tests 3DES bulk encryption

**TABLE 5-5**    `dcatest` Options (Continued)

Option	Description
RSA	Tests RSA public and private keys
DSA	Tests DSA signature verification
RNG	Tests random number generation

# cryptotest Test Modes

**TABLE 5-6**    `cryptotest` Supported Test Modes

Test Mode	Description
Functional	Runs the full set of tests.

# cryptotest Command-Line Syntax for mcatest

`/opt/SUNWvts/bin/cryptotest -f -o dev=mca2,tl=testlist`

**TABLE 5-7**    `cryptotest` Command Line Syntax for `vcatest`

Option	Description
<b>dev=mcaN</b>	Specifies the instance of the device to test such as <code>mca0</code> or <code>mca2</code> . Defaults to <code>mca0</code> if not included. <i>N</i> specifies the placement of the instance number of the device being tested.
<b>tl=testlist</b>	Specifies the list of subtests to be performed. The subtests for <code>tl</code> are separated by the + (plus) character. The supported subtests are AES, Diffie-Hellman (DH), DES, 3DES, DSA, RSA, and RNG, so <code>tl=AES+DH+DES+3DES+DSA+RSA+MD5+SHA1+RNG</code> enables all subtests. You can also insert <code>tl=all</code> , which performs all tests. Defaults to <code>all</code> if no subtests are specified.

# cryptotest Command-Line Syntax for vctest

`/opt/SUNWvts/bin/cryptotest -f -o dev=vca2,t1=testlist`

TABLE 5-8 cryptotest Command Line Syntax for vctest

Option	Description
<b>dev=vcaN</b>	Specifies the instance of the device to test such as vca0 or vca2. Defaults to vca0 if not included. N specifies the placement of the instance number of the device being tested.
<b>t1=testlist</b>	Specifies the list of subtests to be performed. The subtests for t1 are separated by the + (plus) character. The supported subtests are DES, 3DES, DSA, RSA, and RNG, so t1=DES+3DES+DSA+RSA+MD5+SHA1+RNG enables all subtests. You can also insert t1=all, which performs all tests. Defaults to all if no subtests are specified.

# cryptotest Command-Line Syntax for dcatest

`/opt/SUNWvts/bin/cryptotest -f -o dev=vca2|dca2,t1=testlist`

**TABLE 5-9** cryptotest Command Line Syntax for dcatest

Option	Description
<b>dev</b> =dcaN	Specifies the instance of the device to test such as dca0 or dca2. Defaults to dca0 if not included. N specifies the placement of the instance number of the device being tested.
<b>t1</b> =testlist	Specifies the list of subtests to be performed. The subtests for t1 are separated by the + (plus) character. The supported subtests are DES, 3DES, DSA, RSA, and RNG, so t1=DES+3DES+DSA+RSA+MD5+SHA1+RNG enables all subtests. You can also insert t1=all, which performs all tests. Defaults to all if no subtests are specified.



## Disk and Diskette Drives Test (disktest)

- 
- “[disktest Test Requirements](#)” on page 50
  - “[disktest Subtests](#)” on page 52
  - “[disktest Test Options](#)” on page 52
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- 

### disktest Description

disktest verifies the functionality of hard drives and diskette drives using three subtests (see [TABLE 6-1](#))—Media, File System, and Asynchronous I/O.

Most disk drives are supported, including SCSI disks, native or SCSI floppy disks, and IPI drives. The type of drive being tested is displayed at the top of the Test Parameter Options dialog box.

---

**Note** – Disk partitions could range from 0 to 15. disktest can be performed on any of these selected partitions.

---

The disktest Test Parameter Options dialog box shows all the partitions that are available for testing. The file System subtest can only be run if the selected partition is mounted. The WriteRead option of the Media subtest is allowed only if a selected partition is *not* mounted.

---

# disktest Test Requirements

By default, `disktest` does not mount any partitions. To specify that SunVTS premounts all mountable partitions, set the environment variable `BYPASS_FS_PROBE` to 0 (zero) before starting SunVTS. Premounting can be disabled by disabling `BYPASS_FS_PROBE` or changing it to a value other than 0 (zero).

The mount point used by `disktest` is the word *disktest* appended by the name of the disk partition. For example, if the disk partition name is `/dev/dsk/c0t3d0s0`, `disktest` mounts the partition as superuser under the name `/disktest_c0t3d0s0`.



---

**Caution** – If a power failure occurs or if `disktest` is terminated abruptly while the Media subtest is running in WriteRead mode, disk data could possibly be corrupted.

---



---

**Caution** – Running the Media subtest on a disk partition in the WriteRead mode might cause data corruption if the same partition is being used by other applications. Run SunVTS in the offline mode only when there are no other applications running.

---

`disktest` tests the `disktest` drive whether the Volume Management software is running or not. The following mount point names are used:

- If the Volume Management software *is* running, `disktest` tests the disk drive with the mount point name in the `/etc/mnttab` file.
- If the Volume Management software *is not* running, `disktest` tests the disk drive with the device name `dev=/dev/diskette`. Do not edit the `/etc/vold.conf` file to change the diskette drives. Currently, the SunVTS software is hard-coded to use these path names as the default logic names.

Loading an option file (refer to the *SunVTS User's Guide* for option file details) that was created when `BYPASS_FS_PROBE` was set to 0 (zero) might not work if the `BYPASS_FS_PROBE` environment variable is no longer set to 0. Testing might fail with the following error:

```
SUNWvts.disktest.8088 07/24/98 15:47:22 disktest c0t0d0 FATAL:
"Couldn't get file system information on /disktest_c0t0d0s0,
statvfs() system call failure error: No such file or directory.
```

This error is caused when SunVTS expects to use the predefined mount point names that are created when `BYPASS_FS_PROBE` is set to 0 (zero), but these mount points do not exist while `BYPASS_FS_PROBE` is not set to 0.

To use option files with `disktest`, create two separate option files for the two different states of the `BYPASS_FS_PROBE` environment variable.

When a large number of `disktest` instances are run in write/read mode, tests might fail with messages similar to the following.

```
03/22/03 03:33:40 ctech140 SunVTS5.1ps2: VTSID 8011 disktest.FATAL
c1t0d0: "Failed lock mtab semaphore. "semop" system call failure,
errmsg: Invalid argument." Probable_Cause(s): <disktest instances
exceeds system semaphore operation limitation (default system
limit for seminfo_semmnu = 30)><System software error>
Recommended_Action(s): <Add the line "set semsys:seminfo_semmnu=
0x100" to your /etc/system file and reboot the machine> <If the
problem persists, call your authorized Sun service provider.
```

To avoid this issue, add the following entry to the `/etc/system` file and reboot the system.

```
set semsys:seminfo_semmnu=0x100
```

# disktest Subtests

The following table describes the `disktest` subtests:

**TABLE 6-1** `disktest` Subtests

Subtest	Description
Media subtest	<p>Verifies the disk media by enabling users to run <code>disktest</code> in different modes such as <code>ReadOnly</code>, <code>ReadCompare</code>, and <code>WriteRead</code>. The Media subtest treats the disk partition as one large piece of contiguous data.</p> <p>In <code>WriteRead</code> mode, all instances of <code>disktest</code> communicate through a shared memory service to ensure that they do not overlay the same disk area at the same time. This avoids data corruption.</p> <p>Each of the three modes can run two different methods of disk testings. These are <code>Synchronous I/O</code> and <code>Asynchronous I/O</code>.</p> <p><code>SyncIO</code> – Reads and writes data using <code>Read/Write</code> system calls in a sequential fashion until the specified percentage of media is covered.</p> <p><code>AsyncIO</code> – Reads and writes data using <code>aio</code> library calls such as <code>aioread()</code> and <code>aiowrite()</code> until the specified percentage of media is covered. <code>aiowait()</code> is used to synchronize <code>aio</code> operations.</p>
File System subtest	<p>The File system subtest is used to verify the disk file system integrity. It exercises mounted disk partitions carrying the file system. By default, the test only runs on system-mounted partitions, it does not pre-mount any additional partitions. If you want <code>SunVTS</code> to pre-mount all of the unmounted partitions which have a file system, you have to set the environment variable <code>BYPASS_FS_PROBE</code> to '0' (zero). The test creates two temporary files of the size specified by File System File Size, writes the data patterns and compares the two files against each other.</p>

## disktest Test Options

To reach the following dialog box, right-click on the test name in the System Map and select Test Parameter Options. If you do not see this test in the System Map, you might need to expand the collapsed groups, or your system may not include the device appropriate to this test. Refer to the *SunVTS User's Guide* for more details.

c0t0d0(disktest) Test Parameter Options

**Configuration:**  
Capacity: 8.49GB  
Controller: uata0  
Device Kind: disk

**Options:**

Partition: ☒ 0(/) ☐ 2

Test Media: ☒ Enable ☐ Disable

Media Write Read Mode:

Media Test Method: ☒ SyncIO  
☒ AsyncIO

Media Transfer Size:

Media Coverage(% TB GB MB KB B):

Raw Test Pattern(P0 to P15):

Seek Pattern:

Seek Point(% I O M TB GB MB KB B):

Test File System: ☒ Enable ☐ Disable

File System File Size:

File System Transfer Size:

File System Test Pattern:

Instance:

---

Within Instance:

---

Across All Instances:

FIGURE 6-1 disktest Test Parameter Options Dialog Box

The following table describes the `disktest` option menu for different test modes.

**TABLE 6-2** `disktest` Configurations and Options

<code>disktest</code> Options	Description
Partition	Displays the partition for the Media subtest. If a partition is mounted, its mount point is appended after the partition number, such as <code>1 (/usr)</code> , where 1 is the partition number, and <code>" (/usr) "</code> is the mount point.
Test Media	Enables or disables the media subtest.
Media Write Read Mode	Selects Read-Only or Compare after Read, or Read after Write.
Media Test Method	Selects the media test methods (SyncIO and AsyncIO).
Media Coverage (% TB, GB, MB, KB, B)	Tests all or part of a partition (in percentage or in any of TB, GB, MB, KB, B units)
Raw Test Pattern (P0 to P16)	Specifies the write, read pattern. <ul style="list-style-type: none"> <li>• P0 – Low Frequency Pattern</li> <li>• P1 – Low Transition Density Pattern</li> <li>• P2 – High Transition Density Pattern</li> <li>• P3 – Compliant Jitter Pattern</li> <li>• P4 – Compliant Jitter: RPAT</li> <li>• P5 – Compliant Jitter: CRPAT</li> <li>• P6 – Compliant Jitter: JTPAT</li> <li>• P7 – Compliant Jitter: CJTPAT</li> <li>• P8 – Compliant Jitter: SPAT</li> <li>• P9 – Compliant Jitter: CSPAT</li> <li>• P10 – 8 Bit Cable Pattern</li> <li>• P11 – 16 Bit Cable Pattern</li> <li>• P12 – 8 Bit Xtalk Pattern</li> <li>• P13 – 16 Bit Xtalk Pattern</li> <li>• P14 – MFM Pattern</li> <li>• P15 – Generic Test Patterns</li> <li>• P16 – SATA Test Patterns</li> </ul>
Seek Pattern	Specifies the pattern of the disk head movement. <ul style="list-style-type: none"> <li>• S – Sequential</li> <li>• SR – Sequential Reverse</li> <li>• LS – Low Power Sequential</li> <li>• R – Random</li> <li>• LB – Low Power Butterfly</li> <li>• LR – Low Power Reverse Butterfly</li> <li>• AB – Actuator Butterfly</li> <li>• AR – Actuator Reverse Butterfly</li> </ul>

**TABLE 6-2** disktest Configurations and Options *(Continued)*

disktest Options	Description
Seek Point (% , I, O, M, TB, GB, MB, KB, B)	Specifies the seek point offset for the I/O. You can specify the offset as a percentage or any of TB, GB, MB, KB, B or and I, M, O (Initial, Middle, Outer).
Media Transfer Size	Displays the transfer size of the Media subtest.
Test File System	Selects the File System subtest.
File System File Size	Specifies the size for each of the two temporary files for File System testing.
File System Transfer Size	Displays the transfer size of the file system subtest.
File System Test Pattern	Test pattern of File System subtest.
Connection Test for Hard Disk	<ul style="list-style-type: none"><li>• Option Menu for hard disk partition – 0 - 7 [default]</li><li>• Test Media – [Enable] (fixed to Enable)</li><li>• Media Write Read Mode – [Read Only] (fixed to Read Only)</li><li>• Media Test Method-[SyncIO] (fixed to SyncIO)</li><li>• Media Coverage(%) – 1</li><li>• Media Transfer Size – [2 KB]</li><li>• Test File System – [Disable] (fixed to Disable)</li></ul>

**TABLE 6-2** disktest Configurations and Options *(Continued)*

disktest Options	Description
Online Mode for Hard Disk	<ul style="list-style-type: none"> <li>• Partition – 0 - 7 [default]</li> <li>• Test Media – [Enable] [Disable]</li> <li>• Test Mode – [Read-only~] (fixed to Read-only)</li> <li>• Media Coverage (% TB GB MB KB B) – [10~] (fixed to 10%)</li> <li>• Media Transfer Size – [2KB~] (fixed to 2 KB)</li> <li>• Test File System – [Disable~] (fixed to Disable)</li> <li>• Media Test Method – [SyncIO] [AsyncIO]</li> <li>• Raw Test Pattern – [P15~] (fixed to P15)</li> <li>• Seek Pattern – [S~] (fixed to S)</li> <li>• Seek Point (% TB GB MB KB B) – [I~] (fixed to I)</li> </ul>
Functional Test for Hard Disk	<ul style="list-style-type: none"> <li>• Partition – 0 - 7 [default]</li> <li>• Test Media – [Enable] [Disable]</li> <li>• Media Write Read Mode – [Readonly] [CompareRead] [WriteRead]</li> <li>• Media Test method – [SyncIO] [AsyncIO]</li> <li>• Media Coverage (% TB, GB, MB, KB, B)</li> <li>• Raw Test Pattern (P0 to P16)</li> <li>• Media Transfer Size – [2KB] [16KB] [32KB] [64KB] [128KB] [256KB] [512KB]</li> <li>• Test File System – [Enable] [Disable]</li> <li>• File System File Size – [512KB] [2MB] [8MB] [20MB] [100MB] [200MB]</li> <li>• File System Transfer Size – [512B] [1024B] [10KB] [40KB] [80KB]</li> <li>• File System Test Pattern – [sequential] [0x00000000] [0xffffffff] [0x5aa55aa5] [0xdb6db6db] [random]</li> <li>• Seek Pattern – [S~] (fixed to S)</li> <li>• Seek Point (% TB GB MB KB B) – [I~] (fixed to I)</li> </ul>
Functional Test for Floppy Disk	<ul style="list-style-type: none"> <li>• (under Other-Devices group) – Partition: 0 - 7 [default]</li> <li>• Test Media – [Enable] [Disable]</li> <li>• Media Write Read Mode – [Read-only] [BackupWriteRead]</li> <li>• Media Test Method – [SyncIO] [AsyncIO]</li> <li>• Media Coverage (% TB, GB, MB, KB, B)</li> <li>• Raw Test Pattern (P0 to P16)</li> <li>• Media Transfer Size – [2KB] [10KB] [20KB]</li> <li>• Test File System – [Enable] [Disable]</li> <li>• Floppy File Size – [100KB] [200KB]</li> <li>• Floppy Transfer Size – [512B] [1024B] [10KB]</li> <li>• File System Test Pattern – [sequential] [0x00000000] [0xffffffff] [0x5aa55aa5] [0xdb6db6db] [random]</li> </ul>



# disktest Test Modes

TABLE 6-3 disktest Supported Test Modes

Test Mode	Description
Connection	Only one instance of <code>disktest</code> (which monitors UNIX error messages) is allowed for each disk device. <code>disktest</code> displays messages and reports errors. The test also opens the hard disk, checks the disk configuration, reads a few blocks, and then closes the hard disk. No File System subtest is run. No Write option is available in Connection test mode.
Functional	More than one instance of <code>disktest</code> is allowed for one disk device. The File System subtest, Media subtests, and diskette test can be run in Functional test mode. In Functional mode, <code>disktest</code> performs an additional subtest (Write/Read device buffer subtest) for enclosures.
Online	SunVTS <code>disktest</code> runs the Read Only <code>rawtest</code> with fixed transfer size and fixed <code>rawtest</code> pattern. Both SyncIO and AsyncIO test methods are available. The File system subtest is disabled in the Online test mode. Only one <code>disktest</code> instance could be run in the Online test mode.

# disktest Command-Line Syntax

```
/opt/SUNWvts/bin/disktest standard-arguments [-o partition=[0-7],  
rawsub=[E(nable)|D(isable)],rawrw=[Readonly|CompareRead|WriteRead],  
rawiosize=number [KB|kb|random],rawcover=number  
[TB|GB|MB|KB|B|tb|gb|mb|kb|b|],rawpattern=P|0x<8 number pattern  
[0|1|2|3|4|5|6|8|9|10|11|12|13|14|15],method=[AsyncIO+SyncIO],fssub=  
[E(nable)|D(isable)],fssize=number [K|KB|M|MB|k|kb|m|mb],fsiosize=number  
[K|KB|B|k|kb|b|],fspattern=  
[seq(quential)|0x0(0000000)|0xf(ffffff)|0xa(5a5a5a5)|0x5(a5a5a5a)|ran(dom)|0xd(b6db6
```

*db*)],*dev=device-name*,*seekpattern=[S|SR|LS|R|LB|LR|AB|AR]*,*seekpoint=i(nner)|m(iddle)|o(uter)|number [TB|GB|MB|KB|B|tb|gb|mb|kb|b]*,*runtime=number M*,*errcnt=number*,*wrbuftest=Enable|Disable]* ]

**TABLE 6-4** disktest Command-Line Syntax

Argument	Description
<b>partition=</b> <i>0-7</i>	Specifies the partition number as follows: <ul style="list-style-type: none"> <li>• <i>n</i> – The partition number (slice number), usually 0-7 for SPARC and 0-16 for x86</li> <li>• <i>mount-point</i> – The mount point for the mounted partition that you plan to test</li> </ul> For example: <b>partition=6"/(export)"</b>
<b>rawsub=</b> <i>E(nable) D(isable)</i>	Enables or disables the Media subtest. For example: <b>rawsub= Enable</b>
<b>rawrw=</b> <i>Readonly CompareRead WriteRead</i>	Specifies the Media subtest Read, Compare, and Write mode: <ul style="list-style-type: none"> <li>• Read only</li> <li>• Read twice, Compare (works only with SyncIO method)</li> <li>• Write, Read, Compare, Restore</li> </ul> For example: <b>rawrw=ReadOnly</b>
<b>rawiosize=</b> <i>number[... KB kb...] random</i>	Specifies the media size to transfer. The block size can be specified in kilobytes. For example, 2K to 512K. For example: <b>rawiosize=9</b>
<b>rawcover=</b> <i>number number[TB GB MB KB B tb gb mb kb b]</i>	Specifies media coverage from 0-100 (percentage) of the partition. Media Coverage can also be specified in the following units—TB, GB, MB, KB and B. For example: <b>rawcover=40</b> or <b>rawcover=4GB</b>

**TABLE 6-4** disktest Command-Line Syntax (Continued)

Argument	Description
<b>rawpattern</b> = <i>P(0-16) 0x8 digit data pattern</i>	<p><b>rawpattern</b> could be specified as a predefined pattern set, <i>P(0-16)</i>, or an 8 digit pattern could be specified as: <i>0xaa55aa55+0xff00ff00+0x</i>. The following is a description of the supported predefined patterns:</p> <ul style="list-style-type: none"> <li>• P0 – Low Frequency Pattern</li> <li>• P1 – Low Transition Density Pattern</li> <li>• P2 – High Transition Density Pattern</li> <li>• P3 – Compliant Jitter Pattern</li> <li>• P4 – Compliant Jitter: RPAT</li> <li>• P5 – Compliant Jitter: CRPAT</li> <li>• P6 – Compliant Jitter: JTPAT</li> <li>• P7 – Compliant Jitter: CJTPAT</li> <li>• P8 – Compliant Jitter: SPAT</li> <li>• P9 – Compliant Jitter: CSPAT</li> <li>• P10 – 8 Bit Cable Pattern</li> <li>• P11 – 16 Bit Cable Pattern</li> <li>• P12 – 8 Bit Xtalk Pattern</li> <li>• P13 – 16 Bit Xtalk Pattern</li> <li>• P14 – MFM Pattern</li> <li>• P15 – Generic Test Patterns</li> <li>• P16 – SATA Test Patterns</li> </ul> <p>For example: <b>rawpattern</b>=P1</p>
<b>method</b> = <i>AsyncIO+SyncIO</i>	<p>Specifies the Media access method. You can choose to use either or both methods . If you use both access methods together, you must insert a '+' between the two:</p> <ul style="list-style-type: none"> <li>• AsyncIO: Runs the asynchronous I/O test, using the async read/write feature of the Solaris disk driver</li> <li>• SyncIO: Runs the synchronous I/O test.</li> </ul> <p>For example: <b>method</b>=AsyncIO</p>
<b>fssub</b> = <i>E(nable) D(isable)</i>	<p>Enables or disables the File System subtest. File System subtest runs on a mounted partition with a File System.</p>
<b>fsiosize</b> = <i>number[K KB B k kb b]</i>	<p>Indicates the size of the file system subtest I/O transfer in bytes or Kilobytes:</p> <ul style="list-style-type: none"> <li>• B b – bytes</li> <li>• K k KB kb – Kilobytes</li> </ul> <p>512B 1024B 10KB 40KB 80KB</p>

**TABLE 6-4** disktest Command-Line Syntax (Continued)

Argument	Description
<b>fspattern=</b> <code>[seq(quential) 0x0(0000000) 0xf(ffffff) 0xa(5a5a5a5) 0x5(a5a5a5a) ran(dom) 0xd(b6db6db)]</code>	<p>Specifies the file system data pattern as sequential or random or one of the patterns selected from the list. <code>[seq(quential) 0x0(0000000) 0xf(ffffff) 0xa(5a5a5a5) 0x5(a5a5a5a) ran(dom) 0xd(b6db6db)]</code></p> <p>For example:</p> <ul style="list-style-type: none"> <li>• <b>fspattern=0xa</b></li> <li>• <b>fspattern=seq</b></li> </ul>
<b>dev=device-name</b>	<p>Specifies the name of the disk to be tested.</p> <p>For example: c0t3d0.</p>
<b>seekpattern=</b> <code>[S SR LS R LB LR AB AR]</code>	<p>disktest supports the following pattern types:</p> <ul style="list-style-type: none"> <li>• S – Sequential</li> <li>• SR – Sequential Reverse</li> <li>• LS – Low Power Sequential</li> <li>• R – Random</li> <li>• LB – Low Power Butterfly</li> <li>• LR – Low Power Reverse Butterfly</li> <li>• AB – Actuator Butterfly</li> <li>• AR – Actuator Reverse Butterfly</li> </ul> <p>For example: seekpattern=S</p>
<b>seekpoint=</b> <code>[i( inner) m(iddle) o(uter) number  [TB GB MB KB B tb gb mb kb b]</code>	<p>Specifies the seek-point for the I/O. This could be specified either in terms of the range - inner, middle and outer. Or in terms of absolute seek location. The absolute location is specied by a number followed by any of the following units <code>[TB GB MB KB B tb gb mb kb b]</code>.</p> <p>For example:</p> <ul style="list-style-type: none"> <li>• <b>seekpoint=I</b>, starts the I/O from block 1.</li> <li>• <b>seekpoint=M</b>, starts the I/O from middle offset of the partition.</li> </ul>
<b>runtime=[number M]</b>	Specifies the test run time in minutes.
<b>errcnt=number</b>	Specifies the maximum error count.
<b>wrbuftest=[Enable Disable]</b>	Enables or disables the Write/Read Buffer test.

The following example shows how to run `disktest` on a partition `"0"` (which is mounted under `"/"`) for the disk device `c0t0d0`. The media subtest is enabled in `ReadOnly` mode using the `SyncIO` method. The coverage specified is 30% with 512 KB transfer size. The File System subtest is disabled.

```
# /opt/SUNWvts/bin/disktest -f -o partition=0"(/)", rawsub=Enable,  
rawrw=ReadOnly, method=SyncIO, rawcover=30, rawiosize=512KB,  
fssub=Disable, dev=c0t0d0
```



## Data Translation Look-Aside Buffer (dtlbtest)

- 
- [“dtlbtest Description” on page 63](#)
  - [“dtlbtest Options” on page 65](#)
  - [“dtlbtest Command-Line Syntax” on page 66](#)
- 

### dtlbtest Description

The `dtlbtest` verifies the proper functioning of DTLBs, but it does not measure performance of DTLBs.

This test verifies the following function of DTLBs:

- Hit/miss tests – Stress DTLBs by generating large numbers of TLB hits and misses.
- Address Pattern tests – Verify that DTLBs correctly translate virtual addresses to physical addresses with stressful address patterns.

`dtlbtest` detects the installed CPU type and interprets the CPU architecture.

`dtlbtest` is classified as an exclusive SunVTS test because its accuracy and coverage depend significantly on the system’s background *quietness*. For the best result, avoid performing any other SunVTS tests in the background, and also disable all user processes and nonessential daemons.



---

**Caution** – This is an exclusive mode test. This test can not be run in parallel with any other tests or applications.

---

---

**Note** – To test 4 MByte pages, Intimate Shared Memory (ISM) must be enabled. If not already present, append the following to `/etc/system` and reboot:

```
set  shmsys:shminfo_shmmax=0xFFFFFFFFFFFFFFFF
set  shmsys:shminfo_shmmmin=1
set  shmsys:shminfo_shmmni=100
set  shmsys:shminfo_shmseg=10
```

---



# dtlbtest Options

To reach the following dialog box, select the exclusive test mode and right-click on the test name in the System Map and select Test Parameter Options. If you do not see this test in the System Map, you might need to expand the collapsed groups, or your system may not include the device appropriate to this test. Refer to the *SunVTS User's Guide* for more details.

**Options:**

Processor ID: All

Test type: Both

Page size: All

Addr pat test loop: 1

Address pattern seed: 0

Processor Affinity: Bound to:  
Sequential  
Processor 16  
Processor 18  
Processor 19

Within Instance: Apply

Across All Instances: Apply

Reset Cancel

FIGURE 7-1 dtlbtest Test Parameter Options Dialog Box

**TABLE 7-1** dtlbtest Options

Option	Description
Processor ID	Specifies the CPU ID for testing. The default value is all.
Test Type	Specifies the type of test need to be performed, Hit/miss, address pattern, or both. The default value is both.
Page Size	Specifies the DTLB page size to test. The default value is all. Possible page size values are 8k, 64k, 512K, and 4M.
Address Pattern loop count	Specifies the address pattern loop count. The default value is 1. Loop count value ranges between 1 and 1000.
Address pattern seed value	Specifies the address pattern seed value. The default value is 0.

**TABLE 7-2** dtlbtest Supported Test Modes

Test Mode	Description
Exclusive	Performs only the dtlbtest (full test).

## dtlbtest Command-Line Syntax

```
/opt/SUNWvts/bin/dtlbtest [-scruvdtlxf] [-p n] [-i n] [-w n]
[-o cpu=N,test=x,psize=x,aloop=n,aseed=n]
```

**TABLE 7-3** dtlbtest Command-Line Syntax

Argument	Description
<b>cpu</b> = <i>cpuId</i>	Specifies the CPU ID. The default value is all.
<b>test</b> = <i>testType</i>	Specifies the type of test h, a, or both. The default value is both.
<b>psize</b> = <i>pageSize</i>	Specifies the page size to be tested. The default value is all.
<b>aloop</b> = <i>loopCount</i>	Specifies the address pattern loop count. The default value is 1.
<b>aseed</b> = <i>seedValue</i>	Specifies the address pattern seed value. The default value is 0.

## Emulex HBA Test (emlxtest)

---

`emlxtest` verifies the proper functioning of the Emulex LP10000 Dual and Single Port 2 Gigabyte Fibre Channel PCI host bus adapters (HBAs). The supported HBAs are 133 MHz PCI-X 2 Gigabyte Fibre Channel based on the Emulex LP10000-S (Amber-2r) and LP10000DC-S (Crystal-2r) HBAs, which are the single and dual channel versions of the Emulex LP10000 line of products.

---

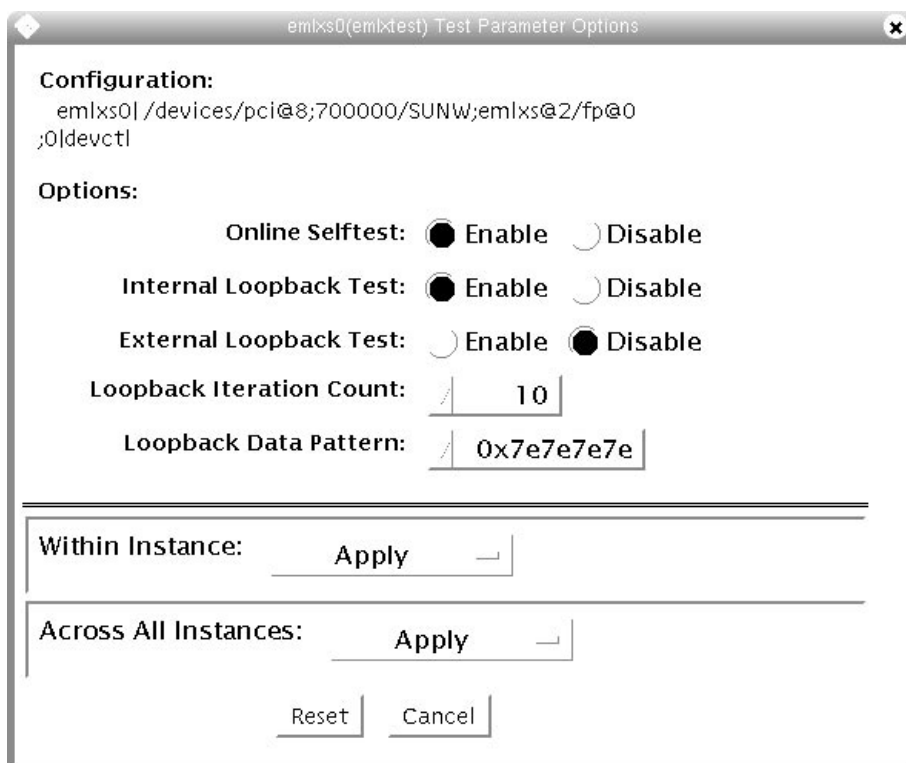
**Note** – `emlxtest` must be performed as a standalone test. Ensure that no other tests are running at the same time as the `emlxtest`.

---

---

### `emlxtest` Options

To reach the following dialog box, right-click on the test name in the System Map and select Test Parameter Options. If you do not see this test in the System Map, you might need to expand the collapsed groups, or your system might not include the device appropriate to this test. Refer to the *SunVTS User's Guide* for more details.



**FIGURE 8-1** emlxtest Test Parameter Options Dialog Box

The following table lists the test parameter options for `emlxtest`.

**TABLE 8-1** `emlxtest` Options

<code>emlxtest</code> Options	Description
Online Selftest	Evaluates the functionality of ISP hardware by performing the following tests: <ul style="list-style-type: none"> <li>• Transmit FIFO test</li> <li>• Receive FIFO test</li> <li>• SRAM test</li> <li>• Misc. Register tests</li> </ul> Run by default, but can be deselected.
Internal Loopback Test	Performs internal loopback test within the host adapter ISP hardware at the interface. This test is done with data sourcing from the system memory and going to the system memory. The desired data pattern, transfer length, and iteration count can be selected with the test parameters menu. Run by default, but can be deselected.
External Loopback Test	Performs an external loopback test. This test is performed with data sourcing from the system memory and going to the system memory. The desired data pattern, transfer length, and iteration count can be selected via the test parameters menu. This is an intervention test, because a loopback cable is needed from the transceiver to the receiver of the Emulex port when testing this port by itself. This subtest can also test the entire fibre channel loop when the loop is connected to the storage to be tested. Not run by default, but can be selected.
Loopback Iteration Count	Sets the number of times to loop the internal 10-bit, internal 1-bit, and external loopback tests. Default value is 10.
Loopback Data Pattern	Selects the data pattern to loop for the internal 10-bit, internal 1-bit, and external loopback tests. Default value is <code>0x7e7e7e7e</code> .

# emlxtest Test Modes

TABLE 8-2 emlxtest Supported Test Modes

Test Mode	Description
Connection	Opens and closes the Emulex port.
Exclusive	Performs the full set of tests.

## emlxtest Command-Line Syntax

```
/opt/SUNWvts/bin/emlxtest standard-arguments
-x -v -o dev=device-name,run-connect=Yes|No,selftest=
Enable|Disable,ilb=Enable|Disable,elb=Enable|Disable,lbfcount=iteration-count,
lbfpattern=hex-pattern
```

**Note** – The `-x` option must be used from the command line.

TABLE 8-3 emlxtest Command-Line Syntax

Argument	Description
<code>dev=device-name</code>	The name of the device to test.
<code>selftest=Enable Disable</code>	Enables or disables the <code>selftest</code> command. Evaluates the functionality of the ISP hardware. Enabled by default.
<code>ilb=Enable Disable</code>	Enables or disables the internal test. Performs internal loopback test within the host adapter ISP hardware at the 1-bit interface. Enabled by default.
<code>elb=Enable Disable</code>	Enables or disables the external loopback test. The desired data pattern, transfer length, and iteration count can be selected on the test parameters menu. Requires a cable for this intervention test. Disabled by default.
<code>lbfcount=iteration-count</code>	Controls the number of times the loopback test will run, for example, 100. Default value is 10.
<code>lbfpattern=hex-pattern</code>	Lists the data pattern to loop, for example, 0x7E7E7E7E. Default value is 0x7E7E7E7E.

## Floating Point Unit Test (`fputest`)

- 
- [“fputest Description” on page 71](#)
  - [“fputest Subtests” on page 71](#)
  - [“fputest Options” on page 73](#)
  - [“fputest Test Modes” on page 74](#)
  - [“fputest Command-Line Syntax” on page 74](#)

---

### `fputest` Description

The `fputest` checks the functionality of the floating point unit in CPUs. The test verifies the functionality by various arithmetic operations. In addition, the `fputest` stresses the CPU with the use of benchmarks. Both single and double precision numbers are used for the operations.

---

**Note** – Three benchmarks of `fputest`—`cparanoia`, `kcdi`, and `kcsqrt`—have been ported over for x86 systems. The x86 version of `fputest` only uses the Double Precision operations for rounding, chopping, sticky bit, and so on.

---

When `fputest` is chosen in Exclusive test mode from the SunVTS graphical user interface, it may run multiple instances in parallel on different CPUs. The number of such instances that may be running in parallel at the same time is dynamically determined depending on system resources.

---

### `fputest` Subtests

Instruction tests include:

- FSR Register test
- Registers test
- NACK test
- Move Registers test
- Positive to Negative test
- Negative to Positive test
- Absolute test
- Single-Precision Integer to Floating Point test
- Double-Precision Integer to Floating Point test
- Single-Precision Floating Point to Integer test
- Double-Precision Floating Point to Integer test
- Single-Precision Round Toward Zero test
- Double-Precision Round Toward Zero test
- Single to Double-Precision Format Conversion test
- Double to Single-Precision Format Conversion test
- Single and Double-Precision Addition, Subtraction, Multiplication, Square-root, Division, and Compare tests
- Single and Double-Precision Compare and Exception if Unordered tests
- Branching and No Branching on Condition Instructions tests
- Single and Double-Precision Chaining tests
- Weitek Status tests
- Lock test
- Single and Double-Precision Datapath tests
- Timing (load) test

Benchmark tests include:

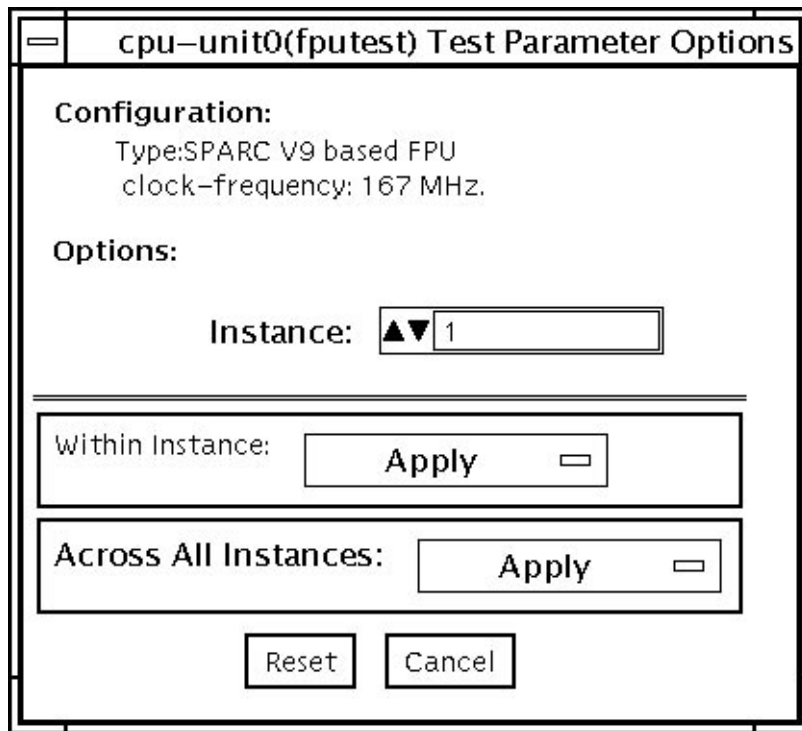
- Lapack test
- Cparanoia test
- Kcsqrt test
- Kcdiv test
- Clorenz test
- Cvector test



---

## fputest Options

To reach the following dialog box, right-click on the test name in the System Map and select Test Parameter Options. If you do not see this test in the System Map, you might need to expand the collapsed groups, or your system might not include the device appropriate to this test. Refer to the *SunVTS User's Guide* for more details.



**FIGURE 9-1** fputest Test Parameter Options Dialog Box

---

**Note** – It is not advisable to use the Processor Affinity option for this test. Doing so reduces the effectiveness of the test.

---

---

# fputest Test Modes

**TABLE 9-1** fputest Supported Test Modes

Test Mode	Description
Connection	Includes all the instruction tests.
Functional	Performs all the instruction tests and all the benchmark tests.
Exclusive	Performs several FPU benchmark tests.
Online	Supported.

---

# fputest Command-Line Syntax

`/opt/SUNWvts/bin/fputest standard-arguments -o dev=cpu-unitN`

---

**Note** – Options `-s`, `-p`, `-i`, and `-w` are not applicable when tests are invoked from the command line.

---

**TABLE 9-2** fputest Command-Line Syntax

Argument	Description
<code>-o</code>	Enables test specific command arguments.
<code>dev=cpu-unitN</code>	Specifies the CPU unit to be tested. <i>N</i> specifies the numeric ID of online CPU. This is not a required option.

Usage Examples:

---

**Note** – When using fputest specific arguments, the `-o` and `dev` options are required.

---

To execute exclusive `fputest` with verbose messages against CPU 72, use the following syntax:

```
# /opt/SUNWvts/bin/fputest -xvo dev=cpu-unit72
```

To execute functional `fputest` with verbose messages against CPU 3, use the following syntax:

```
# /opt/SUNWvts/bin/fputest -fvo dev=cpu-unit3
```



# Infiniband Host Channel Adapter Test (ibhctest)

- 
- [“ibhctest Description” on page 77](#)
  - [“ibhctest Subtests” on page 79](#)
  - [“ibhctest Options” on page 81](#)
  - [“ibhctest Test Modes” on page 83](#)
  - [“ibhctest Command-Line Syntax” on page 83](#)
  - [“ibhctest Subtests” on page 79](#)
- 

## ibhctest Description

ibhctest comprises multiple iRISC CPU cores, two 4x Infiniband ports, and integrated SerDes components. In addition, the ibhctest external associated components include FLASH ROM and DDR memory. ibhctest provides high speed interconnect through PCI interface to external Infiniband fabric. Supported platforms include: two 1U and two 2U x86 AMD Opteron entry-level servers, Sun Fire V2XX, V4XX and E-series high-end servers.

ibhctest exercises and verifies the proper operation of the Tavor chip and its associated components, such as DDR memory, flash PROM, and internal IB packet transmit/receive circuitry. The isolates single faults to the identifiable component(s).

ibhctest supports three execution test modes in SunVTS Connection, Exclusive and Functional. In Connection mode, the test queries for the Tavor firmware and hardware revision, and running internal loopback.

The internal loopback test is run at least once depending on the amount of time each pass takes. In Functional mode all subtests are executed according to the options selected. In Exclusive mode all subtests are executed in sequence.

Tavor supports an internal loopback mechanism that is very similar to the actual operation. The main difference is that data does not go through the integrated SerDes and the 4x IB port circuitry. On the receiving side, data does not get verified by the CRC algorithm. Otherwise, all other components of Tavor that involved in transmitting and receiving data packets are being exercised by `ibhctest`.

Tavor-based HCA uses a single, 256 MB DDR memory for data storage at run time. This data storage is shared by three interdependent clients, Tavor driver, firmware, and hardware. During driver initialization, predetermined data structures and data are laid out in the memory.

With no exclusive access from the driver side, subsequent writes to any memory location that contain real data can cause undesirable results like a system crash. Furthermore, the data allocation size is fixed, writing to the remaining free memory does not add any value in terms of finding faults.

The memory subtest is limited to read only operations which cover the entire DDR memory. The test does not check for data corruption and no mechanism for triggering bit errors through writing to memory. The test uncovers bus related problems.

---

## ibhctest Subtests

**TABLE 10-1** ibhctest Subtests

Subtest	Description
Internal Loopback Test	<p>The HCA supports internal loopback for packets transmitted between QPs that are assigned to the same HCA port. If a packet is being transmitted to a DLID that is equivalent to the Port LID with the LMC bits masked out or the packet DLID is a multicast LID, the packet goes on the loopback path. In this latter case, the packet also is transmitted to the fabric. When a packet is looped back, it must pass the SL2VL mapping. If the mapping yields 15 or a nonoperational VL, the packet is discarded. In the inbound direction, the ICRC and VCRC checks are blindly passed for looped back packets. Note that internal loopback is supported only for packets that are transmitted and received on the same port. Packets that are transmitted on one port and received on another port are transmitted to the fabric. The fabric directs these packets to the destination port.</p> <p>This subtest uses interfaces from the Tavor driver to perform loopback testing. Information such as data pattern for data packets, port number, CQ polling, retries between iteration, and the number of iterations for each <code>ioctl</code> call are passed to the driver. Once finished, status regarding the number passes completed is returned. If the number of passes does not match the number of iterations, a failure has occurred. This might happen when the number of retries is exhausted, and the last failing buffer in the retry series is returned as result. SunVTS then determines exactly what failed in the buffer and reports the failure. The options for this subtest are as follows:</p> <ul style="list-style-type: none"> <li>• <code>lb=Enabled Disabled</code> – Turn the loopback test on or off.</li> <li>• <code>tlbport=1+2</code> – Loopback test on Port 1 and/or 2, default is 1+2.</li> <li>• <code>data=Pattern</code> – Specific data pattern (default patterns 0xa5a5a5a5).</li> <li>• <code>cq=Time</code> – Number of CQ polling time value (in microseconds per iteration). The default is 55000. The maximum is 1000000.</li> <li>• <code>loop=Number</code> – Number of loopback iterations for each pass. The default is 200. The maximum is 1000.</li> <li>• <code>warn=Enabled Disabled</code> – When enabled, prints a warning message.</li> </ul>
DDR READ Test	<p>This subtest comprises two test modes, Sequential and Random. The start and end address offsets are determined dynamically by obtaining them from the firmware. In Sequential mode, length and starting offset are instructed by the test option <code>rdoffset</code> and <code>rdsz</code>. Then the test sequentially reads data from each memory address until all memory locations are covered or the end address is reached. Each read is accomplished by an <code>ioctl</code> call to the driver.</p> <p>The test returns pass or fail based on the completion status of each <code>ioctl</code> call. In Random mode, this subtest reads the number of <code>rdsz</code> times in a randomly generated address bound by the start and end address offset. The options for this subtest are as follows:</p> <ul style="list-style-type: none"> <li>• <code>ddr=Enabled Disabled</code> – Turns the DDR memory test on or off.</li> <li>• <code>rdoffset=Offset</code> – Starts offset of DDR memory read. The default is 0x0 in hexadecimal</li> <li>• <code>rdsz=Size</code> – Read number of byte of DDR memory from Offset to Max address location. The default is 0x2000. The maximum is 256 MB in hexadecimal</li> </ul>





---

**Caution** – In the SunVTS environment, `ibhctest` and `nettest` are mutually exclusive. `nettest` has higher priority if the IB port interface is plumbed up when SunVTS is invoked. These two tests cannot be run at the same time at the command line. If both of these tests are invoked at the command line, `ibhctest` exits gracefully if the IB port interface is plumbed up. The commands to bring down the IB daemon (`ibd [IPoIB]`) are as follows:

```
# ifconfig ibdXX down
# ifconfig ibdXX unplumb
```

Where `XX` is the instance number of the interface.

---

---

## ibhctest Options

To reach the following dialog box, right-click on the test name in the System Map and select Test Parameter Options. If you do not see this test in the System Map, you might need to expand the collapsed groups, or your system might not include the device appropriate to this test. Refer to the *SunVTS User's Guide*.

tavor0(ibhctest) Test Parameter Options

Configuration:

InfiniHost Bridge Chip

Options:

Internal Loopback Test: ☒ Enabled ☐ Disabled

Lb\_Port:

☒ 1
☒ 2

Loopback Data Pattern:

0xA5A5A5A5

Loopback CQ Time:

▲▼ 55000

Loopback Iteration:

▲▼ 200

DDR Memory Test: ☐ Enabled ☐ Disabled

DDR Read Offset:

0x0

DDR Read Length:

0x2000

Prt\_Warn: ☐ Enabled ☐ Disabled

Dev\_List: ☐ Enabled ☒ Disabled

Processor Affinity:

Bound to:

Sequential

Processor 8

Processor 9

Processor 10

Instance: ▲▼ 1

Within Instance:

Apply

Across All Instances:

Apply

Reset

Cancel

**FIGURE 10-1** ibhctest Test Parameter Options Dialog Box

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**TABLE 10-2**    `ibhctest` Options

Option	Description
Internal Loopback Test	Turns the loopback test on or off.
Loopback CQ Time	Number of CQ polling time value (in microseconds per iteration). The default is 55000. The maximum is 1000000.
Loopback Iteration	Number of loopback iterations for each pass. The default is 200. The maximum is 1000.
DDR Memory Test	Turns the DDR memory test on or off.
DDR Read Offset	Starts the offset of DDR memory read. The default is 0x0 in hexadecimal.
DDR Read Length	Read number of byte of DDR memory from Offset to Max address location. The default is 0x2000. The maximum is 256 MB in hexadecimal.
Prt_Wrn	Prints a warning message when enabled.
Dev List	Prints device list. No testing occurs when set.

# ibhctest Test Modes

**TABLE 10-3**    `ibhctest` Supported Test Modes

Test Mode	Description
Connection	Queries for the Tavor firmware and hardware revision and runs the internal loopback test. The internal loopback test runs at least once depending on the duration of each pass.
Exclusive	Executes all subtests sequentially.
Functional	Executes all subtests according to what is selected.

# ibhctest Command-Line Syntax

```
/opt/SUNWvts/bin/ibhctest [-scruvdtlxmf] [-p n] [-i n] [-w n] [-o  
[dev=text] [lb=Enabled | Disabled] [tlbport=1+2] [data=Pattern] [cq=Time] [loop=  
Number] [ddr=Enabled | Disabled] [rdoffset=Offset] [rdsz=Len] [warn=  
Enabled | Disabled] [list] ]
```

Example:

```
# ibhctest -p 0 -svf -o lb=Enabled, tlbport=1+2, data=0xA5A5A5A5,
cq=55000, loop=200, ddr=Enabled, rdoffset=0x0, rdsz=0x2000, warn=
Enabled, dev=tavor1
```

**TABLE 10-4** ibhctest Command-Line Syntax

Option	Description
<i>lb=Enabled Disabled</i>	Turn the loopback test on or off.
<i>tlbport=1+2</i>	Loopback test on Port 1 or 2. The default is 1+2.
<i>data=Pattern</i>	Specific data pattern (default patterns 0xa5a5a5a5).
<i>cq=Time</i>	Number of CQ polling time value (in microseconds per iteration), default: 55000; max: 1000000.
<i>loop=Number</i>	Number of loopback iterations for each pass. The default is 200. The maximum is 1000.
<i>ddr=Enabled Disabled</i>	Turn the DDR Memory test on or off.
<i>rdoffset=Offset</i>	Starts the offset of DDR memory read. Default is 0x0 in hexadecimal.
<i>rdsz=Size</i>	Read number of byte of DDR memory from Offset to Max address location. The default is 0x2000. The maximum is 256 MB in hexadecimal
<i>warn=Enabled Disabled</i>	Prints a warning message when enabled.
<i>list</i>	Prints a device list. No testing occurs when set.

## Level 1 Data Cache Test (l1dcachetest)

- 
- [“l1dcachetest Description” on page 85](#)
  - [“l1dcachetest Options” on page 86](#)
  - [“l1dcachetest Test Modes” on page 89](#)
  - [“l1dcachetest Command-Line Syntax” on page 89](#)
- 

### l1dcachetest Description

l1dcachetest exercises the level 1 Data cache in the CPU module . The test writes, reads, and verifies access of multiple virtual addresses. The virtual addresses are chosen so that they cause targeted hits and misses in the cache. The test dynamically determines the size and organization of the cache and tunes the test accordingly to be effective on the l1dcache.

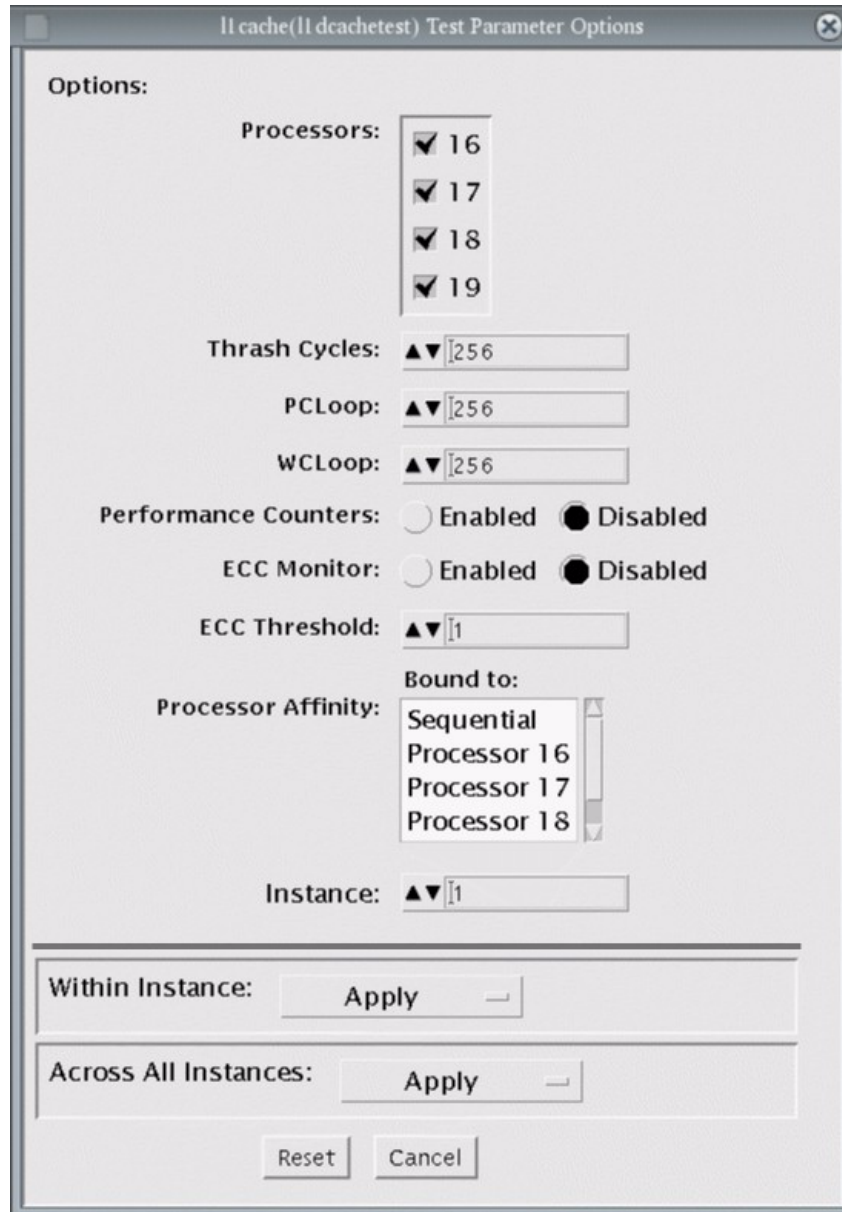
l1dcachetest provides data path testing of on-chip buses. With rapid move to deep sub-micron (DSM) designs, GHz clock frequencies, feature size process of 0.18 micron and below, l1dcachetest ensures the integrity of signals as they traverse conductors on a chip is becoming challenge. l1dcachetest subtests induce crosstalk noise in on-chip data buses by using Maximum Aggressor Fault (MAF) models.

l1dcachetest is self-scaling and adaptive, scaling the size of the system. l1dcachetest is multithreaded. Selection of CPU IDs is one of the options. But if that option is not specified, the test automatically retrieves the number of CPUs in the system and creates that many threads of l1dcachetest to give coverage to the whole system at a given time. The test also determines the sizes and organization of l1dcache.

---

## l1dcachetest Options

To reach the following dialog box, right-click on the test name in the System Map and select Test Parameter Options. If you do not see this test in the System Map, you might need to expand the collapsed groups, or your system might not include the device appropriate to this test. Refer to the *SunVTS User's Guide* for more details.



**FIGURE 11-1** l1dcachetest Test Parameter Options Dialog Box

**TABLE 11-1** 11dcachetest Options

Option	Description
Processors	Selects the CPU IDs for which to run this test. The test uses all CPUs on the system by default. This parameter is optional.
Thrash Cycles	Specifies the number of iterations for data cache subtests. The default is 256.
PCLoop	Specifies the number of iteration for Prefetch Cache Subtests. The default is 256.
WCLoop	Specifies the number of iterations for Write Cache Subtests. The default is 256.
Performance Counters	Enables or disables the performance counter measurements related to data cache and prefetch cache events. The default is off.

---

**Note** – Only one 11dcachetest gets registered for all the CPUs in the system.

---

---

**Note** – The 11dcachetest is automatically bound to a processor. Do not use the Processor Affinity option for the 11dcachetest.

---



# l1dcachetest Test Modes

TABLE 11-2 l1dcachetest Supported Test Modes

Test Mode	Description
Connection	Performs the Connection subtest.
Exclusive	Performs only the l1dcachetest (full test).

## l1dcachetest Command-Line Syntax

**/opt/SUNWvts/bin/l1dcachetest** *standard\_arguments* [-scruvdtlxfn] [-p *n*] [-i *n*] [-w *n*] [-o [ **M**=0+1+2+3+... ], [ **count**=*number* ], [ **pcloop**=*number* ], [wclloop=*number*], [ **perf**=*Enabled* | *Disabled* ], [ **dev**=*l1cache* ]

TABLE 11-3 l1dcachetest Command-Line Syntax

Argument	Description
<b>M</b> =1+2+3...	Selects the CPU IDs for which to run this test. The test uses all CPUs on the system by default. This parameter is optional. The CPU IDs currently present in the system can be retrieved with <code>psrinfo(1M)</code> command. Specifying a CPU ID not present in the system or one that is offline induces an appropriate error messages from the test. For example, if you want to select CPU IDs 4, 5, 6, and 7, specify <b>M</b> =4+5+6+7.
<b>count</b> = <i>number</i>	Specifies the number of iterations for Data Cache Subtests. The default is 256.
<b>pcloop</b> = <i>number</i>	Specifies the number of iterations for Prefetch Cache Subtests. The default is 256.
<b>wclloop</b> = <i>number</i>	Specifies the number of iterations for Write Cache Subtests. The default is 256.
<b>perf</b> = <i>Enabled</i>   <i>Disabled</i>	Enables or disables the Performance counter measurements related to Data Cache and Prefetch Cache events. The default is off.
<b>dev</b> = <i>l1cache</i>	Specifies the device to test. The default value is <i>l1cache</i> .

---

**Note** – If you do not set a value for `count` or `pcloop`, the test run with the default value of `count` and `pcloop`. To disable Data Cache subtests, specify `count=0`. For Prefetch Catch subtests, specify `pcloop=0`.

---

## Level 2 Cache Test (l2sramtest)

- 
- [“l2sramtest Description” on page 91](#)
  - [“l2sramtest Options” on page 92](#)
  - [“l2sramtest Test Modes” on page 93](#)
  - [“l2sramtest Command-Line Syntax” on page 93](#)
- 

### l2sramtest Description

l2sramtest exercises the level2 cache in the CPU module of Sun systems. In most CPUs, the level2 cache is also the external cache, but in some cases the level2 cache is on the chip. This test writes, reads, and verifies access of multiple virtual addresses. This test contains multiple subtests that try to exercise the l2cache by causing hits or misses, performing marching patterns on the l2cache cells, and writing patterns that cause electrical stress.

l2sramtest is self-scaling and adaptive. It scales with the size of the system. It automatically retrieves the number of CPUs in the system and internally creates that many threads of l2sramtest to give coverage to the whole system at a given time. Level 2 cache is shared by all cores in the system thus the test retrieves the number of valid cores in the system and creates that many threads to give coverage. This test also dynamically determines the size and organization of the l2cache. You do not have to input these values.



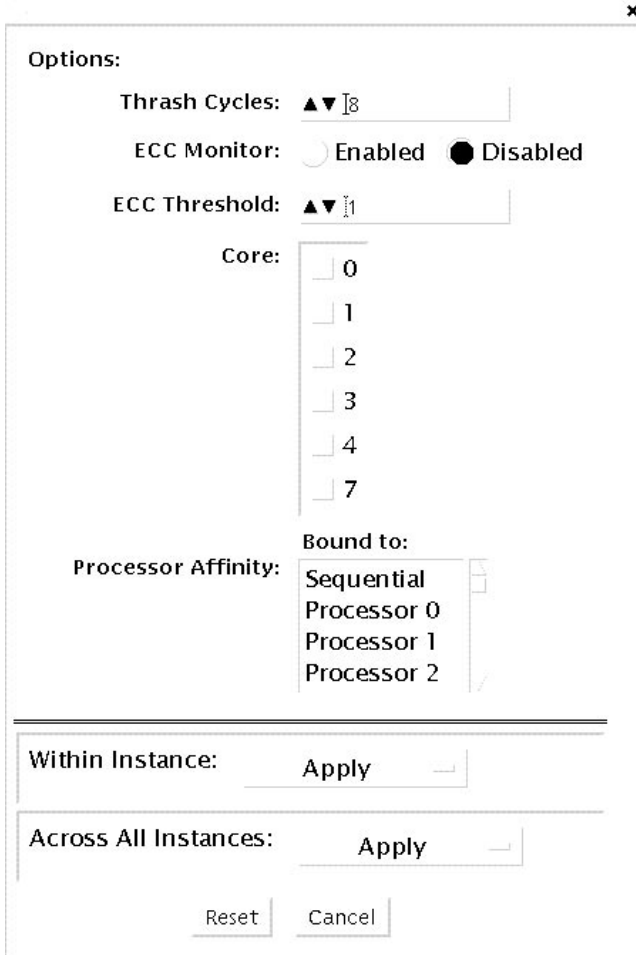
---

**Caution** – This is an exclusive mode test. This test can not be run in parallel with any other tests or applications.

---

# 12sramtest Options

To reach the following dialog box, right-click on the test name in the System Map and select Test Parameter Options. If you do not see this test in the System Map, you might need to expand the collapsed groups, or your system might not include the device appropriate to this test. Refer to the *SunVTS User's Guide* for more details.



A screenshot of the '12sramtest Test Parameter Options' dialog box. The dialog has a title bar with a close button (X). The main area is titled 'Options:' and contains several settings. 'Thrash Cycles' is a numeric field with a spinner icon, set to 8. 'ECC Monitor' has two radio buttons: 'Enabled' (selected) and 'Disabled'. 'ECC Threshold' is a numeric field with a spinner icon, set to 1. 'Core' is a list box with options 0, 1, 2, 3, 4, and 7. 'Processor Affinity' is a section with a 'Bound to:' label and a list box containing 'Sequential', 'Processor 0', 'Processor 1', and 'Processor 2'. At the bottom, there are two sections: 'Within Instance:' with an 'Apply' button, and 'Across All Instances:' with an 'Apply' button. At the very bottom are 'Reset' and 'Cancel' buttons.

Options:

Thrash Cycles: ▲▼ 8

ECC Monitor: ☒ Enabled ☐ Disabled

ECC Threshold: ▲▼ 1

Core:

- ☐ 0
- ☐ 1
- ☐ 2
- ☐ 3
- ☐ 4
- ☐ 7

Processor Affinity:

Bound to:

- ☒ Sequential
- ☐ Processor 0
- ☐ Processor 1
- ☐ Processor 2

Within Instance: Apply

Across All Instances: Apply

Reset Cancel

**FIGURE 12-1** 12sramtest Test Parameter Options Dialog Box

**TABLE 12-1** 12sramtest Options

Option	Description
Thrash Cycles	Specifies the number of thrashing cycles the test completes for the level2 cache on the system. The default value is 8.
Core	Use the core option to perform isolation testing on selected cores only. By default, isolation testing is not enabled and system-wide testing is performed with all available cores.

**Note** – The 12sramtest automatically handles processor binding. Do not use the Processor Affinity option for the 12sramtest.

## 12sramtest Test Modes

**TABLE 12-2** 12sramtest Supported Test Modes

Test Mode	Description
Exclusive	Performs only the 12sramtest (full test).

## 12sramtest Command-Line Syntax

```
/opt/SUNWvts/bin/12sramtest -standard_arguments -o [dev=12sram,count=  
[1...1023],core=[0+1+2+...]]
```

---

**Note** – The `l2sramtest` is not a per CPU test. There will be only one `l2sramtest` for the whole system (one image of Solaris). It will run on all the CPUs of the domain.

---

**TABLE 12-3** `l2sramtest` Command-Line Syntax

Argument	Description
<b>dev</b> = <i>l2sram</i>	Specifies the device. The default value is <code>l2sram</code> .
<b>count</b> = <i>number</i>	Specifies the number of thrashing cycles that the test completes for the level2 cache on the system. Default value for Offline mode is 8.
<b>core</b> =[ <i>0+1+2+...</i> ]	Use the <code>core</code> option to perform isolation testing on selected cores only. By default, isolation testing is not enabled and system-wide testing is performed with all available cores.

## Ethernet Loopback Test (netlbtest)

- 
- [“netlbtest Description” on page 95](#)
  - [“netlbtest Test Requirements” on page 96](#)
  - [“netlbtest Options” on page 97](#)
  - [“netlbtest Test Modes” on page 98](#)
  - [“netlbtest Command-Line Syntax” on page 99](#)
- 

### netlbtest Description

The `netlbtest` replaces the `gemtest` previously included in SunVTS. It provides functional test coverage of the devices which have device drivers that support the Ethernet loopback test. These devices include `eri` (the Ethernet device in the RIO chip) and `ge` (Gigabit Ethernet), `ce` (GigaSwift Ethernet), `dmfe` (10/100 Mbps Ethernet), and `vca` (Sun Crypto Accelerator 4000). The `netlbtest` runs in loopback (external/internal) mode.

The `netlbtest` uses DLPI RAW mode to talk to the device driver. For the purpose of this test, a packet is defined as an Ethernet header followed by the Ethernet data payload (refer to the IEEE 802.3z standard). The test generates and sends out the desired number of packets (a tunable parameter) and expects to receive the same number of packets through the loopback interface, external or internal. If an error occurs (for example, packet mismatch or timeout), an error message indicating the type of error, its probable cause(s), and recommended action(s) is displayed on the SunVTS console.

The data sent out is generated by a random number generator, and put into a data buffer. Each time a packet is sent, it is selected from a different starting point of the data buffer, so that any two consecutively transmitted packets are not the same.

A new debugging capability has been added in `netlbttest`. After one packet is not received, four more packets are transmitted. If all of the packets are not received within the timeout time, the test will stop with the error message, `timed out for receiving . . .`. If up to four packets are missing, the test will stop with an error message, `Missed %d packet(s) . . .`. If a packet is received late and the current transmitted packet is not received, the test will stop with a warning message, `Packet delay . . .`. If the packets arrived late but within five times the timeout value and no packet is missing, the test passes.

---

## netlbttest Test Requirements

You must have the Ethernet card and the device driver installed, a loopback connector in place (external loopback test only), and Intervention mode enabled before running `netlbttest`. `netlbttest` cannot run and does not appear in the GUI if the network interface is connected to a live network. `netlbttest` also requires that the Ethernet device be configured offline before running the test. Use the `ifconfig(1M)` command to bring the Ethernet device down before running `netlbttest`. Enter the following commands to bring the interface down:

```
# ifconfig interface down
# ifconfig interface unplumb
```

To run `netlbttest`, a loopback connector must be connected to the Ethernet interface while performing the external loopback test only. A loopback connector provides the network interface driver the necessary link for testing, while maintaining isolation from a live network.

---

**Note** – The loopback connector is *not* required for both internal and external tests of the Ethernet device. The loopback connector is required for the external loopback test only.

---

The loopback cable for `ge` and Sun GigaSwift Ethernet MMF adapter (`ce` fiber) is based on the following specifications— multimode, duplex, 62.5/125 micron, sc connector, 850nm. The cable can be made by splitting a standard fiber optic cable in two. The two ends of the cable should be connected to the TX and RX ports of the adapter (the order does not matter), thus forming a loop.

The loopback connector for the `eri` device is a standard RJ-45 connector. See Appendix A in the *SunVTS User's Guide* for the diagram. The loopback connector for a Sun GigaSwift Ethernet UTP adapter (`ce` copper) is a standard RJ-45 with all 8 pins connected. See Appendix A of the *SunVTS User's Guide* for the diagram.



# netlbtest Options

To reach the following dialog box, right-click on the test name in the System Map and select Test Parameter Options. If you do not see this test in the System Map, you might need to expand the collapsed groups, or your system might not include the device appropriate to this test. Refer to the *SunVTS User's Guide* for more details.

ce1(netlbtest) Test Parameter Options

**Configuration:**  
Port Address: Unknown  
Host ID: 80d88a43  
Domain Name: nspg.sfbay.sun.com

**Options:**

Total\_packets: ▲▼ 1000

Packet\_Size: ▲▼ 1000

Time\_Out(sec): ▲▼ 10

Loopback: ☒ External ☐ Internal

Print\_Warning: ☐ Enable ☒ Disable

Debug\_Timeout: ☒ Enable ☐ Disable

**Processor Affinity:**

Bound to:  
Sequential  
Processor 2  
Processor 3  
Processor 8

Within Instance: Apply

Across All Instances: Apply

Reset Cancel

FIGURE 13-1 netlbtest Test Parameter Options Dialog Box

Refer to [TABLE 13-1](#) for test parameter descriptions.

**TABLE 13-1** netlbtest Options

netlbtest Options	Description
Configuration	Specifies the port address, host ID, and domain name of the system under test.
Total Packets	Specifies the total number of the packets to send. The default number of packets is 1,000. The maximum number of packets is 100,000,000.
Packet size	Determines the size (in bytes) of the packets to be transmitted. 60 <= packet size <= 1514. The default packet size is 1000 bytes.
Time_Out(sec)	Determines the amount of time (in seconds) that netlbtest can wait to receive packets. If no packets are received within this time frame, netlbtest reports an error message. The range for timeout is from 1 to 1,000 seconds.
Loopback	Determines the external and internal loopback mode. The default setting is internal loopback mode.
Print_Warning	Enables or disables the printing of warning messages. The default setting is Disable.
Processor Affinity	Binds the test to a specific processor. If no processor is specified, the test migrates between processors. This option is only available on multiprocessor systems.
Debug_Timeout	Enables or disables the debugging feature of netlbtest. The default setting is Disable. The range for timeout is from 1 to 1,000 seconds.

## netlbtest Test Modes

**TABLE 13-2** netlbtest Supported Test Modes

Test Mode	Description
Functional	Runs the full set of subtests. The host must not be connected to the network through the intended test device.

Since netlbtest requires a loopback connector for the external loopback test, it can only be selected when Intervention mode is enabled.

# netlbtest Command-Line Syntax

**/opt/SUNWvts/bin/netlbtest** *standard-arguments*

**-o dev=device, tpkts=n, pksz=pkt-size, lb=Internal, warn=Disable, timeout=number-of-seconds**

**TABLE 13-3** netlbtest Command-Line Syntax

Argument	Description
<b>dev=device-name</b>	Specifies the device to test such as <code>ge0</code> or <code>eri0</code> .
<b>tpkts=n</b>	[1...100000], count of packets to loopback. Packets number can be 1 to 100,000. The maximum number of packets is 100,000,000.
<b>pksz=pkt-size</b>	Determines the size (in bytes) of the packets to be transmitted. 60 <= packet size <= 1514. The default packet size is 1000 bytes.
<b>lb=Internal</b>	Selects internal (or external) loopback mode.
<b>warn=Disable</b>	Enables or disables printing of warning messages.
<b>timeout=number-of-seconds</b>	Determines the amount of time (in seconds) that <code>netlbtest</code> can wait to receive packets. If no packets are received within this time frame, <code>netlbtest</code> reports an error message. The range for timeout is from 1 to 1,000 seconds.



## Network Hardware Test (`nettest`)

- 
- “`nettest` Description” on page 101
  - “`nettest` Options” on page 102
  - “`nettest` Test Modes” on page 104
  - “`nettest` Command-Line Syntax” on page 105
- 

### `nettest` Description

`nettest` checks all the networking hardware on the system CPU board and separate networking controllers (for example, a second SBus Ethernet controller). The machine under test must be attached to a network with at least one other system on the network.

---

**Note** – This version of `nettest` is used for *all* networking devices, including Ethernet (`ie` and `le`), token ring (`tr`, `trp`), quad Ethernet (QED), fiber optic (`fddi`, `nf`, `bf`, `pf`), SPARCcluster™ 1 system (`em`), ATM (`sa`, `ba`), HiPPI, 100-Mbits per second Ethernet (`be`, `hme`), and GigaSwift Ethernet (`ce`) devices.

---

The `nettest` mainly uses the Internet Control Message Protocol (ICMP), and requires at least two machines on a network—the machine under test and another machine reliable enough to be a test target. Both machines must support the Transmission Control Protocol/Internet Protocol (TCP/IP) (ICMP is part of TCP/IP). The target machine must either be configured to respond to ICMP broadcast or to RPC broadcast.

First `nettest` determines the target machine(s) to test against. If no targets are specified, it sends an ICMP broadcast to find them. If the test fails to find all necessary `nettest` targets, it tries RPC broadcast to the RPC port mapper daemon. If you specify the targets, `nettest` uses the specified target(s) instead.

After finding the necessary targets, `nettest` performs the following tests:

- Random test – Sends out 256 packets with random data length and random data.
- Incremental test – Sends out packets with length from minimum to maximum packet size using incremental data. (Minimum and maximum values differ for each device.)
- Pattern test – Sends 256 packets of maximum length, where each packet contains one test pattern, and all byte patterns (0 to 0xFF hex) are used. That is, the first packet contains pattern 0, the second packet contains pattern 1, and so on, until the last packet pattern of 0xFF.

---

**Note** – `nettest` is a scalable test. However, the maximum number of networked devices allowed on a system is 255, and the number of instances for each device is limited to 2. If you start the SunVTS exerciser using the `-i` option to specify a default number of instances for all tests, `nettest` cannot assign more than 2 instances per each networked device.

---

## nettest Options

To reach the following dialog box, right-click on the test name in the System Map and select Test Parameter Options. If you do not see this test in the System Map, you might need to expand the collapsed groups, or your system might not include the device appropriate to this test. Refer to the *SunVTS User's Guide* for more details.

**hme0(nettest) Test Parameter Options**

**Configuration:**  
Host\_Name: \_\_\_\_\_  
Host Address: \_\_\_\_\_  
Host ID: \_\_\_\_\_  
Domain Name: \_\_\_\_\_

**Options:**

Target Host: \_\_\_\_\_

Test\_Type: ☒ Random  
☒ Increment  
☒ Pattern

Receive\_Timeout: ▲▼ 20

Number\_of\_Retry: ▲▼ 3

Print\_Warning: ☐ Enable ☒ Disable

Instance: ▲▼ 1

---

Within Instance:

---

Across All Instances:

**FIGURE 14-1** nettest Test Parameter Options Dialog Box

The Configuration section specifies the host name, host ID, host address, and domain name of the system being tested.

**TABLE 14-1** `nettest` Options

<code>nettest</code> Options	Description
Target Host	Specifies one or more targets to be tested against. Target host entries can be either a host name or an Internet address. When no target host is specified, the test finds necessary targets through broadcasting. The default setting leaves this field empty.
Receive Timeout field	The default is 120 seconds, but can be changed. Use a range from 0 to 600 seconds.
Number of Retries field	The default number of retries before flagging an error is three, but can be changed. Use a range between 0 to 128 retries.
Print Warning	Disabled by default. Click Enable to see warning errors, such as retry on timeout.

# nettest Test Modes

Both Connection and Functional test modes are supported by `nettest`. Different test schemes are performed on the network device based on the mode selected.

**TABLE 14-2** `nettest` Supported Test Modes

Test Mode	Description
Connection	Checks whether the device is connected. It searches through all the network interfaces for a specified device name. If <code>nettest</code> does not find the device connected, the test fails; otherwise it returns: <code>device is connected.</code>
Functional	Performs all three tests (Random test, Incremental test, and Pattern test) sequentially. You may specify options that performs heavy stress testing.
Online	Performs only the Random test.



# nettest Command-Line Syntax

`/opt/SUNWvts/bin/nettest` *standard\_arguments* **-o** **target=h1+h2+...**, **dev=interface**, **test=type**, **packets=n**, **pattern=hex**, **timeout=seconds**, **retry=n**, **warn**

TABLE 14-3 nettest Command-Line Syntax

Argument	Description
<b>target=h1+h2+...</b>	A list of test targets by host name or Internet address.
<b>dev=interface</b>	Network interface name. The default value is <code>le0</code> for Ethernet networks.
<b>test=type</b>	The test type Random, Increment, or Pattern for the desired test. The default value is Random+Increment+Pattern (all tests).
<b>packets=n</b>	Number of random/pattern packets. The default is 256.
<b>pattern=hex</b>	Specifies a data pattern, in hexadecimal form. The default is all patterns from 0 to 0xff.
<b>timeout=seconds</b>	Indicates the number of seconds to wait before a timeout. The default is 120 seconds.
<b>retry=n</b>	Indicates the number of test timeout retries. The default is three retries.
<b>warn</b>	When enabled, prints warning messages.



## Physical Memory Test (pmemtest)

- 
- [“pmemtest Description” on page 107](#)
  - [“pmemtest Options” on page 107](#)
  - [“pmemtest Test Modes” on page 110](#)
  - [“pmemtest Command-Line Syntax” on page 110](#)
- 

### pmemtest Description

The `pmemtest` checks the physical memory of the system, memory read errors, and addressing problems. The pseudo driver `mem` reads the physical memory.

This test reads through all the available physical memory. It does not write to any physical memory location.

---

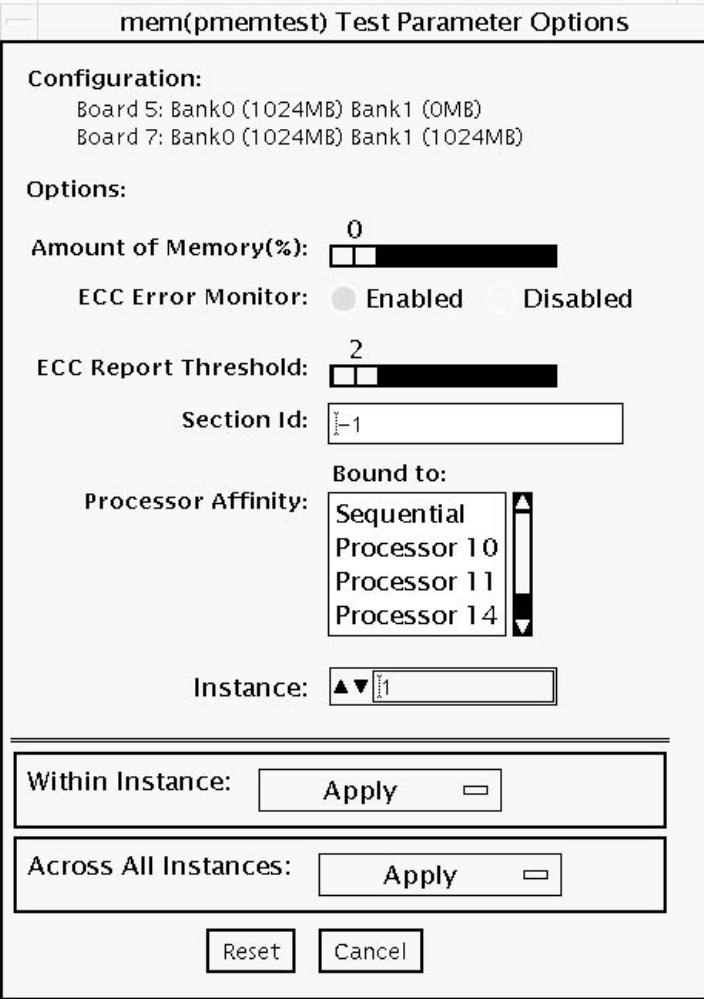
**Note** – ECC errors testing is not supported on x86 platforms

---

### pmemtest Options

`pmemtest` is supported both in physical mapping and logical mapping displays in the UI. In physical mapping, `pmemtest` provides support to test the memory on a per-board basis. Users can select the `pmemtest` that is displayed under the physical memory board, that is to be tested and test only that board. In logical mapping, the `pmemtest` options apply to the complete memory across all boards.

To reach the following dialog box, right-click on the test name in the System Map and select Test Parameter Options. If you do not see this test in the System Map, you might need to expand the collapsed groups, or your system might not include the device appropriate to this test. Refer to the *SunVTS User's Guide* for more details.



The dialog box is titled "mem(pmemtest) Test Parameter Options". It contains the following sections and controls:

- Configuration:**
  - Board 5: Bank0 (1024MB) Bank1 (0MB)
  - Board 7: Bank0 (1024MB) Bank1 (1024MB)
- Options:**
  - Amount of Memory(%):** A slider control set to 0.
  - ECC Error Monitor:** Radio buttons for "Enabled" (selected) and "Disabled".
  - ECC Report Threshold:** A slider control set to 2.
  - Section Id:** A text input field containing "-1".
  - Processor Affinity:** A "Bound to:" label above a list box containing "Sequential", "Processor 10", "Processor 11", and "Processor 14".
  - Instance:** A spinner control set to 1.
- Buttons:** "Reset" and "Cancel" at the bottom.
- Application Scope:** Two rows of controls:
  - Within Instance:** A button labeled "Apply".
  - Across All Instances:** A button labeled "Apply".

**FIGURE 15-1** pmemtest Test Parameter Options Dialog Box

**TABLE 15-1** `pmemtest` Options

<code>pmemtest</code> Options	Description
Configuration	Shows the total amount of physical memory, rounded up to the nearest MB, that is probed by the SunVTS kernel.
Amount of Memory	Specifies the percentage of the physical memory to be tested. The default 0% ensures dividing the total memory equally among instances which results in 100% coverage at the completion of every test pass. One test pass includes one pass each by all instances.
Section ID	For UltraSPARC servers only. When set to -1, <code>pmemtest</code> will test one memory section in each pass, automatically testing each subsequent memory section as testing progresses. When set to a number other than -1, only the section specified is tested. A section is defined by the pass and instance number settings.
Instance	Instances are the number of copies of <code>pmemtest</code> to run simultaneously on the memory being tested.

---

**Note** – The amount of memory option is specified on a per-instance basis. The real memory coverage for one test pass depends on the amount of memory option and the number of instances. For example, if there are four instances, and each instance specifies 50% for the amount of memory option, then this will result in 200% (4 times 50%) coverage on each test pass. For guaranteed 100% memory coverage for each test pass, choose the default percentage size option as 0% for all instances.

---

## pmemtest Test Modes

**TABLE 15-2** pmemtest Supported Test Modes

Test Mode	Description
Connection	One percent of the memory is read. pmemtest also informs you how much physical memory is available.
Functional	The amount of memory to be read can vary. By default 100% of the memory is tested.
Online	The amount of memory to be read can vary. By default 100% of the memory is tested.

## pmemtest Command-Line Syntax

For 32-bit configurations:

```
/opt/SUNWvts/bin/pmemtest standard-arguments -o size=[0-100],dev=  
device-name,section=section-id
```

For 64-bit configurations:

```
/opt/SUNWvts/bin/64/pmemtest standard-arguments -o size=[0-100],dev=  
device-name,bdinfo=number,section=section-id
```

**TABLE 15-3** pmemtest Command-Line Syntax

Argument	Description
<b>size=0-100</b>	Specifies the percentage of memory to be tested. The default is 0% (for 100% memory coverage).
<b>dev=device-name</b>	Specifies the device to test, for example, mem.
<b>section=section-id</b>	For UltraSPARC servers only. When set to -1, pmemtest tests one memory section in each pass, automatically testing each subsequent memory section. When set to a number other than -1, only the section specified is tested. A section is defined by the pass and instance number settings.

## Qlogic Host Bus Adapter Test (qlctest)

- 
- “qlctest Description” on page 111
  - “qlctest Subtests” on page 112
  - “qlctest Options” on page 112
  - “qlctest Test Modes” on page 116
  - “qlctest Command-Line Syntax” on page 116
- 

### qlctest Description

qlctest is made up of several subtests that test the functions of Qlogic ISP22xx, ISP2300, ISP2312, ISP24xx, and ISP6322 based host bus adapters. This includes all Amber and Amber2, Crystal and Crystal2, Pyramid, and Summit cards.

For qlctest to perform properly, you must have Version 4.4.5 or later of the San Foundation Kit (SFK) installed which allows HBAAPI support. To install the latest SFK, go to <http://sunsolve.sun.com> and download and install the software (SAN\_4.4.5\_install\_it.tar.gz).

You must reboot the system for driver changes to take effect. Older 1 GB Fiber Channel Host Bus Adapters (FC HBAs) typically do not use the latest SFK. If you wish to test HBAs without the latest SFK installed, use the previous version of SunVTS (5.1 PS9). 2 GB FC HBAs are not displayed in qlctest without the latest SFK installed.

This test is not scalable.

---

**Note** – Do not run customer data while running qlctest, as the test takes priority over customer data requests. You cannot access data while qlctest is running.

---

---

**Note** – Do not run other tests while `qlctest` is running. `qlctest` might cause other tests to fail.

---

## qlctest Subtests

There are nine supported subtests:

- Fcode revision check
- Firmware revision check
- Board revision check
- Checksum Firmware subtest
- Selftest
- Mailbox Loopback subtest
- Internal 10-bit Loopback subtest
- Internal 1-bit Loopback subtest
- External Loopback subtest

The Fcode, Firmware, and Board revision checks run when any other subtest is selected, and are not manually selected. The external loopback test is an intervention test. To test the Fibre loop, the QLC port must be attached to a loopback connector or to storage.

---

**Note** – A Fibre-channel loopback connector or cable is required when running internal loopback tests to prevent intermittent test failures. Refer to CR 6331949 in the release notes for additional information.

---

For subtest descriptions, see the `qlctest` Options table.

## qlctest Options

To reach the following dialog box, right-click on the test name in the System Map and select Test Parameter Options. If you do not see this test in the System Map, you might need to expand the collapsed groups, or your system might not include the device appropriate to this test. Refer to the *SunVTS User's Guide* for more details.



qlc0(qlctest) Test Parameter Options

**Configuration:**  
QLC controller at /devices/pci@7c0/pci@0/pci@1/  
pci@0;2/SUNW;qlc@1/fp@0;0devctl

**Options:**

Online Selftest: ☒ Enable ☐ Disable

Mailbox Loopback Test: ☒ Enable ☐ Disable

Firmware Checksum Test: ☒ Enable ☐ Disable

Internal Loopback Test 10 bit: ☒ Enable ☐ Disable

Internal Loopback Test 1 bit: ☒ Enable ☐ Disable

External Loopback Test: ☐ Enable ☒ Disable

Loopback Transfer Count:

Loopback Iteration Count:

Loopback Data Pattern:

Processor Affinity: 

Bound to:  
Sequential  
Processor 0  
Processor 1  
Processor 2

Within Instance:

Across All Instances:

**FIGURE 16-1** qlctest Test Parameter Options Dialog Box

**TABLE 16-1** qlctest Options

qlctest Options	Description
Fcode revision check	Retrieves the fcode revision string. This core subtest is always run but not shown in the options dialog box.
Firmware revision check	Retrieves the firmware revision string. This core subtest is always run but not shown in the options dialog box.
Board revision check	Retrieves the board revision levels. This core subtest is always run but not shown in the options dialog box.
Selftest	<p>Evaluates the functionality of ISP hardware by performing the following tests:</p> <ul style="list-style-type: none"> <li>• Transmit FIFO test</li> <li>• Receive FIFO test</li> <li>• SRAM test</li> <li>• Misc. Register tests</li> </ul> <p>Enabled by default, but can be deselected.</p>
Mailbox Loopback subtest	Loads a series of registers into the input mailboxes on the card and then reads the output mailboxes and compares results. This verifies that the system side of the card is operating correctly, and that the internal data paths are correct. Enabled by default, but can be deselected.
Firmware Checksum subtest	Runs an internal checksum test on the installed firmware. This verifies that the RISC RAM on the card is fully functional and that the installed firmware is still intact. This test also serves as a quick RAM check of the RISC RAM. Enabled by default, but can be deselected.
Internal 10-bit Loopback subtest	Performs internal loopback test within the host adapter ISP hardware at the 10-bit interface. This test is done with data sourcing from the system memory and going to the system memory. The desired data pattern, transfer length, and iteration count can be selected via the test parameters menu. Enabled by default, but can be deselected.
Internal 1-bit Loopback subtest	Performs internal loopback test within the host adapter ISP hardware at the 1-bit interface. This test is done with data sourcing from the system memory and going to the system memory. The desired data pattern, transfer length, and iteration count can be selected via the test parameters menu. Enabled by default, but can be deselected.

**TABLE 16-1** qlctest Options (Continued)

qlctest Options	Description
External Loopback subtest	Performs an external loopback test. This test is performed with data sourcing from the system memory and going to the system memory. The desired data pattern, transfer length, and iteration count can be selected via the test parameters menu. This is an intervention test, because a loopback cable is needed from the transceiver to the receiver of the QLC port when testing this port by itself. This subtest can also test the entire fibre channel loop when the loop is connected to the storage to be tested. Not enabled by default, but can be selected.
Loopback Transfer Count	Controls the packet size used in the internal 10-bit, internal 1-bit, and external loopback tests. Default value is 0x10000.
Loopback Iteration Count	Sets the number of times to loop the internal 10-bit, internal 1-bit, and external loopback tests. Default value is 10.
Loopback Data Pattern	Selects the data pattern to loop for the internal 10-bit, internal 1-bit, and external loopback tests. Default value is 0x7e7e7e7e.

## qlctest Test Modes

**TABLE 16-2** qlctest Supported Test Modes

Test Mode	Description
Connection	Opens and closes the QLC port.
Exclusive	Supported.

## qlctest Command-Line Syntax

**/opt/SUNWvts/bin/qlctest** *vts-system-options* **-o dev=device-name, selftest=Enable|Disable, mbox=Enable|Disable, checksum=Enable|Disable, ilb-10=Enable|Disable, ilb=Enable|Disable, elb=Enable|Disable, xcnt=0xtransfer-count, icnt=iteration-count, lbfpattern=0xpattern**

**TABLE 16-3** qlctest Command-Line Syntax

Argument	Description
<b>dev</b>	The name of the device to test.
<b>selftest=</b> <i>Enable Disable</i>	Enables or disables the <b>selftest</b> command. Evaluates the functionality of the ISP hardware. Enabled by default.
<b>mbox=</b> <i>Enable Disable</i>	Enables or disables the mailbox loopback command. This test writes data patterns into the mailboxes and then reads them back from the output mailboxes and verifies that the data is correct. Enabled by default.
<b>checksum=</b> <i>Enable Disable</i>	Enables or disables the checksum command. Runs an internal checksum test on the installed firmware. This verifies that the RISC RAM on the card is fully functional and that the installed firmware is still intact. This test also serves as a quick RAM check of the RISC RAM. Enabled by default.
<b>ilb-10=</b> <i>Enable Disable</i>	Enables or disables the internal 10-bit test. Performs internal loopback test within the host adapter ISP hardware at the 10-bit interface. Enabled by default.
<b>ilb=</b> <i>Enable Disable</i>	Enables or disables the internal 1-bit test. Performs internal loopback test within the host adapter ISP hardware at the 1-bit interface. Enabled by default.

**TABLE 16-3** qlctest Command-Line Syntax

Argument	Description
<b>e1b</b> = <i>Enable Disable</i>	Enables or disables the external loopback test. The desired data pattern, transfer length, and iteration count can be selected on the test parameters menu. Requires a cable for this intervention test. Disabled by default.
<b>xcnt</b> = <i>0xtransfer-count</i>	Controls the packet size to be transferred, for example, 0x1000. Default value is 0x10000.
<b>icnt</b> = <i>iteration-count</i>	Controls the number of times the loopback test will run, for example, 100. Default value is 10.
<b>lbfpattern</b> = <i>0xpattern</i>	Lists the data pattern to loop, for example, 0x7E7E7E7E. Default value is 0x7E7E7E7E.



## RAM Test (ramtest)

- 
- [“ramtest Description” on page 119](#)
  - [“ramtest Options” on page 120](#)
  - [“ramtest Test Modes” on page 124](#)
  - [“ramtest Command-Line Syntax” on page 124](#)
- 

### ramtest Description

ramtest is designed to stress the memory modules (RAM) instead of the whole memory subsystem.

---

**Note** – All the subtests/marches that are not intended for a particular platform are treated as an invalid option and the test provides a FATAL message indicating the invalid option.

---

You can now specify any march any number of times and in any order. The order of the entire command-line interface sequence is maintained for execution. This allows for a reduction in run time and provides an option for running an effective and stressful march earlier.

The Exclusive mode testing puts high stress on the memory and the system interconnect.



---

**Caution** – This is an exclusive mode test. This test can not be run in parallel with any other tests or applications.

---

---

**Note** – ECC error reporting is not yet supported on x86 platforms.

---

---

## ramtest Options

To reach the following dialog box, right-click on the test name in the System Map and select Test Parameter Options. If you do not see this test in the System Map, you might need to expand the collapsed groups. Refer to the *SunVTS User's Guide* for more details.



ram(ramtest) Test Parameter Options

**Configuration:**  
Total Physical Memory : 32768MB

**Options:**

Reserve: ▲▼ 20

Stride: / Random

User Defined Banks to Stride: 1

Custom stride mask: 0xC600

Memory Locking: ☐ Enabled ☒ Disabled

ECC Error Monitor: ☒ Enabled ☐ Disabled

ECC Error Threshold: ▲▼ 2

No of Passes: ▲▼ 1

Data Pattern Descramble Ratio(%): ▲▼ 50

NTA Test Loops per Pass: ▲▼ 1

LA Test Loops per Pass: ▲▼ 1

LR Test Loops per Pass: ▲▼ 1

SS Test Loops per Pass: ▲▼ 1

Processor Affinity: Bound to:  
Sequential  
Processor 16  
Processor 17  
Processor 18

Instance: ▲▼ 1

Within Instance: Apply

Across All Instances: Apply

Reset Cancel

**FIGURE 17-1** ramtest Test Parameter Options Dialog Box

The following table details the `ramtest` options:

**TABLE 17-1** `ramtest` Options

<code>ramtest</code> Options	Description
Reserve	Reserve option represents the percentage of physical memory that is assumed to be in use by the OS or other processes. If you see excessive swapping while running <code>ramtest</code> , increase this percentage. The default is 20%; this means that <code>ramtest</code> allocates 80% of physical memory size for testing. Swapping decreases stress on memory and increases it on the system itself. For memory testing purposes, minimize swapping by tuning the reserve option. If the allocation or locking (in case Memory Locking is enabled) does not succeed, the amount of memory is reduced and the allocation process is repeated. Once the allocation succeeds, the amount of memory allocated is displayed in the messages.
Stride	By default this option is set to Random. It can be set to Column or Row also. In case of random, either Row or Column are randomly selected for each pass. Value of stride defines the memory locations addressed consecutively in certain subtests, in a hardware dependent manner. All testable memory is still tested. Using different strides, checks coupling among different sets of memory cells; therefore random is the recommended value for this option unless both Column and Row are being explicitly used in different instances. For FA type of uses, stride may also be set to UserDefined, in this case the test will stride the number of banks specified in the <code>userstride</code> option. Stride may be set to Custom in which case the stride values are randomly selected from the strides specified in the <code>stridemask</code> value.
User-Defined Banks to Stride	Set the number of banks that the test should stride. The value is currently limited to between 1 and 16. Row striding is not possible while using this option.

**TABLE 17-1** ramtest Options (Continued)

ramtest Options	Description
Stride Mask	<p>Specifies the strides used. Each thread selects one of the stride values from stridemask by selecting one of the bits in the mask.</p> <p>The bits in the stridemask value represent the Least Significant Bit of the stride. Thus a value of 0x4000 calls for a stride of 16384 (using Bit 14 of the address). Multiple bits can be set mixing row and column strides. The Memory Controller section of the PRM for the CPU of the test system for information on how the memory reference address is divided between rows and columns in the DRAM.</p> <p>The value can be specified as a decimal (NNN), hexadecimal (0xNNN), or octal (0NNN) value. The maximum value is 0x400000 (4194304). The default value is 0xC600 which represents strides using Bits 15, 14, 10, and 9.</p> <p>Default values are as follows:</p> <ul style="list-style-type: none"> <li>• ram.h:#define DSTRIDEMASK 0xC600</li> </ul>
Memory Locking	<p>By default memory locking is Disabled. To turn it on, set lock to Enabled. This test uses ISM to lock the memory into the core, which allows 4 MB virtual pages and avoids swapping. Running without locking, adds more randomness to the addressing sequence.</p>
Number of Passes	<p>Specifies the number of passes, in the same instance. Increase passes in case lock is enabled. This saves time spent on locking the memory every time a new process or instance is spawned by the SunVTS kernel. This pass has no relation with the system passes in the SunVTS infrastructure. It appears that ramtest is taking longer to complete system passes.</p>
NTA March Test	<p>Specifies number of loops of NTA March(30N) Test, per pass. Increasing the number of loops of any subtest increases the relative time spent on that subtest in each pass. This increase also increases the time taken to complete a pass. NTA March Test attacks coupling and stuck at faults. NTA March is efficient at finding single-, double-, and some triple-bit errors. Depending on the stride option, coupling faults can be found between cells in adjacent columns, or rows that are targeted.</p> <p>Test time will be higher when row striding is selected because of greater page faults generated. For efficiency purposes, total memory is divided among available CPUs.</p>
LA March Test	<p>Specifies number of loops of LA March(22N) Test, per pass. Increasing the number of loops of any subtest increases the relative time spent on that subtest in each pass. This increase also increases the time taken to complete a pass. LA March Test attacks coupling and stuck-at-faults.</p>

**TABLE 17-1** `ramtest` Options (Continued)

<code>ramtest</code> Options	Description
LR March Test	Specifies number of loops of LR march(14N) test, per pass. Increasing the number of loops of any subtest increases the relative time spent on that subtest in each pass. This increase also increases the time taken to complete a pass. LR March Test attacks coupling and stuck-at-faults.

# ramtest Test Modes

**TABLE 17-2** `ramtest` Supported Test Modes

Test Mode	Description
Exclusive	Stresses memory modules and generates enormous amount of memory traffic.

# ramtest Command-Line Syntax

```
/opt/SUNWvts/bin/ramtest standard-arguments [ -o  
  
[ reserve=Integer between 0 and 90 ]  
[ stride=Row | Column | Random | UserDefined | Custom  
[ userstride=1 - 16 ]  
[ stridemask=0x40 - 0x400000 ]  
[ lock=Enabled | Disabled ]  
[ dratio=Integer between 0 and 100 ]  
[ pass=32 bit integer ]  
[ ntaloops=32 bit integer]  
[ laloops=32 bit Integer ]  
[ lrloops=32 bit Integer ]
```

**TABLE 17-3** ramtest Command-Line Syntax

Argument	Description
<b>reserve</b>	<p>Specifies the amount of memory that will not be allocated for testing. <code>reserve</code> represents a percentage of the total physical memory in the system. When the test starts, it probes the total memory present in the system, then tries to allocate <math>(100 - \text{reserve})\%</math> of memory. If the allocation or locking does not succeed the amount of memory is reduced before the retry. Before starting the test, the amount of memory allocated for testing is displayed.</p> <p>Default value for <code>reserve</code> option is 20.</p> <p>On low memory systems, keep the reserve value higher to avoid excessive swapping.</p> <p>For 32-bit booted systems, the reserve value represents the percentage of 4 GB rather than the percentage of total physical memory.</p>
<b>stride</b>	<p>By default <code>stride</code> is set to <code>random</code>. It can be set to <code>Column</code> or <code>Row</code> also. For <code>random</code>, either <code>Row</code> or <code>Column</code> are randomly selected for each pass. The value of <code>stride</code> defines the memory locations addressed consecutively in certain subtests, in a hardware dependent manner. All testable memory is still tested. Using different <code>stride</code> checks coupling among a different set of memory cells, therefore <code>random</code> is the recommended value for this option unless both <code>Column</code> and <code>Row</code> are being explicitly used in different instances. For FA type of uses, <code>stride</code> may also be set to <code>UserDefined</code>, in this case the test will stride the number of banks specified in the <code>userstride</code> option.</p> <p><code>stride</code> may be set to <code>Custom</code> in which case the <code>stride</code> values are randomly selected from the strides specified in the <code>stridemask</code> value.</p>
<b>userstride</b>	<p>Use this option to set number of banks the test should stride. One of the good choices could be the interleave on the suspect bank, during FA. the value is limited between 1 and 16. This also means row striding is not possible while using this option.</p>
<b>stridemask</b>	<p>When <code>stride=custom</code> is selected, this value specifies the strides used. Each thread selects one of the stride values from <code>stridemask</code> by selecting one of the bits in the mask.</p> <p>The bits in the <code>stridemask</code> value represent the Least Significant bit of the stride. Thus a value of <code>0x4000</code> calls for a stride of 16384 (using bit 14 of the address). Multiple bits can be set by mixing row and column strides.</p> <p>The value can be specified as a decimal (NNN), hexadecimal (0xNNN), or octal (0NNN) value. The maximum value is <code>0x400000</code> (4194304). The default value is <code>0xC600</code> which represents strides using Bits 15, 14, 10, and 9.</p>

**TABLE 17-3** ramtest Command-Line Syntax

Argument	Description
<b>lock</b>	<p>By default memory locking is disabled. To turn it on set the lock to enabled. The test uses ISM to lock the memory into the core, this gives 4 MB virtual pages and avoids swapping. Running without locking adds more randomness to the addressing sequence.</p> <p>On low memory systems, this option can be enabled to avoid excessive swapping.</p> <p>Solaris 10 users, perform the following steps:</p> <ol style="list-style-type: none"> <li>1. Issue the following command:  <pre>% <b>prctl</b> \$\$</pre> <p>If resource controls <code>project.max-shm-memory</code> and <code>project.max-shm-ids</code> are listed in the output, proceed to the next step, otherwise follow the instructions given for Solaris 9.</p> </li> <li>2. Retrieve the default project with the following command:  <pre>% <b>projects -d root</b></pre> <p>This command outputs the default project name, <i>project1</i> in this example, for the Super User.</p> </li> <li>3. Set the resource control <code>project.max-shm-memory</code> with the following command:  <pre>% <b>prctl -t privileged -r -n \</b>  <b>project.max-shm-memory -v 9223372036854775807 \</b>  <b>-i project project1</b></pre> <p>For further information please refer to the <i>Solaris Tunable Parameters Reference Manual</i> applicable to your Solaris release.</p> </li> </ol>
<b>pass</b>	<p>Specifies the number of passes in the single instance. Increase <code>pass</code> if <code>lock</code> is enabled. This saves time spent on locking the memory when a new process or instance is spawned by the SunVTS kernel. This <code>pass</code> has no relation with the system passes in the SunVTS infrastructure, it will appear that <code>ramtest</code> is taking longer to complete system passes.</p>
<b>ntaloops</b>	<p>Specifies number of loops of NTA march(30N) test, per pass. Increasing the number of loops of any subtest increases the relative time spent on that subtest in each pass. This increase also increases the time taken to complete a pass. NTA march test attacks stuck-at-faults, two-cell coupling faults, and some three-cell coupling faults.</p>
<b>laloops</b>	<p>Specifies number of loops of LA march(22N) test, per pass. Increasing the number of loops of any subtest increases the relative time spent on that subtest in each pass. This increase also increases the time taken to complete a pass. LA march test attacks coupling and stuck-at-faults.</p>

**TABLE 17-3** `ramtest` Command-Line Syntax

Argument	Description
<b>lrloops</b>	Specifies number of loops of LR march(14N) test, per pass. Increasing the number of loops of any subtest increases the relative time spent on that subtest in each pass. This increase also increases the time taken to complete a pass. LR march test attacks coupling and stuck-at-faults.
<b>dratio</b>	Descrambles ratio tunes the algorithm used to generate data patterns in <code>ramtest</code> . A descramble ratio of 100 means that all the data patterns generated will be descrambled. If a descramble ratio is 0, the test will generate the data patterns tuned toward bus noise. Default value is 50, which means that half the data patterns are descrambled.
<b>custom</b>	<p>When <code>stride=custom</code> is selected, this value specifies the strides used. Each thread selects one of the stride values from <code>stridemask</code> by selecting one of the bits in the mask.</p> <p>The bits in the <code>stridemask</code> value represent the least significant bit of the <code>stride</code>. Thus a value of 0x4000 calls for a <code>stride</code> of 16384 (using Bit 14 of the address). Multiple bits can be set mixing row and column strides.</p> <p>The value can be specified as a decimal (NNN), hexadecimal (0xNNN), or octal (0NNN) value. The maximum value is 0x400000 (4194304). The default value is 0xC600 which represents strides using Bits 15, 14, 10, and 9.</p>





## Serial Ports Test (`serialtest`)

---

- “`serialtest` Description” on page 129
  - “Loopback Connectors” on page 130
  - “`serialtest` Asynchronous Testing” on page 130
  - “`serialtest` Options” on page 130
  - “`serialtest` Test Modes” on page 134
  - “`serialtest` Command-Line Syntax” on page 134
- 

### `serialtest` Description

The `serialtest` checks the system on-board serial ports (`asy[0,1]`), and any multiterminal interface (ALM2) boards (`mcp[0-3]`). Data is written and read in asynchronous mode using external loopback path.

---

**Note** – `serialtest` supports testing the `asy` driver (16550 UART). The 16550 device only works in asynchronous mode. The maximum baud rate is 115200.

---

---

**Note** – The 16550 UART can support two devices but some systems only carry one physical port (TTYs1 or TTYb, `/dev/term/b`, known as COM2 in the x86 environment). Do not try to test port a. The operating system shows two ports, but physically there is only one port.

---

Intervention mode must be enabled to run this test. This test is nonscalable.

---

## Loopback Connectors

The loopback test requires null modem and plug connectors, which are described in [Appendix A](#).

The loopback for the Plug A to A option is described in the section “[9-Pin Female Single-Port Loopback Plug](#)” on page 181.

---

## serialtest Asynchronous Testing

This mode uses the asynchronous communication protocol as described in the `zs(7D)` and `se(7D)` man pages. The `termio(7I)` interface is used for configuring port characteristics.

You can select the loopback paths to use, the amount of data to transfer, and the baud rate to be used for the transfer.

The test writes and reads data through the loopback path and compares the data to the original data. The test first sends a single character. If no errors or timeouts are detected, the rest of the data is simultaneously written and read, then compared for data integrity.

---

## serialtest Options

To reach the following dialog box, right-click on the test name in the System Map and select Test Parameter Options. If you do not see this test in the System Map, you might need to expand the collapsed groups, or your system might not include the device appropriate to this test. Refer to the *SunVTS User's Guide* for more details.

**FIGURE 18-1** serialtest Test Parameter Options Dialog Box

**TABLE 18-1** serialtest Options

Option	Description
Test Type	<p>Selects how the test will run. Test options include:</p> <ul style="list-style-type: none"> <li>• a – Runs the test on port A.</li> <li>• b – Runs the test on port B.</li> <li>• a_b – Runs the test on ports A and B sequentially.</li> <li>• a_b_concurrent – Runs the test on port A and port B concurrently.</li> </ul>
Loopback Type	<p>Selects the loopback test. Options include:</p> <ul style="list-style-type: none"> <li>• Plug_a_to_a__b_to_b – An external loopback plug for a, b, a_b, and a_b_concurrent test types.</li> <li>• no_modem_a_to_b – An external loopback cable for a_to_b and a_to_b_concurrent test types.</li> </ul>
Data Type	<p>Selects the data pattern to transfer. You can select:</p> <ul style="list-style-type: none"> <li>• Random</li> <li>• Sequential</li> <li>• Alphanumeric</li> <li>• 0x00-0xff</li> </ul>
Async Baud Rate	<p>Selects the baud rate for Asynchronous mode testing. The valid rates are 110, 300, 600, 1200, 4800, 9600, 19200, 38400, 57600, 76800, 115200, 153600, 230400, 307200, 460800, and ALL. The default rate is 9600 baud. Some platforms can only support up to 38400 or 76800. If the device is connected to the super I/O port then the max baud rate supported is 115200. The test will return an error if you try to use a higher baud rate than is supported. For baud rates greater than 153600, the serial line drivers must be set for RS-423 mode and not RS-232 mode. The RS-423 and RS-232 modes are usually selected by a hardware jumper on the motherboard.</p> <p>Consult your hardware installation manual for more information.</p>
Async Data Size	<p>Selects the total number of bytes to transfer in Asynchronous mode. This can range from 1 to 10000.</p>
Async Flow Control	<p>Selects the type of flow control to use in Asynchronous mode testing. You can select Hardware (RTS/CTS), Software (XON/XOFF) or None. The default depends on the loopback type. Software flow control is not allowed on a, b, a_b, or a_b_concurrent loopback types.</p>
<p><i>The following options are not available for the devices connected to the super I/O port. They are available for all other supported I/O ports.</i></p>	
Test Mode	<p>Selects the mode to run the test in. The modes available are Asynchronous, Synchronous, or Both. When Both is selected, the test runs first in Asynchronous mode then Synchronous mode.</p>

**TABLE 18-1** serialtest Options (Continued)

Option	Description
Sync Baud Rate	Selects the device generated clock rate for Synchronous mode testing. The valid rates are from 110 to 230400. The rate does not have to be a specific value as required for async mode baud rates. The default rate is 9600. Some platforms can only support up to 38400 or 76800. The test will return an error if you try to use a higher rate than is supported. For rates greater than 100000, the serial line drivers must be set for RS-423 mode and not RS-232 mode. The RS-423 and RS-232 modes are usually selected by a hardware jumper on the motherboard. Consult your hardware installation manual for more information.
Sync Packet	Selects the tolerance level of Synchronous mode dropped packets during the many_packets subtest. The default is Drop20 percent. The valid range is from 0 percent to 99 percent. Some packet drops are expected especially at higher Toleranceclock rates and on a heavily loaded system.
Sync Poll Wait	Selects the number of seconds in additional time to wait for a Synchronous mode packet to be sent. Additional time may be needed when there is heavy system activity and time-outs are being detected. In general, the user can decrease the value to 0 when the system load is light or increase the value when there is a heavy system load.
<i>The following option is available only for devices connected to the super I/O port. They do not apply to any other supported I/O ports.</i>	
User Defined Baud Rate	Allows the user to set new baud rate values for the test. First select the User Defined option from the Async menu. Then enter the new value in the User Defined Baud Rate field.

## serialtest Test Modes

**TABLE 18-2** serialtest Supported Test Modes

Mode	Description
Connection	Attempts to open the port to determine if the device is connected. If the port does not open and if the port is not busy, the test exits with an error. If it is successful or fails with a busy or exclusive use error, then the port is considered connected, and the test passes.
Functional	Performs the selected loopback test.

## serialtest Command-Line Syntax

**/opt/SUNWvts/bin/serialtest** *standard-arguments* **-o dev=device-name, porta=port-name,T=test-type,L=loopback-type,M=mode,D=data-pattern,AB=async-baud-rate,S=async-data-size,par=none | even | odd,BS=1 | 10 | 100 | 1000 | 3000 | 5000 | 10000,F=flow-control,B=sync-baud-rate,DP= sync-drop-tolerance,P=sync-poll-wait**

Command-line syntax for devices connected to the super I/O port:

`/opt/SUNWvts/bin/serialtest` *standard-arguments* **-o** **dev**=device-name, **porta**=port-name, **T**=test-type, **L**=loopback-type, **D**=data-pattern, **AB**=async-baud-rate, **BS**=1 | 10 | 100 | 1000 | 3000 | 5000 | 10000, **F**=flow-control, **UAB**=User-Baud-Rate

TABLE 18-3 serialtest Command-Line Syntax

Argument	Description
<b>dev</b> =device-name	Identifies the serial port(s) to test. There is no default value. You must specify a device name such as: <ul style="list-style-type: none"><li>• asy0</li><li>• asy1</li></ul>
<b>porta</b> =port-name	The name of the first device of a serial device pair. The default is A.
<b>T</b> =test-type	Specifies the type of test to run: <ul style="list-style-type: none"><li>• a = runs the test on port A.</li><li>• b = runs the test on port B.</li><li>• a_b = runs the test on ports A and B sequentially.</li><li>• a_b_concurrent = runs the test on port A and port B concurrently.</li><li>• a_to_b = runs the test from port A to port B.</li></ul>
<b>L</b> =loopback-type	The type of loopback connector attached to ports: <ul style="list-style-type: none"><li>• Plug_a_to_a_b_to_b</li><li>• Modem_a_to_b (Sun internal only)</li></ul>
<b>M</b> =mode	The default test mode is asynchronous (asynch).
<b>D</b> =data-pattern	Selects the data pattern to transfer. You can select: <ul style="list-style-type: none"><li>• Random</li><li>• Sequential</li><li>• Alphanumeric</li><li>• 0x00-0xFF</li></ul>
<b>AB</b> =async-baud-rate	Asynchronous baud rate (default = 9600). The valid values are between 110 - 460800. Note: Some platforms can only support asynchronous baud rates up to 38400 or 76800. For baud rates greater than 153600 the serial line drivers must be set for RS-423 mode and not RS-232 mode.
<b>S</b> =async-data-size	Asynchronous mode total number of bytes to write. Values range from 1 to 10000 bytes.

**TABLE 18-3** (Continued) `serialtest` Command-Line Syntax

Argument	Description
<b>par</b> = <i>none even odd</i>	Parity used in the async loop test. Default value is none.
<b>BS</b> = <i>1 10 100 1000 3000 5000 10000</i>	Number of bytes in each write during async loop test. Default value is 100.
<b>F</b> = <i>flow-control</i>	Asynchronous mode flow control: <ul style="list-style-type: none"><li>• Hardware (RTS/CTS)</li><li>• Software (xon/xoff)</li><li>• None</li></ul>
<b>Note</b> – <i>sync-baud-rate</i> , <i>sync-drop-tolerance</i> , <i>sync-poll-wait</i> , <i>User-Baud-Rate</i> are not supported on x86 platforms.	



## System Test (systest)

- 
- [“systest Description” on page 137](#)
  - [“systest Options” on page 138](#)
  - [“systest Test Modes” on page 140](#)
  - [“systest Command-Line Syntax” on page 140](#)
  - [“systest Description” on page 137](#)
  - [“systest Description” on page 137](#)

---

### systest Description

The `systest` checks the overall functionality of a Sun system by exercising the CPU, I/O, and memory channels.

The test ensures the concurrency of the different channels by the use of Solaris threads. The test aims at stimulating failures that might be caused due to the interaction of the various different hardware modules in the system. It is very stressful on the CPU, and stresses the parallel computational capability of a multiprocessor system.



---

**Caution** – This is an exclusive mode test. This test can not be run in parallel with any other tests or applications.

---

## systemtest Options

To reach the following dialog box, right-click on the test name in the System Map and select Test Parameter Options. If you do not see this test in the System Map, you might need to expand the collapsed groups, or your system might not include the device appropriate to this test. Refer to the *SunVTS User's Guide* for more details.

**system(systemtest) Test Parameter Options**

**Configuration:**  
System Configuration= Sun Microsystems sun4u  
Memory size= 256 Megabytes  
System clock-frequency= 110 MHz

**Options:**

Asynch I/O Test: ☒ Enable ☐ Disable

Memory Test: ☒ Enable ☐ Disable

Cpu Test: ☒ Enable ☐ Disable

Internal Systest Passes:

Systest Isolation:

Within Instance:

Across All Instances:

**FIGURE 19-1** systest Test Parameter Options Dialog Box



**Caution** – Use discretion when defining the syspass parameter. One linpack pass (syspass=1) takes approximately 40 minutes on a server with 12 processors. If the syspass value is set to a high value, it also increases the probability of detecting residual errors.



---

**Caution** – Use strong discretion when defining the System Isolation (`sysiso`) parameter. Be aware that `sysiso` may online / offline CPUs in the system. Do not use `sysiso` on production servers. If you choose CPUs (`sysiso=2`) isolation, the run time may be much higher than for board(s) (`sysiso=1`) isolation. The total run time for isolation can not be precisely estimated. If a residual error is found in the initial evaluation phase, the isolation functionality will online or offline CPUs in order to detect the defective boards and CPUs in the system.

---

---

**Note** – Users are advised to not use the Processor Affinity option for this test. Doing so reduces the effectiveness of the test.

---

**TABLE 19-1** `systest` Options

<code>systest</code> Options	Description
Asynch I/O Test	Enables or disables the Asynch I/O subtest. The default is enable.
Memory Test	Enables or disables the Memory subtest. The default is enable.
CPU Test	Enables or disables the CPU/FPU subtests. The default is enable.
Internal System Passes	Defines the number of internal linpack passes. A set of boards and CPUs will be declared good after <code>syspass</code> number of passes. The default is 1.
System Isolation	Defines the type of isolation that <code>systest</code> needs to perform if a residual error is found in the initial evaluation phase. 0 = No isolation (default) 1 = Board(s) isolation only 2 = Board(s) and CPUs isolation

Use the default values for an initial evaluation of the system.

# systest Test Modes

TABLE 19-2 `systest` Supported Test Modes

Test Mode	Description
Exclusive	Performs only the <code>systest</code> (full test).

# systest Command-Line Syntax

`/opt/SUNWvts/bin/systest` *standard-arguments* **-o** **-io=enable|disable**  
**-mem=enable|disable**, **-cpu=enable|disable**, **-dev=system**, **-syspass=1,2000**, **-sysiso=0|1|2**

TABLE 19-3 `systest` Command-Line Syntax

Argument	Description
<b>io=enable disable</b>	Enables or disables the Asynch I/O subtest.
<b>mem=enable disable</b>	Enables or disables the Memory subtest.
<b>cpu=enable disable</b>	Enables or disables the CPU/FPU subtests.
<b>dev=system</b>	Specifies the pseudo device name.
<b>syspass=1,2000</b>	Defines the number of internal linpack passes. A set of boards and CPUs is declared good after <code>syspass</code> number of passes. The default is 1.
<b>sysiso=0 1 2</b>	Defines the type of isolation that <code>systest</code> needs to perform if a residual error is found in the initial evaluation phase. 0 = No isolation 1 = Board(s) isolation only 2 = Board(s) and CPUs isolation

# Command-Line Examples

The following examples assume you execute `systest` from the command-line with verbose enabled.

Example 1 invokes the following:

```
# ./systest -xv
```

- `systest` with default parameter values.
- I/O, MEM, and CPU subtests.
- One internal pass of linpack and no isolation.

Example 2 invokes the following:

```
# ./systest -xv -o io=Disable,mem=Enable,cpu=Enable,dev=system
```

- `systest` without the I/O subtest
- MEM and CPU subtests.
- One internal pass of linpack and no isolation.



---

**Caution** – Do not perform the following `systest` examples (3 and 4) on production servers, because `systest` may online or offline CPUs.

---

Example 3 invokes the following:

```
# ./systest -xv -o syspass=15,sysiso=1
```

- I/O, MEM, and CPU subtests.
- Declares a set of boards free from residual errors after 15 internal passes of the linpack algorithm.
- If an error is found, `systest` performs boards isolation.

Example 4 invokes the following:

```
# ./systest -xv -o syspass=10,sysiso=2
```

- I/O, MEM, and CPU subtests.
- Declares a set of boards and CPUs free from residual errors after 10 internal passes of the linpack algorithm.
- If an error is found, `systest` performs boards AND CPUs isolation.



## Tape Drive Test (`tapetest`)

---

- [“tapetest Description” on page 143](#)
  - [“tapetest Test Requirements” on page 144](#)
  - [“tapetest Options” on page 144](#)
  - [“tapetest Test Modes” on page 148](#)
  - [“tapetest Command-Line Syntax” on page 148](#)
- 

### `tapetest` Description

The `tapetest` synchronous I/O test writes a pattern to a specified number of blocks (or, for a SCSI tape, writes to the end of the tape). The `tapetest` then rewinds the tape and reads and compares the data just written. The `tapetest` asynchronous I/O test sends a series of up to five asynchronous read/write requests to the tape drive, writing to the tape, and then reading and comparing the data. The terms asynchronous and synchronous referred to here, and in the Method field are not related to the SCSI messaging terms of the same name.

The `tapetest` file test writes four files to the tape and then reads them back, comparing the data. For tape library testing, the pass count is incremented only after all tapes in the library have been tested. The read/write algorithms are enhanced for DLT tape by using a random data pattern (1.5:1 compression) and a more robust read compare algorithm. Some default parameters have also changed.

The Async I/O subtest uses the asynchronous read and write feature of the Solaris tape driver to exercise tape drives. In read-only mode the test sends a maximum of four asynchronous read packets, each with a random size and a random offset, to the tape drive. The test then waits for all outstanding I/O activity to complete before issuing another round of packets. This process continues until the whole area being tested has been covered. In read-write mode, one write packet is issued for every

four read packets to ensure a spot check of the write operation. The area of the tape to be tested is written to first in order for the test to work correctly. This test is only supported under the Solaris 8 and Solaris 9 OSs and future compatible releases.

---

## tapetest Test Requirements

If you have a tape drive in your system, load a blank writable tape (scratch tape) before you start SunVTS. If you fail to do this, the `tapetest` option may display `drive type:unknown` on the option menu for the `tapetest`.

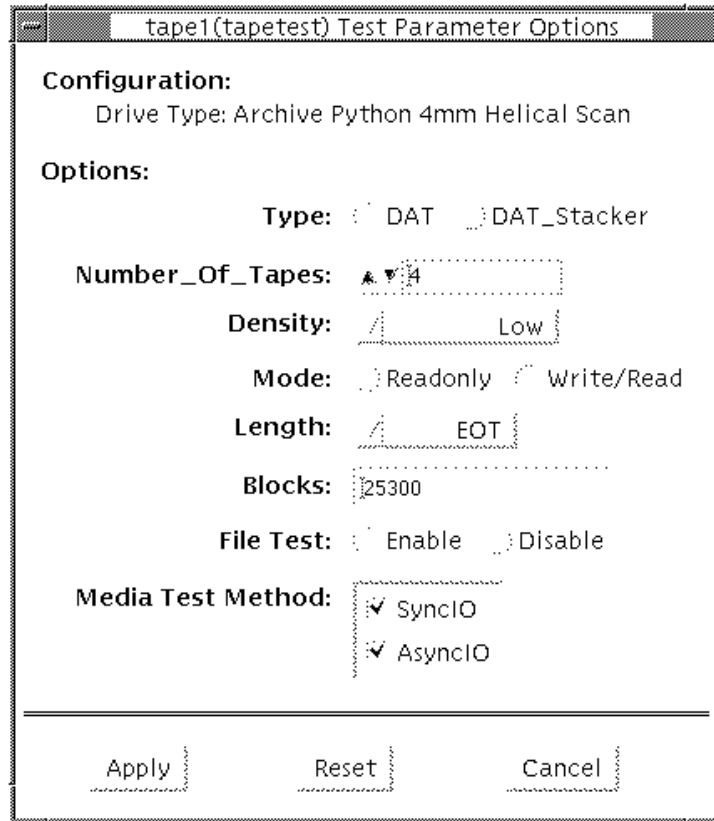
---

## tapetest Options

To reach the following dialog box, right-click on the test name in the System Map and select Test Parameter Options. If you do not see this test in the System Map, you might need to expand the collapsed groups, or your system might not include the device appropriate to this test. Refer to the *SunVTS User's Guide* for more details.

`tapetest` supports 4-mm, 8-mm, DLT, 1/4-inch cartridge, and 1/2-inch front-load tape drive testing. The options available for each of the tape devices differ slightly. An example of the Test Parameter Options dialog box for a device is shown in [FIGURE 20-1](#).





**FIGURE 20-1** tapetest Test Parameter Options Dialog Box

---

**Note** – This test does not immediately stop after being disabled.

---

---

**Note** – Selecting nondefault options can significantly increase the run time for the test.

---

**Note** – The Test Parameter Options dialog box for the 1/4-inch, 1/2-inch, DLT, and 8-mm tape drives differ slightly from [FIGURE 20-1](#).

**TABLE 20-1** tapetest Options

tapetest Options	Description
Type	Normal tape drive or tape library (stacker).
Number of Tapes	The number of tapes in the tape library. tapetest registers a single tape library pass only after all tapes in the library pass.
Density	<p>The following settings are available for most tape drives:</p> <ul style="list-style-type: none"> <li>• Low – Tests the l tape device.</li> <li>• Medium – Tests the m tape device.</li> <li>• Compression – Tests the c tape device.</li> <li>• All – Tests the l, m, and c tape devices.</li> </ul> <p>For half-inch tape drives, the available settings are 800, 1600, and 6250 BPI (blocks per inch).</p> <p>For certain QIC drives, select QIC-11 (1-byte block ID) mode, QIC-24 (4-byte block ID) mode, or Both.</p> <p>Note: On a DLT drive, the l and m settings both use no compression. tapetest does not support changing DLT capacity settings indicated on the front panel.</p>
Mode	<p>If you enable Write/Read mode, the test first writes to the tape and then reads it back to compare. If you enable Read_Only mode, the test assumes the tape has been properly written and merely reads and compares. This mode is useful to check proper head alignment.</p> <p>Note: If a read only pass is attempted and the tape was not previously written by tapetest, using the same test parameters currently set, a Big Read Failure will occur.</p>
Length	<p>The amount of the tape to be tested. The choices are:</p> <ul style="list-style-type: none"> <li>• EOT – The default; tests to the entire tape.</li> <li>• Long – The SCSI tape tests 70,000 blocks of the tape.</li> <li>• Short – Only the first 1000 blocks are tested.</li> <li>• Specified – You must type the number of blocks to be tested in the blocks field.</li> </ul>
# of Blocks	If you select Specified under the Length option, you must type the number of blocks you want to test.

**TABLE 20-1** tapetest Options

tapetest Options	Description
Blocksize	<p>Block size specification. This option is only available for Tandberg QIC tape drives. There are two possible values. 512-bytes is for use with older tape media that have transfer size restrictions, while 64-kbytes is for use with current, high-capacity tape media.</p> <p>Note – This option is only available in command line interface mode.</p> <p>Note – With patches 110278-01 or 110211-01 applied, DLT writes either a 512 byte or 65536 byte block depending on how this parameter is set.</p>
File Test	<p>The tape file test sequence is as follows:</p> <ol style="list-style-type: none"> <li>1. Writes three files.</li> <li>2. Rewinds.</li> <li>3. Reads part of the first file.</li> <li>4. Forward spaces to the start of the second file.</li> <li>5. Reads the second file.</li> <li>6. Forward spaces to the start of the third file.</li> <li>7. Tries to read to the end of that file for SCSI tapes only. The tape file test tries to backspace to the start of the second file and read it.</li> </ol>
Retension	<p>When enable is selected, the program retensions the tape.</p>
Media Test Method	<ul style="list-style-type: none"> <li>• Sync I/O—tapetest reads and or writes the number of blocks selected in Length.</li> <li>• Async I/O—tapetest makes four asynchronous read requests to the tape drive. If read and write testing is selected, one asynchronous write request is also sent. The test continues after completing the requests.</li> </ul> <p>Note – When testing Tandberg QIC drives, Async I/O testing is restricted to read-only due to asynchronous behavior differences with other tape drives.</p> <p>Note – This option is not associated with the synchronous data transfer request SCSI message. It is only synchronous or asynchronous in nature because the numbers of reads and writes are not synchronous to each other. The SDTR message is not invoked.</p>

## tapetest Test Modes

The tapetest supports the following test modes. It performs different test schemes on the tape device, according to the mode you select.

**TABLE 20-2** tapetest Supported Test Modes

Test Mode	Description
Connection	tapetest verifies that the drive can be opened and that the drive type can be determined. If both checks are successful, or if the drive is currently busy, then the test passes. The tapetest fails if the open operation is unsuccessful for any reason other than the drive is busy.
Functional	tapetest checks the status, rewinds the tape, erases and retensions it. If the device is a cartridge tape, tapetest writes a pattern to nblks or eot (default), rewinds the tape, and then reads and compares of the pattern. On the other hand, if the device is busy or if no tape cartridge can be found in the drive, the test cannot run and fails.

## tapetest Command-Line Syntax

```
/opt/SUNWvts/bin/tapetest standard-arguments -o dev=device-name, s=block-count, d=density, m=mode, l=length, method=method, ft=Enables | Disables, ret=Enables | Disables, dat=dat-type, 8mm=8mm-type, num=magazine-size, blocksize=block-size
```

**TABLE 20-3** tapetest Command-Line Syntax

Argument	Explanation
<b>dev=device-name</b>	Specifies the <i>device-name</i> of the tape drive (required).
<b>s=block-count</b>	Specifies the number of blocks to be tested.
<b>d=density</b>	Specifies the density of the tape to open.
<b>m=mode</b>	Enables either the Write-Read or Read-Only tests.
<b>l=length</b>	Specifies the length of the test (EOT, Specified, Long, or Short).
<b>method=method</b>	Specifies the media test method (SyncI/O and or AsyncI/O) used. Note: This option does not invoke the SCSI message synchronous data transfer request. It is only asynchronous or synchronous in nature.

**TABLE 20-3** tapetest Command-Line Syntax (*Continued*)

Argument	Explanation ( <i>Continued</i> )
<b>ft</b> = <i>Enables   Disables</i>	Enables or disables the File test.
<b>ret</b> = <i>Enables   Disables</i>	Enable or disables tape retension.
<b>dat</b> = <i>dat-type</i>	If you are testing a digital audio tape drive, specify whether it is a regular DAT drive or a DAT stacker. The choices are DAT and DAT_Stacker.
<b>8mm</b> = <i>8mm-type</i>	If you are testing an 8-mm tape drive, specify whether it is a regular 8-mm tape drive or a tape library. The command line choices are 8mm and 8mm_Library.
<b>num</b> = <i>magazine-size</i>	If you are testing a tape library, specify the magazine size.
<b>blocksize</b> = <i>block-size</i>	This option is only available on a Tandberg QIC drive and DLT drives. Specify whether to use a 64 kbyte block transfer or a 512 byte block transfer. Use 512 bytes when testing older media in the drive. DLT supports 512 byte and 65536 byte modes



## USB Device Test (`usbtest`)

---

- [“usbtest Description” on page 151](#)
  - [“usbtest Subtests” on page 152](#)
  - [“usbtest Options” on page 152](#)
  - [“usbtest Test Modes” on page 154](#)
  - [“usbtest Command Line Syntax” on page 154](#)
- 

### `usbtest` Description

The `usbtest` verifies the proper functioning of the hardware of the USB subsystem. The test supports audio, keyboard, and printer devices. The `usbtest` for audio verifies the proper functioning of the hardware and software components of the USB audio subsystem. Specifically, the test tests the USB audio devices, USB microphones and speakers.

---

**Note** – `usbtest` tests the USB ports using various USB devices such as the USB key board, the USB printer, or the USB audio devices.

---

For keyboards, the test verifies whether the keyboard(s) attached to the USB bus are USB-compliant. The test flashes the LEDs of a compliant keyboard. The `usbtest` for printers verifies the proper functioning and integrity of the USB parallel port devices. The test data, either ASCII or a PostScript file, is sent to the printer through the USB bulk transfer. The test saves and restores the printer settings.

The `usbtest` options and subtest available depends on the peripheral device attached to the USB port.

## usbtest Subtests

The following table lists the `usbtest` subtests for both the USB audio and printer devices.

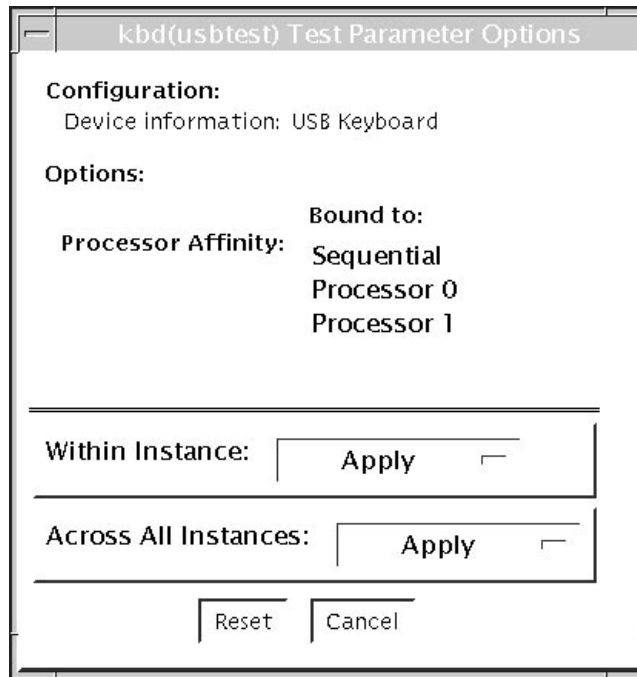
**TABLE 21-1**    `usbtest` Subtests

Subtest	Description
<i>USB Audio Devices</i>	
Tone	Supported in Connection test mode only. This subtest generates two seconds of sound output to the speakers. You must decide, by listening, if the test passed or failed.
Record/Play	Records and plays one second of random data at 8kHz sampling. This subtest reads random data from the USB microphone port and plays the music back to the USB speakers.
Audio	Plays a 30 second music file that is output to the speakers. You must decide, by listening, if the test passed or failed. Things to listen for are distortion or lack of music.
<i>USB Printer Devices</i>	
getdevicid	Retrieves the IEEE 1284 ID string of the printer.
Printer	Prints strings of ASCII characters (from 0x32 to 0x7e), and can also print the postscript file <code>usbpppdata.ps</code> to the printer.

## usbtest Options

To reach the following dialog box below, right-click on the test name in the System Map and select Test Parameter Options. If you do not see this test in the System Map, you might need to expand the collapsed groups, or your system might not include the device appropriate to this test. Refer to the *SunVTS User's Guide* for more details.





**FIGURE 21-1** usbtest Test Parameter Options Dialog Box

**TABLE 21-2** usbtest Options

Option	Description
<i>USB Audio Devices</i>	
Volume	Specifies the volume of 0 to 255. The default is 80.
Audiotest	Enables or disables the audio music test. The default is Enable.
<i>USB Printer Devices</i>	
DeviceID	Specifies the path to the printer
Printer	Enables or disables the Printer subtest. This test requires a printer to be attached to the USB port.
Printer_data_type	Specifies the test data: ascii, postscript, or both.
Printer_delay_seconds	Specifies, in seconds, the delay after the Printer subtest is performed

## usbtest Test Modes

This test supports Connection and Functional test modes.

**TABLE 21-3**    *usbtest* Supported Test Modes

Test Mode	Description
Connection	For a USB audio device, the test outputs two seconds of sound to the speakers For a USB keyboard device, the test runs the full test. For USB printer device, the test runs only the <code>getdev</code> id subtest.
Functional	For a USB audio device, the test performs Records and Plays subtest by default. You can enable Audio Music test. For a USB keyboard device, the test runs the full test. For a USB printer device, the <code>getdev</code> id subtest is enabled by default and the Printer subtest is disabled by default.

## usbtest Command Line Syntax

```
/opt/SUNWvts/bin/usbtest -cvf -o dev=dev-name, getdevid=
Enable | Disable, printer=Enable | Disable, data=ascii, postscript, delay=0-86400,
M=Enable | Disable, O=speaker | headphone | line-out, V=0-255, MF=musicfilename
```

**TABLE 21-4**    *usbtest* Command-Line Syntax

Argument	Description
<b>dev</b> =dev-name	Target device name. This test runs on the specified device name. The default device name is <code>/dev/kbd</code> .
<i>USB Printer Devices</i>	
<b>getdev</b> id=Enable   Disable	Enables or disables <code>getdev</code> id subtest. This option is available only for a USB printer device. This option is enabled by default.
<b>printer</b> =Enable   Disable	Enables or disables the printer test. This option is disabled by default. This option requires a printer to be attached to the USB port.
<b>data</b> =ascii	Selects whether to send ASCII text or PostScript data to the printer.

**TABLE 21-4** `usbtest` Command-Line Syntax (*Continued*)

Argument	Description
<b>d</b> <b>elay</b> =0-86400	Enables you to choose a delay between passes of the printer test.
<i>USB Audio Devices</i>	
<b>M</b> = <i>Enable</i>   <i>Disable</i>	Enables or disables the music play test. The default is Enable.
<b>O</b> = <i>speaker</i>   <i>headphone</i>   <i>line-out</i>	Selects the output device that <code>usbtest</code> requires.
<b>V</b> =0-255	Music output volume. The default is 80.
<b>MF</b> = <i>musicfilename</i>	Selects the music file. The default is <code>music.au</code> .



## Virtual Memory Test (vmemtest)

- 
- [“vmemtest Description” on page 157](#)
  - [“vmemtest Swap Space Requirements” on page 158](#)
  - [“vmemtest Options” on page 158](#)
  - [“vmemtest Test Modes” on page 162](#)
  - [“vmemtest Command-Line Syntax” on page 162](#)
- 

### vmemtest Description

The `vmemtest` checks virtual memory—the combination of physical memory and the swap partitions of the disk(s).

---

**Note** – This test might not stop immediately after being disabled.

---

This test uses the Solaris `valloc` (page-aligned) system call to allocate, write, read, and compare virtual memory. These operations normally cause heavy paging activity on the system and simulate a stressful environment for the operating system. This test also detects memory read errors, and addressing problems, and displays the corresponding virtual memory addresses on failure.

---

**Note** – Do not run the `vmemtest` with `fwcamtest` at the same time on any Sun Blade system. This will cause `vmemtest` to fail.

---

---

**Note** – ECC error reporting is not yet supported on x86 platforms.

---

---

## vmemtest Swap Space Requirements

Running this test places a significant burden on the operating system, since it uses the majority of swap space available for testing. Use the `vmemtest` swap space reserve option when nonSunVTS test processes are started after SunVTS testing has started. Refer to the *SunVTS User's Guide* for a complete discussion of swap space requirements.

---

## vmemtest Options

To reach the following dialog box, right-click on the test name in the System Map and select Test Parameter Options. If you do not see this test in the System Map, you might need to expand the collapsed groups, or your system might not include the device appropriate to this test. Refer to the *SunVTS User's Guide* for more details.

kmem(vmemtest) Test Parameter Options

**Configuration:**  
Total Swap: 8464MB

**Options:**

Mode:
☒ Regular
☐ Page

Reserve:

Test Amount(MB):

Contiguous Errors:

ECC Error Monitor:
☒ Enabled
☐ Disabled

ECC Error Threshold:

Sequential Test:
☒ Enabled
☐ Disabled

Data Pattern(seq):

File Caching Test:
☒ Enabled
☐ Disabled

Data Pattern(file):

Random Test:
☐ Enabled
☒ Disabled

Data Pattern(rand):

Page Striding Test:
☐ Enabled
☒ Disabled

Data Pattern(page):

March Test:
☐ Enabled
☒ Disabled

Data Pattern(march):

Block Copy Test:
☐ Enabled
☒ Disabled

User Defined Pattern:

Bound to:

Processor Affinity:

Instance:

Within Instance:

Across All Instances:

**FIGURE 22-1** vmemtest Test Parameter Options Dialog Box

**TABLE 22-1** `vmemtest` Options

<b>vmemtest Options</b>	<b>Description</b>
Mode	<p>Two modes are available:</p> <ul style="list-style-type: none"><li>• Regular mode tests the specified amount of memory as one block and passes as the size argument to the different test algorithm functions (subtests).</li><li>• Page mode tests assign virtual memory one page at a time. Each page is mapped to the temporary file <code>/tmp/vmem.page</code> and is then paged out to storage once test data is written. Next, the temporary page is paged back into memory for a read and compare.</li></ul> <p><code>vmemtest</code> runs in Regular mode as default setting.</p>
Reserve	<p>Specifies the amount of memory to reserve from being tested by <code>vmemtest</code>. The test ensures that this amount of memory is left free on the system while evaluating the size of memory for testing. If specified value of reserve is zero, this test will use a default value evaluated based on the available free swap space for the instance. Trying to reserve more memory than available free memory by this instance will cause the test to fail.</p>
Test Amount	<p>An amount can be specified to test the virtual memory, instead of the default. The default value is 0, which means the default memory size is evaluated within the test.</p> <p>Record the memory configuration details on the target system before selecting nondefault setting for the amount option.</p> <p>If negative values are specified, the test assumes the default setting. The actual size of memory tested by the instance is always evaluated with reference to the available free swap space on the system.</p>
<code>vmemtest</code> Configuration	<p>The amount of memory listed in the Configuration field is equivalent to the sum of the used and available swap space amounts returned by the <code>swap -s</code> command. This number indicates the amount of virtual memory found, rounded up to the nearest Kbyte.</p>
Contiguous Errors	<p>Specifies the max number of contiguous memory errors, which will be considered and counted as one noncontiguous error. The default value is 10.</p>



**TABLE 22-1** `vmemtest` Options (*Continued*)

<b><code>vmemtest</code> Options</b>	<b>Description</b>
Test Method	<p><code>vmemtest</code> runs the Sequential and File Caching subtests by default.</p> <ul style="list-style-type: none"> <li>• Sequential subtest – Tests the whole memory from the beginning address to the end address in a sequence.</li> <li>• Address Random subtest – Randomly selects memory addresses within the specified range to test.</li> <li>• Page Striding subtest – Noncontiguous memory test, implemented sequentially and non-sequentially.</li> <li>• Sequential striding – Tests from the first page to the last page, within a specified test range. Only one word is tested per page.</li> <li>• Nonsequential striding – Tests randomly from first to last page, within a specified memory range. Alternates testing one word per page until all pages are tested.</li> <li>• Block Copy test – Writes and reads data between two memory blocks. Each memory block is half the memory to be tested.</li> <li>• File Caching test – Aimed at improving performance through the use of file caching in the Solaris kernel. This test is useful for large memory configurations. This test takes 30 to 70% less time than the Sequential test method.</li> </ul>
Predefined Pattern	<p>Select one of the following patterns to use for the test:</p> <ul style="list-style-type: none"> <li>• Address—uses the virtual addresses of the tested memory locations.</li> <li>• walk_1—uses a pattern that starts with 0x80000000 through 0x11111111.</li> <li>• walk_0—uses a pattern that starts with 0x7fffffff through 0x00000000.</li> <li>• 0x00000000—uses all ones and zeros for testing.</li> <li>• 0x5aa55aa5—uses 0x5aa55aa5 pattern.</li> <li>• 0xdb6db6db—uses 0xdb6db6db pattern.</li> <li>• Checkerboard—uses 0xaaaaaaaa patterns.</li> <li>• UserDefined—uses the pattern that is specified in the User Defined Pattern area (see below).</li> </ul>
User Defined Pattern	<p>Only used if the Predefined Pattern is set to UserDefined. The pattern specified should be in the form of an 8-digit, hexadecimal number such as 0x2a341234. Default setting is up=0x7ff77ff7</p>
Instance	<p>Specifies how many copies of the <code>vmemtest</code> test to run.</p>

# vmemtest Test Modes

TABLE 22-2 vmemtest Supported Test Modes

Test Mode	Description
Functional	Writes a pattern to an amount of the specified virtual memory. The data is then read back and compared. If there is a miscompare, the virtual address is reported and the data is read again and compared.

In Functional test mode, `vmemtest` writes a pattern to an amount of virtual memory specified by the user. Then the data is read back and compared. If there is a miscompare, the data is read again and compared. Whenever there is a miscompare, the virtual address is reported.

## vmemtest Command-Line Syntax

```
/opt/SUNWvts/bin/vmemtest standard_arguments -o mode=type, reserve=  
n, amount=n, cerr=n, type1=enable | disable, pp1=pattern, type2=enable | disable, pp2=  
pattern, type3=n, pp3=pattern, type4=enable | disable, pp4=pattern, type5=  
enable | disable, pp5=pattern, type6=enable | disable, up=hex_pattern
```

TABLE 22-3 vmemtest Command-Line Syntax

Argument	Explanation
<b>mode=Page   Regular</b>	Specifies which mode of the <code>vmemtest</code> to run. <ul style="list-style-type: none"><li>• <b>Page</b> – tells the write/read memory test to proceed one system memory page at a time.</li><li>• <b>Regular</b> – uses the <code>valloc</code> option to allocate the entire assigned memory, which is read and compared one long word at a time.</li></ul>
<b>reserve=n</b>	Specifies the amount of MB of virtual memory to reserve.
<b>amount=n</b>	Specifies the number of MB of memory to be tested instead of the default.
<b>cerr=n</b>	Specifies the maximum number of contiguous errors to be counted as one non contiguous error.

**TABLE 22-3** vmemtest Command-Line Syntax

Argument	(Continued)Explanation
<b>type1=value</b> <b>pp1=pattern</b>	type1 is the sequential test. The value is Enabled or Disabled. The default is Enabled. The default for the pp1 pattern is address. Select the pp1 pattern from: address,walk_0,walk_1,Checkerboard, 0x00000000,0xffffffff,0x5aa55aa5, 0xdb6db6db,random, <i>UserDefined</i>
<b>type2=value</b> <b>pp1=pattern</b>	type2 is the File cache test. The value is Enabled or Disabled. The default is Enabled. The default for the pp1 pattern is address. Select the pp1 pattern from: address,walk_0,walk_1,Checkerboard, 0x00000000,0xffffffff,0x5aa55aa5,0xdb6db6db, random, <i>UserDefined</i>
<b>type3=value</b> <b>pp3=pattern</b>	type3 is the Random address test. The value is Enabled or Disabled. The default is Disabled. The default of the pp3 pattern is checkerboard. Select the pp3 pattern from: Checkerboard,0x00000000,0xffffffff, 0x5aa55aa5,0xdb6db6db, <i>UserDefined</i>
<b>type4=value</b> <b>pp4=pattern</b>	type4 is the page_striding test. The value is Enabled or Disabled. The default is Disabled. The default of the pp4 pattern is checkerboard. Select the pp4 pattern from: Checkerboard,0x00000000,0xffffffff, 0x5aa55aa5,0xdb6db6db, <i>UserDefined</i>
<b>type5=value</b> <b>pp5=pattern</b>	type5 is the march_c test. The value is Enabled or Disabled. The default is Disabled. The default for the pp5 pattern is checkerboard. Select the pp5 pattern from: Checkerboard,0x00000000,0xffffffff, 0x5aa55aa5,0xdb6db6db, <i>UserDefined</i>
<b>type6=value</b>	type6 is the Block_Copy test. The value is Enabled or Disabled. The default is Disabled. Note – The Block_Copy subtest uses its own set of the data patterns predefined in the test. It does not require any user specified data patterns for testing.
<b>up=hex_address</b>	Only used if the pp argument is set to UserDefined. The pattern specified should be in the form of a 8-digit, hexadecimal number such as 0x2a341234.



## Loopback Connectors

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- “Loopback Connection Overview” on page 165
- “25-Pin RS-232 Loopback Plug” on page 167
- “25-Pin RS-232 Port-to-Port Loopback Cable” on page 169
- “8-Pin to 8-Pin Loopback Cable” on page 170
- “8-Pin Loopback Plug” on page 171
- “25-Pin Port A-to-Port B Loopback Plug” on page 172
- “25-Pin Port A-to-A Port B-to-B Loopback Plug” on page 174
- “96-Pin Female Loopback Connector” on page 175
- “96-Pin Female Special Loopback Connector” on page 177
- “37-Pin RS-449 Loopback Cable” on page 178
- “37-Pin RS-449 Loopback Plug” on page 179
- “9-Pin Male Single-Port Loopback Plug” on page 180
- “9-Pin Female Single-Port Loopback Plug” on page 181
- “9-Pin to 25-Pin Port-to-Port Loopback Cable” on page 182
- “9-Pin to 9-Pin Port-to-Port Loopback Cable” on page 183
- “NT to TE Loopback Cable” on page 183
- “Twisted-Pair Ethernet (TPE) Loopback Cable for Fast Ethernet” on page 184
- “TPE Loopback Cable for Gigabit and 10/100 Ethernet” on page 184
- “x86 Platform RJ-45 Serial Port Loopback Connector” on page 185
- “9-Pin Male Single-Port Loopback Plug” on page 185
- “9-Pin Female Single-Port Loopback Plug” on page 186
- “9-Pin Male DB-9 External Loopback Connector” on page 186
- “9-Pin Female DB-9 External Loopback Connector” on page 187

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## Loopback Connection Overview

Loopback connectors aid in testing communication ports. The connectors take the form of either a single plug or a port-to-port cable with some communication connections shorted (looped-back).

**Note** – Loopback connectors must be wired properly and connected firmly for the Serial Port tests to work correctly. Miswired, poorly soldered, or missing loopback connectors can cause erroneous diagnostic error messages.

The following table depicts the pin assignments for most loopback plugs and cables that may be used when testing a system.

**TABLE A-1** Loopback Connector Pin Assignments

Signal Description	EIA	CCITT #	RS-449 A	RS-449 B	DIN 8 8-pin round	DB9 9-pin	DB25 25-pin	Direction	Alpha ID
Chassis/Frame Ground	AA	101	1	NC*	NC*	NC*	1	None	AA
Transmit Data (TxDa)	BA	103	4	22	3	3	2	Output	BA
Receive Data (RxDa)	BB	104	6	24	5	2	3	Input	BB
Request To Send (RTSa)	CA	105	7	25	6	7	4	Output	CA
Clear To Send (CTSa)	CB	106	9	27	2	8	5	Input	CB
Data Set Ready (DSRa)	CC	107	11	29	NC*	6	6	Input/output	CC
Signal Ground (SG)	AB	102	19	NC*	4	5	7	None	AB
Data Carrier Detect (DCDa)	CF	109	13	31	7	1	8	Input	CF
Transmit Clock In (TRxCa)	DB	114	5	23	NC*	NC*	15	Input	DB
Receive Clock In (RTxCa)	DD	115	8	26	8	NC*	17	Input	DD
Data Terminal Ready (DTRa)	CD	108	12	30	1	4	20	Output	CD
External Clock Out (TRxCa)	DA	113	17	35	NC*	NC*	24	Output	DA
Secondary Data Carrier Detect (DCDb)	SCF	122	NC*	NC*	NC*	NC*	12	Input	SCF
Secondary Clear to Send (CTSb)	SCB	121	NC*	NC*	NC*	NC*	13	Input	SCB

**TABLE A-1** Loopback Connector Pin Assignments *(Continued)*

Signal Description	EIA	CCITT #	RS-449 A	RS-449 B	DIN 8 8-pin round	DB9 9-pin	DB25 25-pin	Direction	Alpha ID
Secondary Transmit Data (TxDb)	SBA	118	NC*	NC*	NC*	NC*	14	Output	SBA
Secondary Receive Data (RxDb)	SBB	119	NC*	NC*	NC*	NC*	16	Input	SBB
Secondary Request to Send (RTSb)	SCA	120	NC*	NC*	NC*	NC*	19	Output	SCA

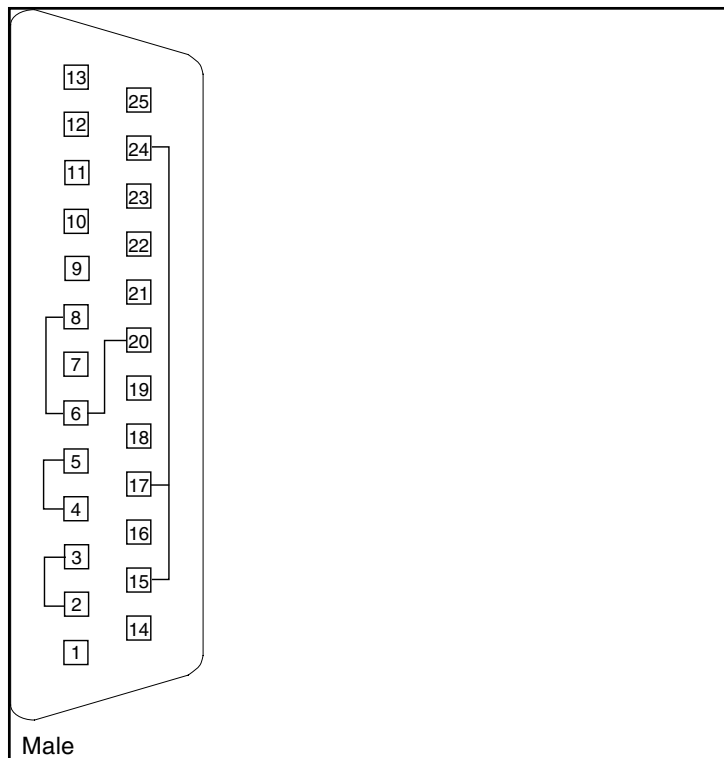
\*NC = No connection

# 25-Pin RS-232 Loopback Plug

[FIGURE A-1](#) shows the RS-232 and RS-423 single-port loopback plug, which is a specially wired male DB-25 connector. The plug is connected to a serial port in the back of the system under test. The following table lists the pin connections for connecting the first plug to the second plug:

**TABLE A-2** 25-pin RS-232 Loopback Plug Wiring Plug Connections

First Plug	Second Plug
Pin 3	Pin 2
Pins 6 and 8	Pin 20
Pins 15 and 17	Pin 24



**FIGURE A-1** 25-pin RS-232 Loopback Plug Wiring Diagram



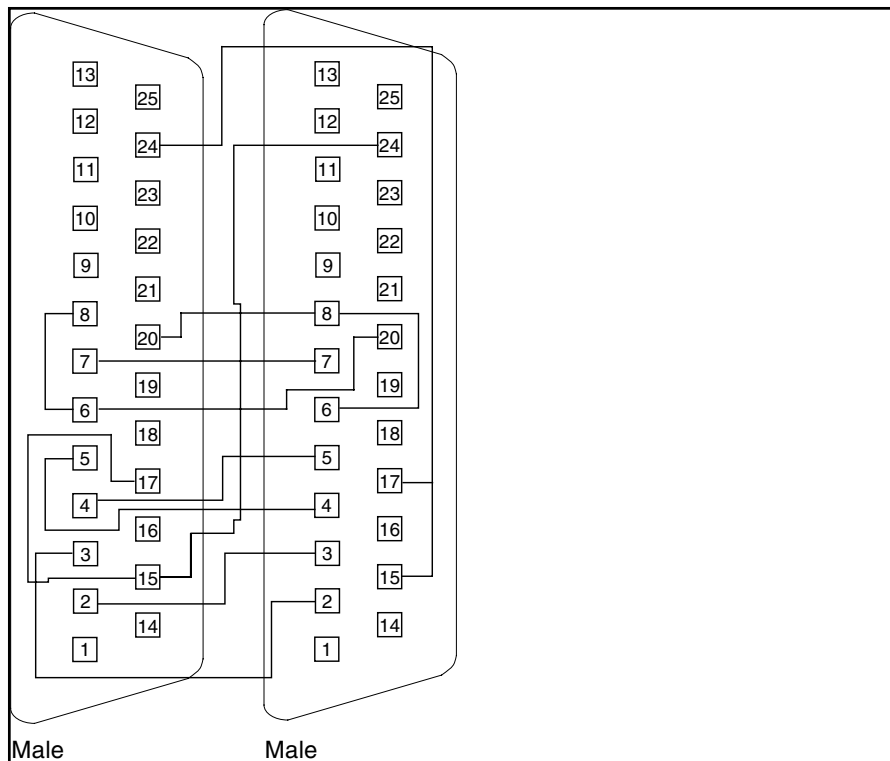
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# 25-Pin RS-232 Port-to-Port Loopback Cable

FIGURE A-2 shows how to connect 25-pin RS-232 and RS-423 port to 25-pin RS 232 and RS 423 port loopback cables (two DB-25 connections). The cable is connected to a pair of serial ports in the back of the system under test. Both plugs are male. The following table lists the pin connections for connecting the first plug to the second plug:

**TABLE A-3** 25-pin RS-232 Port-to-Port Loopback Cable Wiring Plug Connections

First Plug	Second Plug
Pin 2	Pin 3
Pin 3	Pin 2
Pin 4	Pin 5
Pin 5	Pin 4
Pins 6 and 8	Pin 20
Pin 7	Pin 7
Pins 15 and 17	Pin 24
Pin 20	Pins 6 and 8
Pin 24	Pins 15 and 17



**FIGURE A-2** 25-pin RS-232 Port-to-Port Loopback Cable Wiring Diagram

# 8-Pin to 8-Pin Loopback Cable

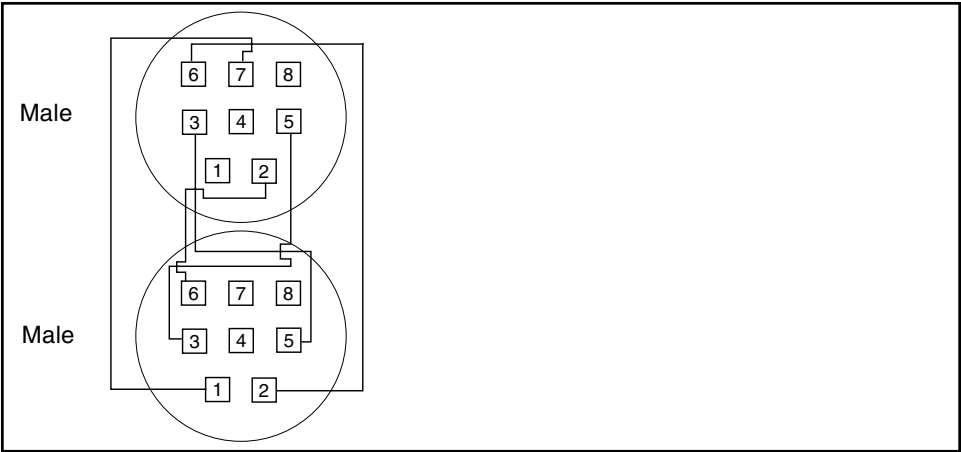
**FIGURE A-3** shows how to connect 8-pin round DIN RS-232 port to RS-423 to 8-pin round-DIN RS-232 and RS-423 port loopback cables. Both plugs are male. The following table lists the pin connections for connecting the first plug to the second plug:

**TABLE A-4** 8-Pin to 8-Pin Loopback Cable Wiring Plug Connections

First Plug	Second Plug
Pin 3	Pin 5
Pin 5	Pin 3

**TABLE A-4** 8-Pin to 8-Pin Loopback Cable Wiring Plug Connections (Continued)

First Plug	Second Plug
Pin 6	Pin 2
Pin 2	Pin 6
Pin 7	Pin 1



**FIGURE A-3** 8-Pin to 8-Pin Loopback Cable Wiring Diagram

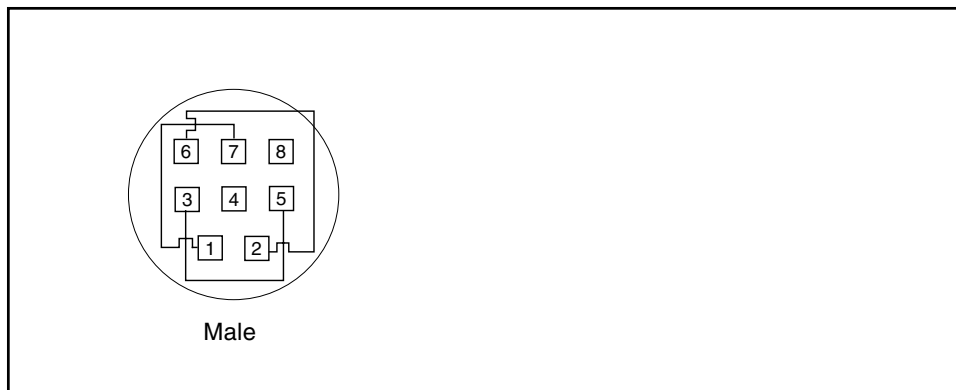
Pin 8, Receive clock In (DD), remains unconnected.

# 8-Pin Loopback Plug

**FIGURE A-4** shows how to connect male 8-pin round-DIN RS-232 and RS-423 single-port loopback plugs. The following table lists the pin connections for connecting the first plug to the second plug:

**TABLE A-5** 8-Pin Loopback Plug Wiring Plug Connections

First Plug	Second Plug
Pin 3	Pin 5
Pin 6	Pin 2
Pin 1	Pin 7



**FIGURE A-4** 8-Pin Loopback Plug Wiring Diagram

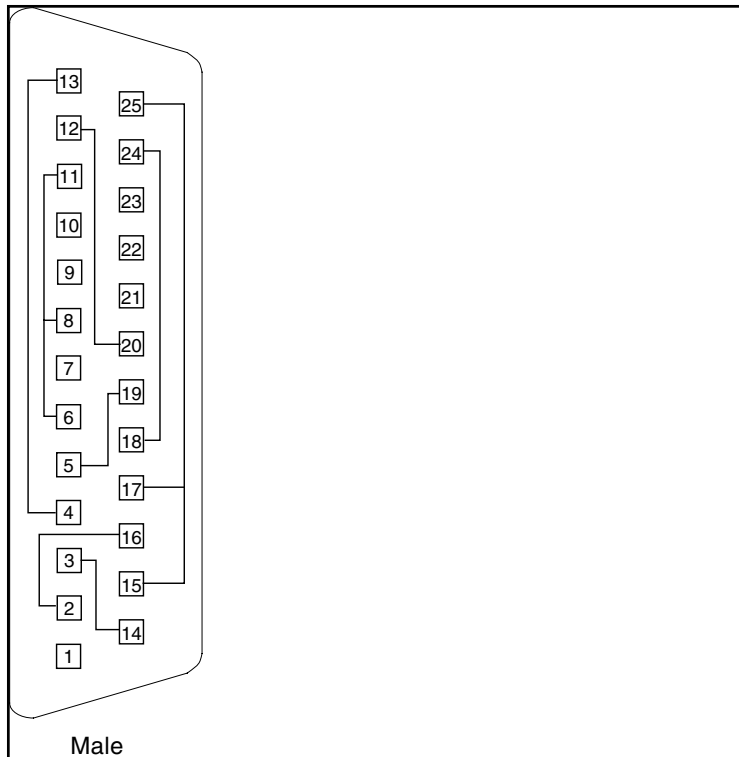
Pin 8, Receive Clock In (DD), remains unconnected.

## 25-Pin Port A-to-Port B Loopback Plug

**FIGURE A-5** shows how to connect a 25-pin Port A to Port B loopback plug for most systems. The following table lists the pin connections for connecting the first plug to the second plug:

**TABLE A-6** Port A-to-Port B Loopback Plug Wiring Plug Connections

First Plug	Second Plug
Pin 16	Pin 2
Pin 3	Pin 14
Pin 13	Pin 4
Pin 5	Pin 19
Pins 6 and 8	Pin 11
Pin 12	Pin 20
Pin 18	Pin 24
Pins 15 and 17	Pin 25



**FIGURE A-5** Port A-to-Port B Loopback Plug Wiring Diagram

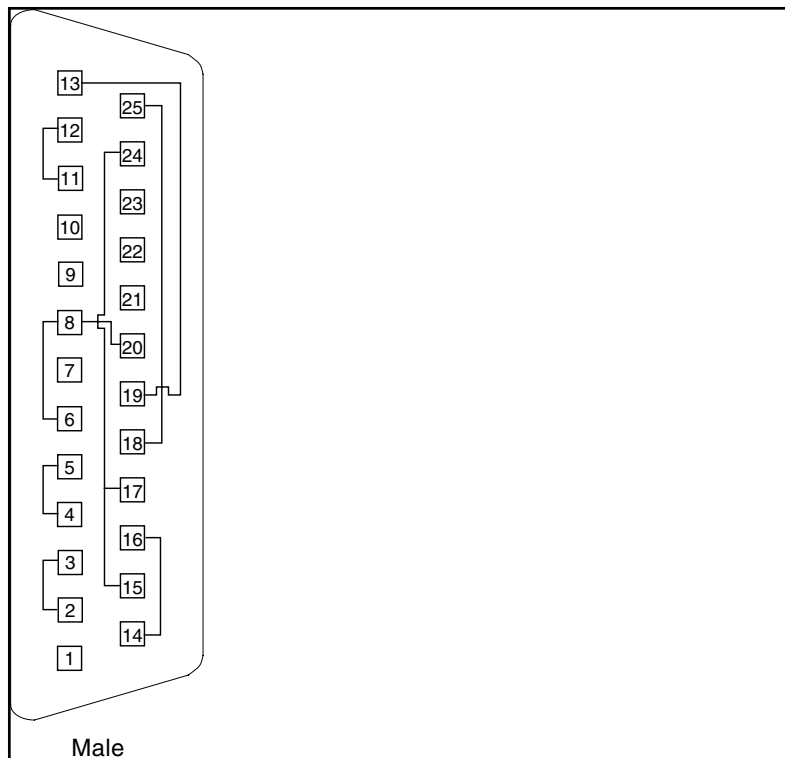
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## 25-Pin Port A-to-A Port B-to-B Loopback Plug

If your system has a single communications port to connect it to peripherals, follow the connection instructions in [FIGURE A-6](#) to make a male 25-pin loopback plug for that communication port. The following table lists the pin connections for connecting the first plug to the second plug:

**TABLE A-7** Port A-to-A, Port B-to-B Loopback Plug Wiring Plug Connections

First Plug	Second Plug
Pin 3	Pin 2
Pin 5	Pin 4
Pins 6 and 8	Pin 20
Pin 12	Pin 11
Pin 13	Pin 19
Pin 16	Pin 14
Pins 15 and 17	Pin 24
Pin 25	Pin 18



**FIGURE A-6** Port A-to-A, Port B-to-B Loopback Plug Wiring Diagram

## 96-Pin Female Loopback Connector

**FIGURE A-7** shows a 96-pin connector that can be ordered from Sun (part number 370-1366). The following table lists the pin connections for connecting the first plug to the second plug:

**TABLE A-8** 96-Pin Female Loopback Connector Wiring Plug Connections

First Plug	Second Plug
Pins 4 and 12	Pin 77
Pin 5	Pin 20
Pins 6	Pin 36
Pin 10	Pin 11

**TABLE A-8** 96-Pin Female Loopback Connector Wiring Plug Connections (*Continued*)

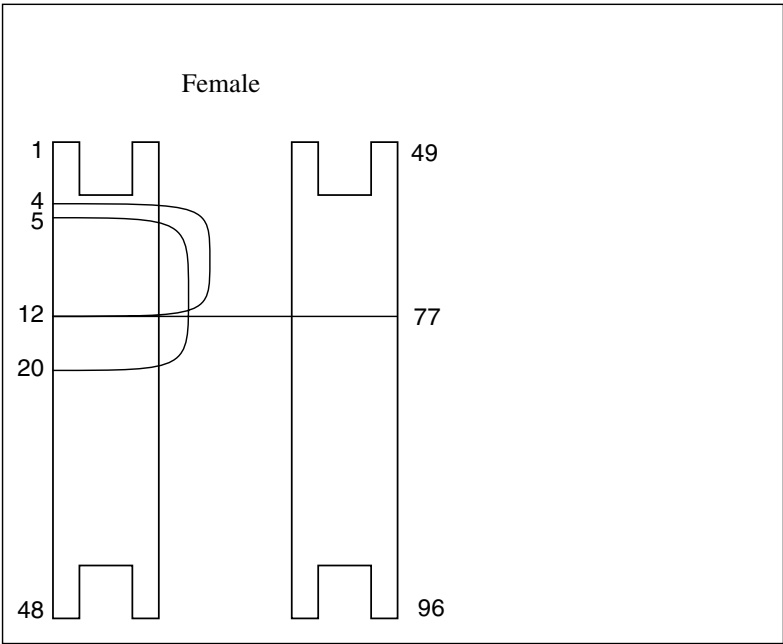
First Plug	Second Plug
Pin 13	Pin 16
Pin 14	Pin 15
Pin 18	Pin 19
Pin 21	Pin 24
Pin 28	Pin 60
Pin 29	Pin 68
Pin 30	Pin 34
Pin 37	Pin 40
Pin 38	Pin 39
Pin 42	Pin 43
Pin 45	Pin 48
Pin 46	Pin 47
Pin 52	Pin 78
Pin 53	Pin 55
Pin 54	Pin 75
Pin 58	Pin 59
Pin 61	Pin 64
Pin 62	Pin 63
Pin 66	Pin 67
Pin 69	Pin 72
Pin 76	Pin 79
Pin 82	Pin 83
Pin 85	Pin 88
Pin 86	Pin 87
Pin 90	Pin 91
Pin 93	Pin 96
Pin 94	Pin 95

The following are the materials used for this plug:

- PCR-E96FA(1)
- PCS-E96LKPA(1)
- 3751 Metal Plug(1) (9563K42)



- AWG28 Madison Cable(8" long) UL/CSA Approved

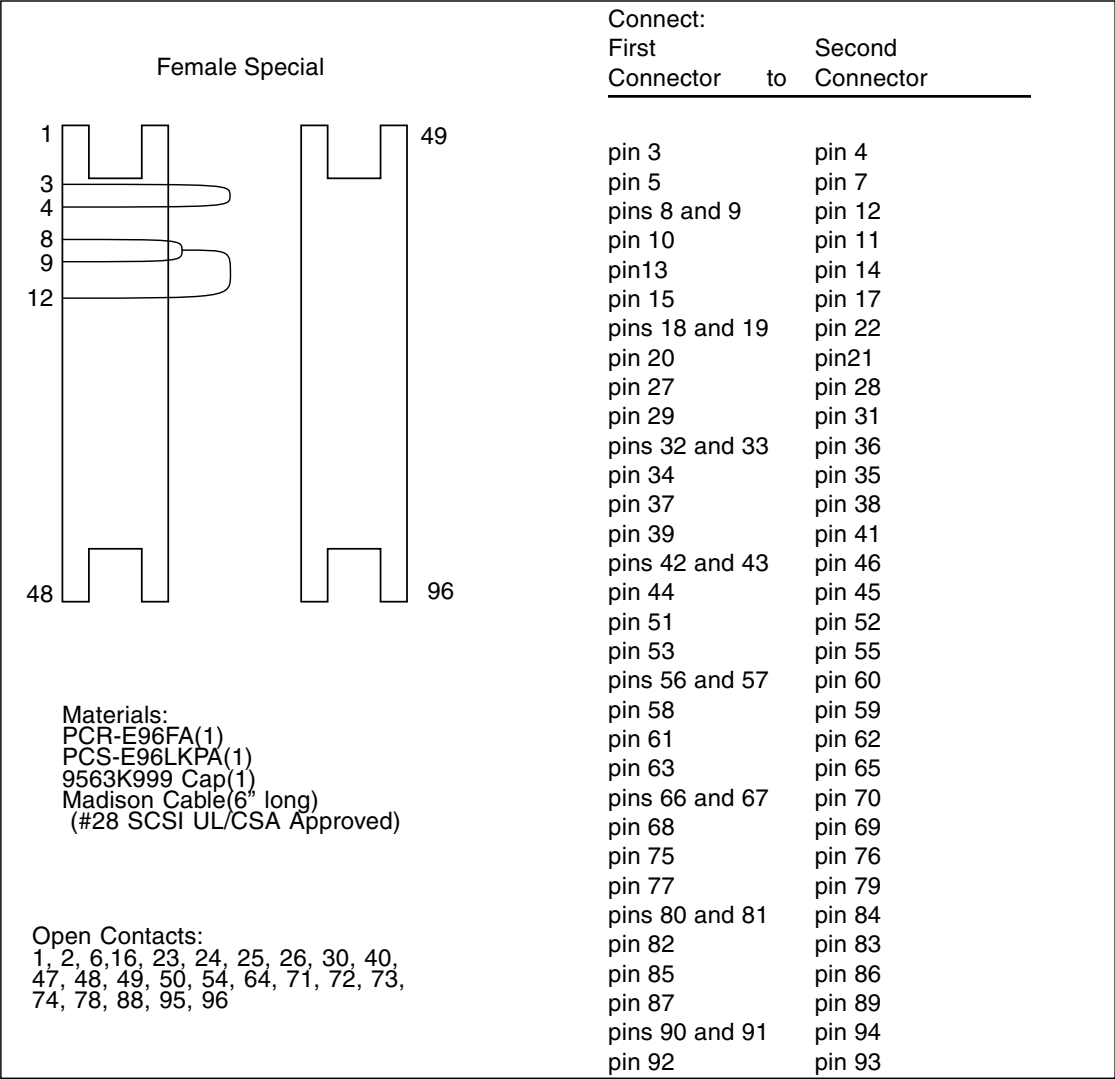


**FIGURE A-7** 96-Pin Female Loopback Connector Wiring Diagram

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## 96-Pin Female Special Loopback Connector

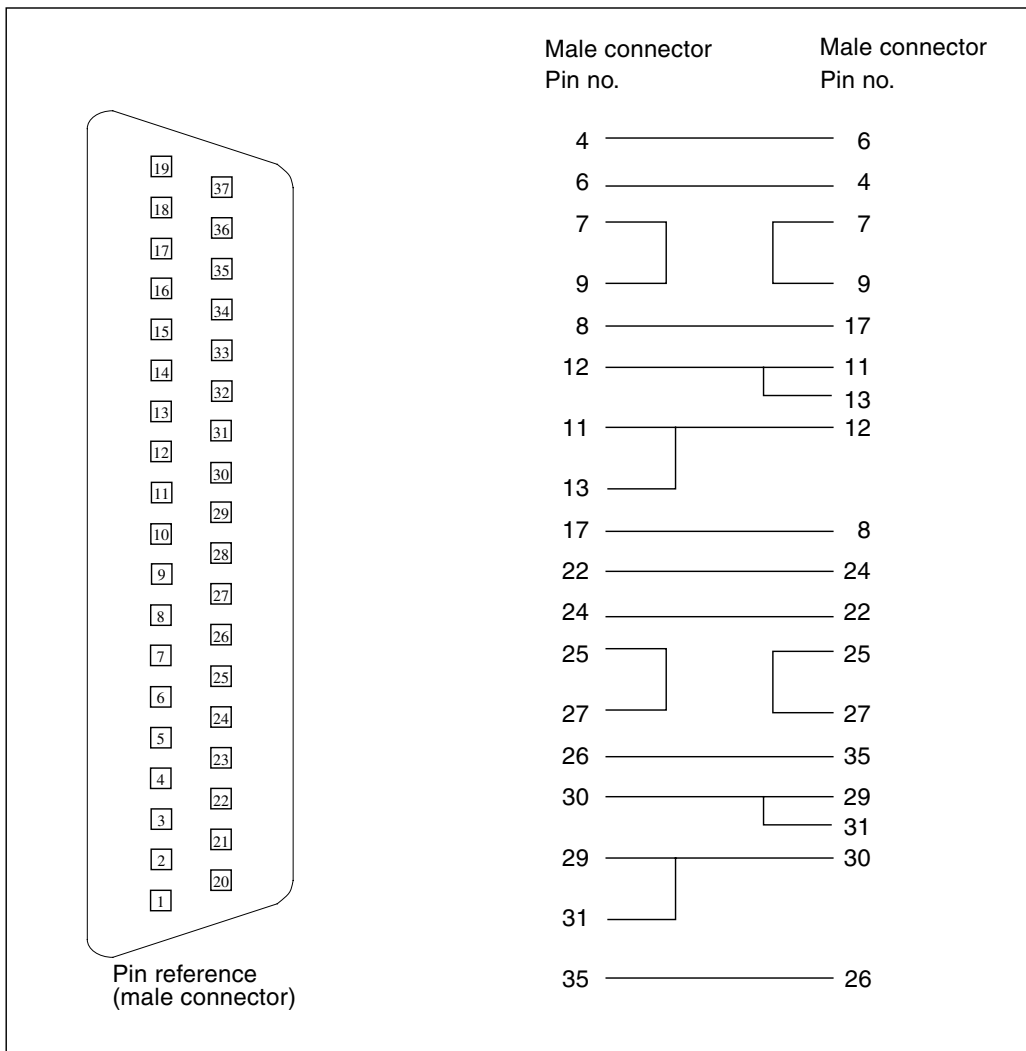
This 96-pin connector can be ordered from Sun (part number 370-1381).



**FIGURE A-8** 96-Pin Female Special Loopback Connector Wiring Diagram

# 37-Pin RS-449 Loopback Cable

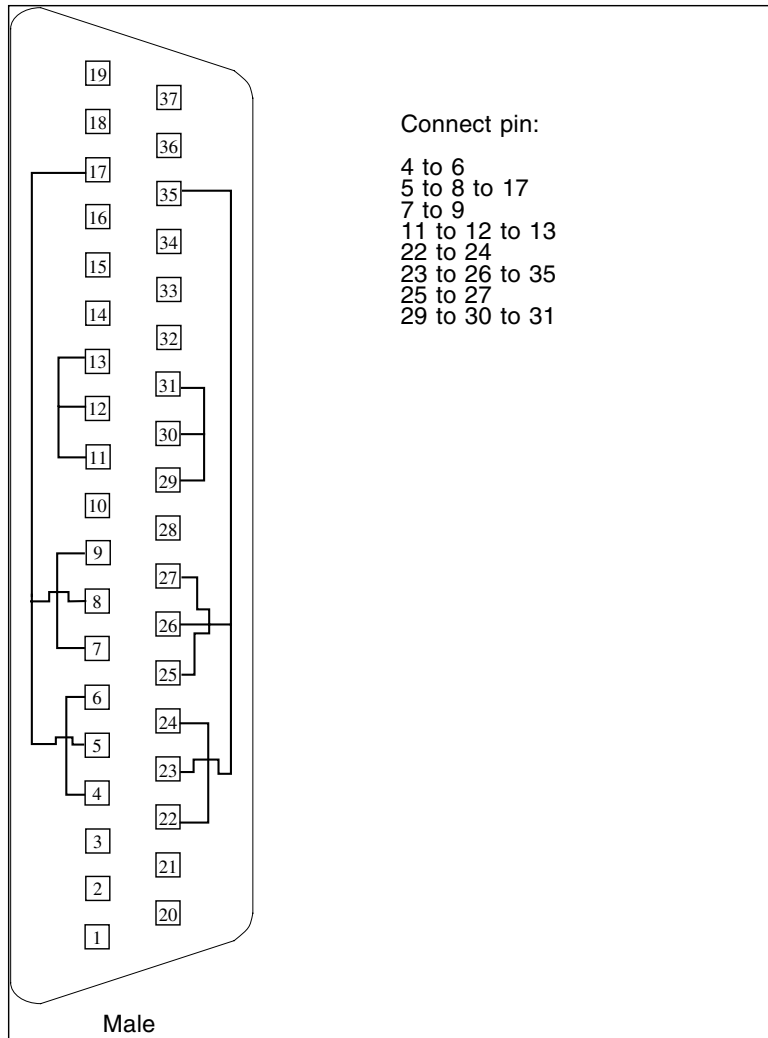
Use these wiring instructions for a loopback cable for two 37-pin RS-449 synchronous ports.



**FIGURE A-9** 37-Pin RS-449 Loopback Cable Wiring Diagram

## 37-Pin RS-449 Loopback Plug

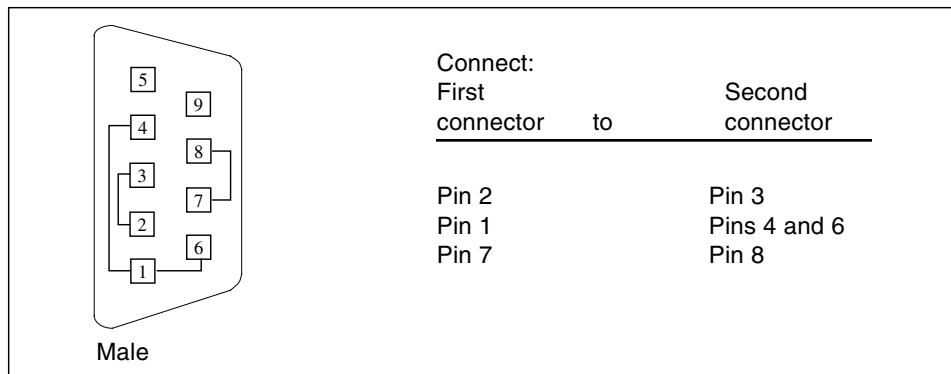
Use these wiring instructions to make a male 37-pin RS-449 loopback plug. This plug is also available from Sun (part number 530-1430).



**FIGURE A-10** 37-Pin RS-449 Loopback Plug Wiring Diagram

## 9-Pin Male Single-Port Loopback Plug

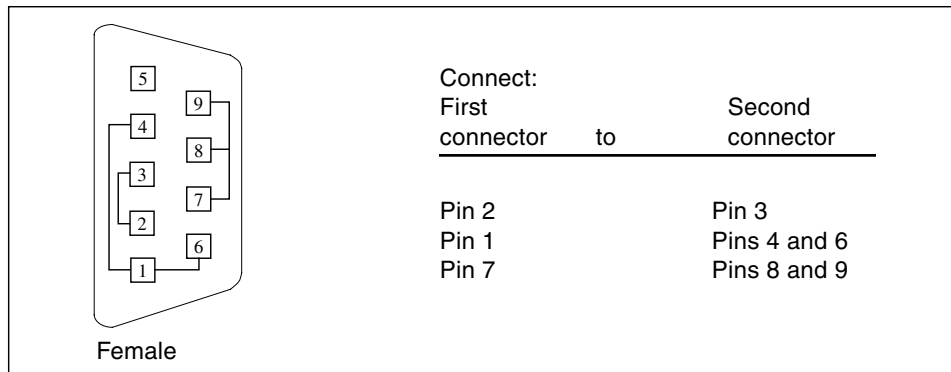
Use these wiring instructions for male 9-pin RS-232 and RS-423 single-port loopback plugs.



**FIGURE A-11** 9-Pin Male Single-Port Loopback Plug Wiring Diagram

## 9-Pin Female Single-Port Loopback Plug

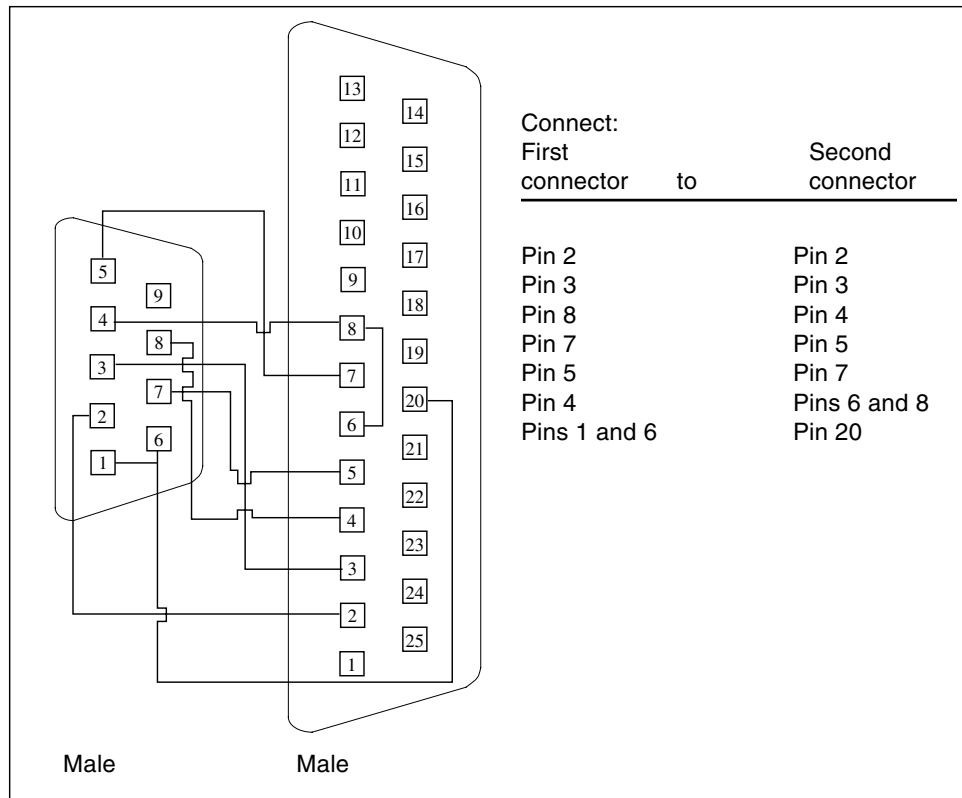
Use these wiring directions for female 9-pin RS-232 and RS-423 single-port loopback plugs. Use this loopback plug with the `pcmciaetest`.



**FIGURE A-12** 9-Pin Female Single-Port Loopback Plug Wiring Diagram

# 9-Pin to 25-Pin Port-to-Port Loopback Cable

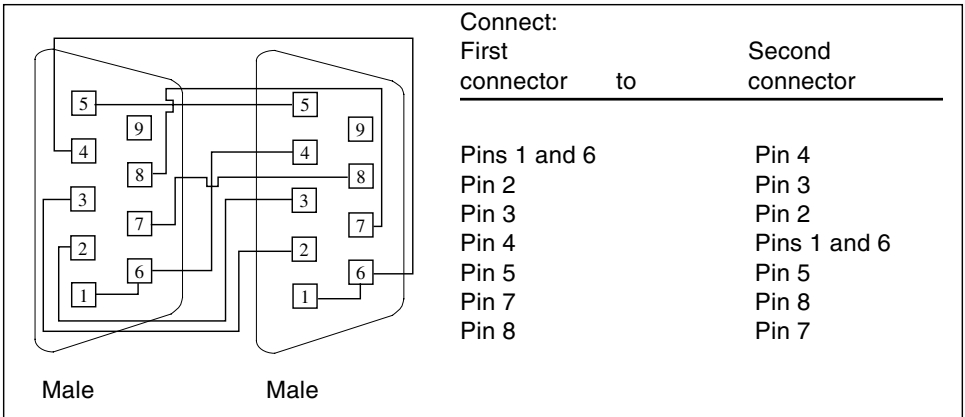
Use these wiring instructions for a 9-pin RS-232 and RS-423 port to 25-pin RS-232 and RS 423 port loopback cables. Both connectors are male.



**FIGURE A-13** 9-Pin to 25-Pin Port-to-Port Loopback Cable Wiring Diagram

# 9-Pin to 9-Pin Port-to-Port Loopback Cable

Use these wiring instructions for 9-pin RS-232 and RS 423 port to 9-pin RS-232 and RS-423 port loopback cables. Both plugs are male.



**FIGURE A-14** 9-Pin to 9-Pin Port-to-Port Loopback Cable Wiring Diagram

This cable has no Sun part number assigned to it.

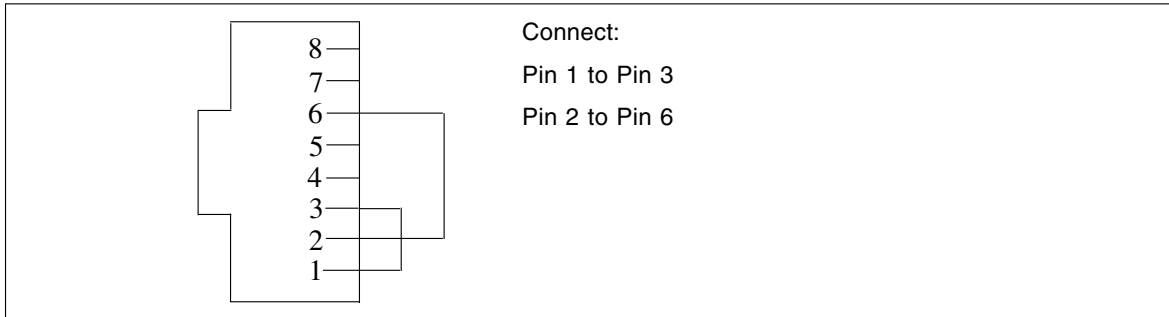
# NT to TE Loopback Cable

Using two standard RJ-45 connectors, and connect pin 1 to pin 1, pin 2 to pin 2, and so on, for all pins. This loopback is a straight-through connection.

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## Twisted-Pair Ethernet (TPE) Loopback Cable for Fast Ethernet

Use the following wiring instructions for standard RJ-45 connectors for Fast Ethernet. Use loopback cable in `netlbtest` for `eri` devices.



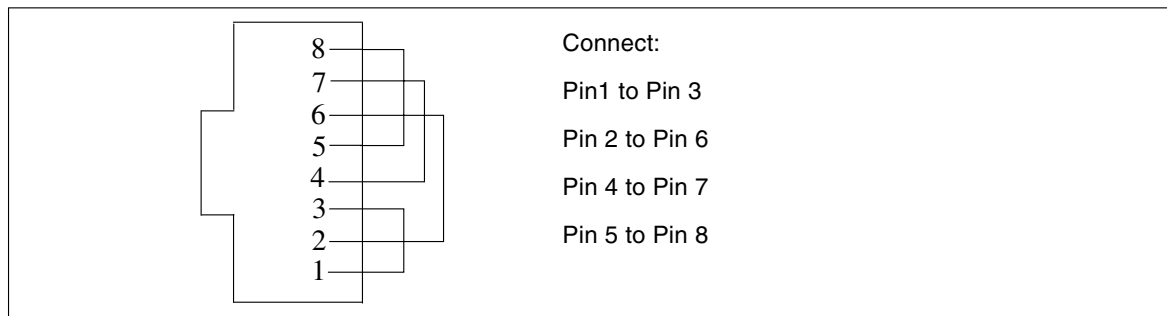
**FIGURE A-15** Twisted-Pair Ethernet (TPE) Loopback Cable Wiring Diagram for Fast Ethernet

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## TPE Loopback Cable for Gigabit and 10/100 Ethernet

Use the following wiring instructions for RJ-45 plugs for Gigabit and 10/100 Ethernet. This loopback cable is used in `netlbtest` for Gigabit and 10/100 devices.

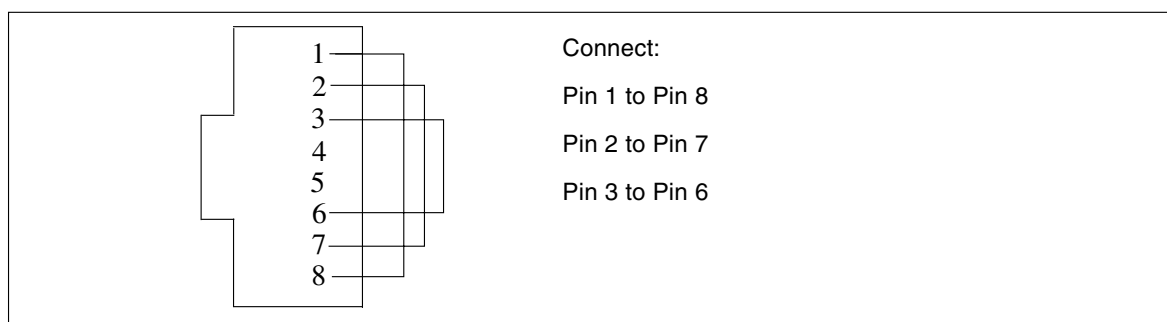




**FIGURE A-16** TPE Loopback Cable Wiring Diagram for Gigabit and 10/100 Ethernet

## x86 Platform RJ-45 Serial Port Loopback Connector

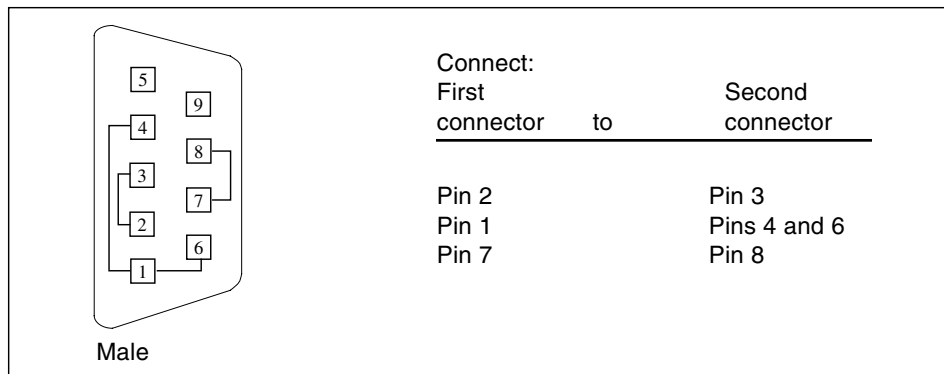
Use the following wiring instructions for x86 platform RJ-45 serial port loopback plugs.



**FIGURE A-17** x86 Platform RJ-45 Serial Port Loopback Connector

## 9-Pin Male Single-Port Loopback Plug

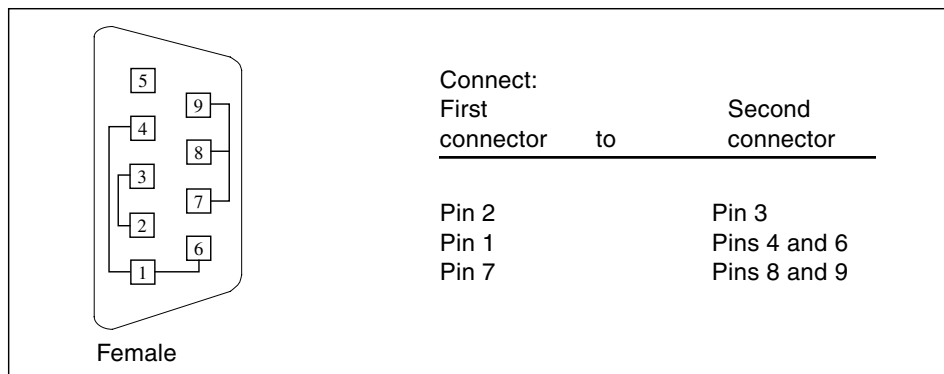
Use these wiring instructions for male 9-pin RS-232 and RS-423 single-port loopback plugs.



**FIGURE A-18** 9-Pin Male Single-Port Loopback Plug Wiring Diagram

## 9-Pin Female Single-Port Loopback Plug

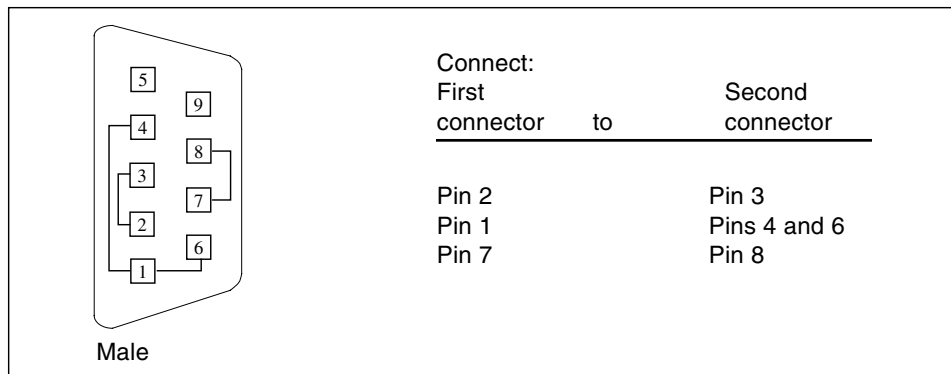
Use these wiring directions for female 9-pin RS-232 and RS-423 single-port loopback plugs. Use this loopback plug with the `pcmciaetest`.



**FIGURE A-19** 9-Pin Female Single-Port Loopback Plug Wiring Diagram

## 9-Pin Male DB-9 External Loopback Connector

Use these wiring instructions for male 9-pin DB-9 external loopback plugs.



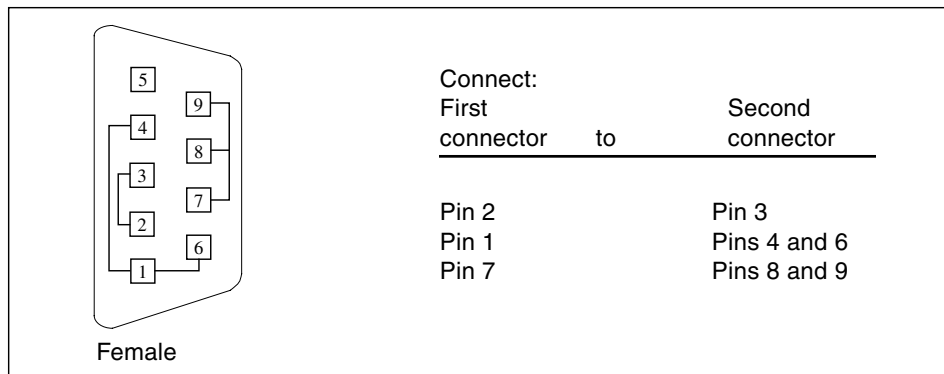
**FIGURE A-20** 9-Pin Male DB-9 External Loopback Connector Wiring Diagram

The signals and names for the DB-9 connector are as follows.

Pin	Name	Signal
1	DCD	Data Carrier Detect
2	RxD	Receive Data
3	TxD	Transmit Data
4	DTR	Data Terminal Ready
5	SGND	Signal Ground
6	DSR	Data Set Ready
7	RTS	Request To Send
8	CTS	Clear To Send
9	RI	Ring Indicator

## 9-Pin Female DB-9 External Loopback Connector

Use these wiring instructions for female 9-pin DB-9 external loopback connectors. Use this loopback connector with the netlbttest on x86 platforms.



**FIGURE A-21** 9-Pin Female DB-9 External Loopback Connector Wiring Diagram

# Glossary

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<b>administrative domain</b>	An arbitrary collection of hosts and networks that are monitored by the software as a single hierarchical entity. You may choose to divide their enterprise into several domains, each to be managed by different users.
<b>agent</b>	A software process, usually corresponding to a particular local managed host, that carries out manager requests and makes local system and application information available to remote users.
<b>bus</b>	A point-to-point network component. Used by the software to represent a network link to which many other hosts may be connected.
<b>community</b>	A string similar to a password that is used to authenticate access to an agent's monitored data.
<b>COMA</b>	Cache Only Memory Architecture. In a COMA machine, additional hardware including tag and state memory is added to the DRAM of each processing node to convert it into a kind of cache called attraction memory. See also NUMA.
<b>CTQ</b>	Critical to Quality. A Sun Sigma terminology, used to identify key issues for a product based on voice of the customer.
<b>diagnosis</b>	Correct interpretation and reporting of error.
<b>diagnosability</b>	Ability of system to detect and correctly report errors when they occur.
<b>diagnostics</b>	A test to uncover faults. These tests are used in design and validation to find margins and set limits on error protection, and stress tests used in operations to screen finished product for escapes.
<b>diagnostic harness</b>	The harness or environment that schedules, manages, and controls execution of diagnostic tests on a platform.

<b>DRAM</b>	Dynamic Read Access Memory. A type of semiconductor random access memory that stores information in integrated circuits that contain capacitors. Because capacitors lose their charge over time, the dynamic RAM must be periodically "refreshed" or recharged. Contrast with SRAM. See also VRAM.
<b>EEPROM</b>	Electrically Erasable Programmable Read Only Memory. A special type of PROM that can be erased by exposing it to an electrical charge. Like other types of PROM, EEPROM retains its contents even when the power is turned off. Also like other types of ROM, EEPROM is not as fast as RAM. See also PROM, ROM, and RAM.
<b>FC-AL</b>	Fibre Channel-Arbitrated Loop A connector that provides high bandwidth, increased distance, and additional connectivity from host to peripherals.
<b>FIFO</b>	First-In First-Out Memory that stores data in queue order so the first input element goes out the first.
<b>FRU</b>	Field Replaceable Unit.
<b>GUI</b>	The graphical user interface, or GUI, is a window that provides the user with a method of interacting with the computer and its special applications, usually with a mouse or other selection device. The window usually includes such things as windows, an intuitive method of manipulating directories and files, and icons.
<b>HDLC</b>	High Level Data Link. A common layer two protocol that handles both data and control messages.
<b>hop</b>	The number of routers a packet goes through before reaching its destination.
<b>module</b>	A software component that may be loaded dynamically to monitor data resources of systems, applications and network devices.
<b>node</b>	A node is a workstation or server.
<b>OpenBoot Diagnostics</b>	Standalone operation, without native operating system. Interactive menu driven, which provides ability to run tests from OpenBoot PROM level.
<b>offline diagnostics</b>	Diagnostics that require the aid of the native operating system to function. Primarily tests components not currently in use by customer.
<b>online diagnostics</b>	Diagnostics that require the aid of the native operating system to function. Primarily tests components currently in use by customer.
<b>NUMA</b>	Non-Uniform Memory Access. CC/NUMA or CC-NUMA is Cache-Coherent Non-Uniform Memory Access. In a CC-NUMA machine, the physical address on the memory bus of a processing node is used to determine the home node memory location of a particular datum. The cache hierarchy on each processing node is constructed to replicate and hold copies of data from not only the local memory, but also the memory of remote nodes.
<b>PICL</b>	Platform Information and Control Library.

<b>POST</b>	Power on Self Test. Standalone operation, without native operating system. Mostly non interactive, automated tests, run when power is applied to the system.
<b>production environment</b>	One of two environments in which software is deployed. The production environment is a “real” environment (as opposed to a test environment) in which you manage and monitor your hardware.
<b>PROM</b>	Programmable Read Only Memory A memory chip on which data can be written only once. When a program has been written onto a PROM, it remains there forever. Unlike RAM, PROMs retain their data even when the power is turned off. See also RAM.
<b>RAM</b>	Random Access Memory A type of computer memory that can be accessed randomly; that is, any byte of memory can be accessed without touching the preceding bytes. RAM is the most common type of computer and device memory.
<b>ROM</b>	Read Only Memory Computer memory on which data has been prerecorded. Once data has been written onto a ROM chip, it cannot be removed and can only be read.
<b>RTS/CTS</b>	Ready to Send/Clear to Send A hardware flow control handshake protocol used with serial lines.
<b>SEEPROM</b>	Serial EEPROM. See also EEPROM.
<b>segment</b>	An object representing a “segment” of the network, and used as a basis for a local network.
<b>SNMP</b>	Simple Network Management Protocol. A simple protocol designed to allow networked entities (hosts, routers, and so on) to exchange monitoring information.
<b>SNMPv2 usec</b>	SNMP version 2, user-based security model security standards.
<b>SRAM</b>	Static Random Access Memory. A faster and more reliable RAM than dynamic random access memory (DRAM). DRAM offers access times of about 60 nanoseconds, while SRAM access can be as low as 10 nanoseconds. It is static because it does not require refreshment as does DRAM. See also VRAM.
<b>standalone diagnostics</b>	Diagnostics that run without the aid of the native operating system. Usually an operating environment is built to provide basic scheduling capabilities.
<b>standard error</b>	An open file normally connected directly to a primary output device, such as a terminal, printer, or screen. Error messages and other diagnostic output normally go to this file and then to the output device. You can redirect the standard error output into another file instead of to the printer or screen.

- standard input** (Standard input device) The device from which a program or system normally takes its input. Usually a terminal or the keyboard.
- standard output** (Standard output device) The device to which a program or system normally sends its output. Usually a terminal or the screen.
- URL** Uniform Resource Locator. A URL is a textual specification describing a resource which is network-accessible.
- VRAM** Video Random Access Memory. A type of dynamic RAM (DRAM) used in high-speed graphics frame buffers. With conventional DRAM, both the processor and the frame buffer logic must access RAM by sharing the same signal lines or buses on the RAM chips. VRAM provides separate buses for the processor and the frame buffer logic. See also DRAM, SRAM.



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