

Oracle® Database Appliance Licensing Information User Manual



Release 19.23

F95874-01

May 2024

The Oracle logo, consisting of a solid red square with the word "ORACLE" in white, uppercase, sans-serif font centered within it.

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Preface

This document, which is part of the Program Documentation under the terms of your Oracle licensing agreement, is intended to help you understand the features, functionality, and options available for Oracle Programs.

If you have a question about your licensing needs, then contact your Oracle sales representative or the License Management Services representative at <http://www.oracle.com/us/corporate/license-management-services/index.html>. You can also refer to the resources listed in “Related Documents” for more information.

- [Audience](#)
- [Documentation Accessibility](#)
- [Related Documents](#)
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Audience

This book is intended for all purchasers of Oracle Database Appliance.

Documentation Accessibility

For information about Oracle's commitment to accessibility, visit the Oracle Accessibility Program website at <http://www.oracle.com/pls/topic/lookup?ctx=acc&id=docacc>.

Access to Oracle Support

Oracle customers that have purchased support have access to electronic support through My Oracle Support. For information, visit <http://www.oracle.com/pls/topic/lookup?ctx=acc&id=info> or visit <http://www.oracle.com/pls/topic/lookup?ctx=acc&id=trs> if you are hearing impaired.

Related Documents

For more information about Oracle Database Appliance, go to <http://www.oracle.com/goto/oda/docs> and click the appropriate release.

For more information about using Oracle Database, go to <http://docs.oracle.com/database/> and select the database release from the menu.

For more information about Oracle Integrated Lights Out Manager 3.2, see https://docs.oracle.com/cd/E37444_01/.

For more details about other Oracle products that are mentioned in Oracle Database Appliance documentation, see the Oracle Documentation home page at <http://docs.oracle.com>.

Conventions

The following text conventions are used in this document:

Convention	Meaning
boldface	Boldface type indicates graphical user interface elements associated with an action, or terms defined in text or the glossary.
<i>italic</i>	Italic type indicates book titles, emphasis, or placeholder variables for which you supply particular values.
monospace	Monospace type indicates commands within a paragraph, URLs, code in examples, text that appears on the screen, or text that you enter.

1

Introduction

This Licensing Information document is a part of the product or program documentation under the terms of your Oracle license agreement and is intended to help you understand the program editions, entitlements, restrictions, prerequisites, special license rights, and/or separately licensed third party technology terms associated with the Oracle software program(s) covered by this document (the "Program(s)").

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2

Oracle Database Appliance Licensing Overview

This chapter provides an overview of the licensing process for each hardware platform.

Topics:

- [About Licensing](#)
Oracle Database Appliance provides capacity-on-demand licensing.
- [CPUs and Core Count](#)
Review this section for information about core license options for your Oracle Database Appliance hardware.
- [Oracle Database Appliance KVM Hard Partitioning Compliance](#)
Review this section for information about Oracle Database Appliance KVM Hard Partitioning compliance.

About Licensing

Oracle Database Appliance provides capacity-on-demand licensing.

Capacity-on-demand (CoD) refers to an Oracle Database Appliance server that has a subset of its cores turned off so that the Oracle Database Enterprise Edition or Oracle Database Standard Edition 2 software license cost can be reduced. The number of cores can be reduced before or after the deployment. The number of active cores can be increased at a later time, when more capacity is needed. Oracle Database is licensed separately. Refer to *Database Licensing Information User Manual* for additional information.

When you add your hardware Support Identifier (SI) for Oracle Database Appliance to your My Oracle Support account, you establish a license for all the cores on your system. Oracle Database Appliance models can be deployed as bare metal systems or KVM-based virtualized systems. Both platform types offer capacity-on-demand licensing.

With bare-metal installations, you disable cores that you do not intend Oracle Database to use:

- For Oracle Database Appliance X10, X9-2S, X9-2L, X9-2-HA, X8-2S, X8-2M, X8-2-HA, X7-2S, X7-2M, X7-2-HA: Issue the command `odacli modify-cpucore` to set the low water mark and to increase the cores.

Chapter three explains how to obtain a licensing key and record your initial license requirements with My Oracle Support. It also explains how to change the licensed core count, if necessary, later. Chapter three is appropriate for bare metal installations where your license covers the cores that you are using for Oracle Database.

Chapter four contains information about the third-party products that are included with Oracle Database Appliance.

 **Note:**

When you use Integrated Oracle Data Guard with two Oracle Database Appliance systems, you do not require a separate Oracle Active Data Guard license.

About License Options for Oracle Database Standard Edition 2 on Oracle Database Appliance

For the purposes of licensing Oracle Database Standard Edition 2 on Oracle Database Appliance running multi-chip modules, where each chip in a multi-chip module is counted as an occupied socket for licensing purposes, you may exceed the 2 sockets per server limit. Oracle Database Standard Edition 2 requires one processor license for every 8 enabled cores on Oracle Database Appliance running multi-chip modules. If the number of enabled cores is not divisible by 8, the quotient must be rounded up to the nearest whole number to determine the number of Oracle Database Standard Edition 2 processor licenses required.

 **Note:**

The minimum licenses when licensing by SE2 Named User Plus (NUP) metric are 10 NUP licenses per server.

CPUs and Core Count

Review this section for information about core license options for your Oracle Database Appliance hardware.

- Oracle Database Appliance X10-HA has 2 servers, each with 2 CPUs of 32 cores each. When you deploy Oracle Database Appliance X10-HA, all 128 cores (64 cores in each server) are active with hyper-threading enabled by default.
- Oracle Database Appliance X10-L contains 2 CPUs with 32 cores for a total of 64 cores. When you deploy Oracle Database Appliance X10-L, all 64 cores are active with hyper-threading enabled by default.
- Oracle Database Appliance X10-S contains 1 CPU with 32 cores. When you deploy Oracle Database Appliance X10-S, all 32 cores are active with hyper-threading enabled by default.
- Oracle Database Appliance X9-2-HA has 2 servers, each with 2 CPUs of 16 cores each. When you deploy Oracle Database Appliance X9-2-HA, all 64 cores (32 cores in each server) are active with hyper-threading enabled by default.
- Oracle Database Appliance X9-2L contains 2 CPUs with 16 cores for a total of 32 cores. When you deploy Oracle Database Appliance X9-2L, all 32 cores are active with hyper-threading enabled by default.
- Oracle Database Appliance X9-2S contains 1 CPU with 16 cores. When you deploy Oracle Database Appliance X9-2S, all 16 cores are active with hyper-threading enabled by default.
- Oracle Database Appliance X8-2-HA has 2 servers, each with 2 CPUs of 16 cores each. When you deploy Oracle Database Appliance X8-2-HA, all 64 cores (32 cores in each server) are active with hyper-threading enabled by default.

- Oracle Database Appliance X8-2M contains 2 CPUs with 16 cores for a total of 32 cores. When you deploy Oracle Database Appliance X8-2M, all 32 cores are active with hyper-threading enabled by default.
- Oracle Database Appliance X8-2S contains 1 CPU with 16 cores. When you deploy Oracle Database Appliance X8-2S, all 16 cores are active with hyper-threading enabled by default.
- Oracle Database Appliance X7-2-HA has 2 servers, each with 2 CPUs of 18 cores each. When you deploy Oracle Database Appliance X7-2-HA, all 72 cores (36 cores in each server) are active with hyper-threading enabled by default.
- Oracle Database Appliance X7-2M contains 2 CPUs with 18 cores for a total of 36 cores. When you deploy Oracle Database Appliance X7-2M, all 36 cores are active with hyper-threading enabled by default.
- Oracle Database Appliance X7-2S contains 1 CPU with 10 cores. When you deploy Oracle Database Appliance X7-2S, all 10 cores are active with hyper-threading enabled by default.

Oracle Database Appliance bare metal licensing is determined by the number of enabled cores that you have on your system. The capacity-on-demand licensing feature enables you to change the enabled core count as your system resource needs change.

Initialize the number of licensed cores to the level currently required by your applications. Each server node has the same number of active cores. Later, increase the licensed core count as your applications require more capacity. Change the number of licensed cores by obtaining and applying a core key to configure your appliance, using the tables for your Oracle Database Appliance platform.

Oracle Database Appliance X10-HA Licensed Core Licensing Options

The table provides an example of how the licensed cores for each node of an Oracle Database Appliance X10-HA relate to active cores for Oracle RAC and Oracle Enterprise Edition and active cores for Oracle RAC One Node.

Table 2-1 Example of Bare Metal License Options for Oracle Database Appliance X10-HA Systems

Licensed Cores for Each Node (X10-HA Only)	Active Cores for Oracle RAC and Oracle Enterprise Edition (X10-HA Only)	Active Cores for Oracle RAC One Node (X10-HA Only)
2	4	2
4	8	4
6	12	6
8	16	8
10	20	10
12	24	12
14	28	14
16	32	16
18	36	18
20	40	20
22	44	22
24	48	24

Table 2-1 (Cont.) Example of Bare Metal License Options for Oracle Database Appliance X10-HA Systems

Licensed Cores for Each Node (X10-HA Only)	Active Cores for Oracle RAC and Oracle Enterprise Edition (X10-HA Only)	Active Cores for Oracle RAC One Node (X10-HA Only)
26	52	26
28	56	28
30	60	30
32	64	32

Oracle Database Appliance X10-L and X10-S Licensed Core Licensing Options**Table 2-2 Bare Metal License Options for all Oracle Database Appliance X10-L and X10-S Systems**

Licensed Cores for Single Node (X10-S)	Licensed Cores for Single Node (X10-L)
2	2
4	4
6	6
8	8
10	10
12	12
14	14
16	16
Not applicable	18
Not applicable	20
Not applicable	22
Not applicable	24
Not applicable	26
Not applicable	28
Not applicable	30
Not applicable	32

Oracle Database Appliance X9-2-HA Licensed Core Licensing Options

The table provides an example of how the licensed cores for each node of an Oracle Database Appliance X9-2-HA relate to active cores for Oracle RAC and Oracle Enterprise Edition and active cores for Oracle RAC One Node.

Table 2-3 Example of Bare Metal License Options for Oracle Database Appliance X9-2-HA Systems

Licensed Cores for Each Node (X9-2-HA Only)	Active Cores for Oracle RAC and Oracle Enterprise Edition (X9-2-HA Only)	Active Cores for Oracle RAC One Node (X9-2-HA Only)
2	4	2
4	8	4
6	12	6
8	16	8
10	20	10
12	24	12
14	28	14
16	32	16
18	36	18
20	40	20
22	44	22
24	48	24
26	52	26
28	56	28
30	60	30
32	64	32

Oracle Database Appliance X9-2L and X9-2S Licensed Core Licensing Options**Table 2-4 Bare Metal License Options for all Oracle Database Appliance X9-2L and X9-2S Systems**

Licensed Cores for Single Node (X9-2S)	Licensed Cores for Single Node (X9-2L)
2	2
4	4
6	6
8	8
10	10
12	12
14	14
16	16
Not applicable	18
Not applicable	20
Not applicable	22
Not applicable	24
Not applicable	26
Not applicable	28
Not applicable	30

Table 2-4 (Cont.) Bare Metal License Options for all Oracle Database Appliance X9-2L and X9-2S Systems

Licensed Cores for Single Node (X9-2S)	Licensed Cores for Single Node (X9-2L)
Not applicable	32

Oracle Database Appliance X8-2-HA Licensed Core Licensing Options

The table provides an example of how the licensed cores for each node of an Oracle Database Appliance X8-2-HA relate to active cores for Oracle RAC and Oracle Enterprise Edition and active cores for Oracle RAC One Node.

Table 2-5 Example of Bare Metal License Options for Oracle Database Appliance X8-2-HA Systems

Licensed Cores for Each Node (X8-2-HA Only)	Active Cores for Oracle RAC and Oracle Enterprise Edition (X8-2-HA Only)	Active Cores for Oracle RAC One Node (X8-2-HA Only)
2	4	2
4	8	4
6	12	6
8	16	8
10	20	10
12	24	12
14	28	14
16	32	16
18	36	18
20	40	20
22	44	22
24	48	24
26	52	26
28	56	28
30	60	30
32	64	32

Oracle Database Appliance X8-2M and X8-2S Licensed Core Licensing Options**Table 2-6 Bare Metal License Options for all Oracle Database Appliance X8-2M and X8-2S Systems**

Licensed Cores for Single Node (X8-2S)	Licensed Cores for Single Node (X8-2M)
2	2
4	4
6	6
8	8
10	10

Table 2-6 (Cont.) Bare Metal License Options for all Oracle Database Appliance X8-2M and X8-2S Systems

Licensed Cores for Single Node (X8-2S)	Licensed Cores for Single Node (X8-2M)
12	12
14	14
16	16
Not applicable	18
Not applicable	20
Not applicable	22
Not applicable	24
Not applicable	26
Not applicable	28
Not applicable	30
Not applicable	32

Oracle Database Appliance X7-2-HA Licensed Core Licensing Options

The table provides an example of how the licensed cores for each node of an Oracle Database Appliance X7-2-HA relate to active cores for Oracle RAC and Oracle Enterprise Edition and active cores for Oracle RAC One Node.

Table 2-7 Example of Bare Metal License Options for Oracle Database Appliance X7-2-HA Systems

Licensed Cores for Each Node (X7-2-HA Only)	Active Cores for Oracle RAC and Oracle Enterprise Edition (X7-2-HA Only)	Active Cores for Oracle RAC One Node (X7-2-HA Only)
2	4	2
4	8	4
6	12	6
8	16	8
10	20	10
12	24	12
14	28	14
16	32	16
18	36	18
20	40	20
22	44	22
24	48	24
26	52	26
28	56	28
30	60	30
32	64	32
34	68	34

Table 2-7 (Cont.) Example of Bare Metal License Options for Oracle Database Appliance X7-2-HA Systems

Licensed Cores for Each Node (X7-2-HA Only)	Active Cores for Oracle RAC and Oracle Enterprise Edition (X7-2-HA Only)	Active Cores for Oracle RAC One Node (X7-2-HA Only)
36	72	36

Oracle Database Appliance X7-2 Licensed Core Licensing Options

Table 2-8 Bare Metal License Options for all Oracle Database Appliance X7-2 Systems

Licensed Cores for Single Node (X7-2S)	Licensed Cores for Single Node (X7-2M)
2	2
4	4
6	6
8	8
10	10
Not applicable	12
Not applicable	14
Not applicable	16
Not applicable	18
Not applicable	20
Not applicable	22
Not applicable	24
Not applicable	26
Not applicable	28
Not applicable	30
Not applicable	32
Not applicable	34
Not applicable	36

Oracle Database Appliance KVM Hard Partitioning Compliance

Review this section for information about Oracle Database Appliance KVM Hard Partitioning compliance.

Oracle Database Appliance DB systems and application KVMs conform to Oracle Linux KVM Hard Partitioning requirements as specified in Hard Partitioning with Oracle Linux KVM:

<https://www.oracle.com/a/ocom/docs/linux/ol-kvm-hard-partitioning.pdf>

Considerations for CPU Pool Subscription

CPU Pools are used for management of CPU resources only. Database license requirements are determined by KVM Hard Partitioning only.

In a DB system with a shared or internal CPU pool, the cores are pinned based on the database shape selected for the DB system. For example, if you create a DB system with `odb2`, then you must license 2 cores.

In a DB system with a shared CPU pool, consider both undersubscription and oversubscription scenarios.

- **Undersubscription:** Consider a shared CPU pool with 8 cores. If you create a DB system with `odb2` and another with `odb4`, then a total of 6 cores are used. So, although the shared CPU pool is undersubscribed, you must run the `virsh` command in each KVM or DB system to determine the pinned cores that need to be licensed. In this case, it is the same 8 cores visible to both DB systems.
- **Oversubscription:** Consider a shared CPU pool with 8 cores, and you create a DB system with `odb4` and another with `odb6`. Although the shared CPU pool is oversubscribed, you must run the `virsh` command in each KVM or DB System to determine the pinned cores that need to be licensed. In this case, it is the same 8 cores visible to both DB systems.

Note that the same CPU pool subscription policy applies to shared CPU pools for application KVMs too.

Validating the CPUs for an Oracle Database Appliance DB System and Application KVM

You must run the `virsh` command for every DB system to identify the pinned vCPUs and CPU cores licensing requirements.

To validate that the CPUs for a virtual machine are pinned to a physical thread or core, run the following command:

```
# virsh --readonly vcpuinfo VM_name --pretty
```

The following example illustrates how you can validate the CPUs for an Oracle Database Appliance DB System with the db shape `odb2` that uses 2 cores (4 vCPUs):

1. Determine the VM Name by running this command:

```
# odacli describe-dbsystem -n dbs1 | grep "VM Name"
VM Name: x97f18b518
```

In this example, the DB System name is `dbs1`.

2. Run the `virsh` command to confirm that vCPUs are pinned to physical threads or cores for `x97f18b518`:

```
# virsh --readonly vcpuinfo x97f18b518 --pretty

VCPU:          0
CPU:           19
State:         running
CPU time:      79.3s
CPU Affinity:  19,24,55,60 (out of 72)

VCPU:          1
CPU:           55
State:         running
CPU time:      71.5s
```

```
CPU Affinity: 19,24,55,60 (out of 72)

VCPU:        2
CPU:         60
State:       running
CPU time:    71.7s
CPU Affinity: 19,24,55,60 (out of 72)

VCPU:        3
CPU:         24
State:       running
CPU time:    73.1s
CPU Affinity: 19,24,55,60 (out of 72)
-----
```

In this example, the pinned vCPUs are 19, 24, 55, and 60, out of 72.

3

Capacity-On-Demand Licensing Information

This chapter provides capacity-on-demand licensing information for Oracle Database Appliance bare metal installations.

- [Oracle Database Appliance Capacity-on-Demand Licensing](#)
Capacity-on-Demand software licensing enables you to deploy as few or as many processors in Oracle Database Appliance as your workload requires.
- [Capacity-On-Demand Licensing for Oracle Database Appliance X10-S, X10-L, and X10-HA](#)
Review capacity-on-demand licensing for Oracle Database Appliance X10-S, X10-L, and X10-HA.
- [Capacity-On-Demand Licensing for Oracle Database Appliance X9-2S, X9-2L, and X9-2-HA](#)
Review capacity-on-demand licensing for Oracle Database Appliance X9-2S, X9-2L, and X9-2-HA.
- [Capacity-On-Demand Licensing for Oracle Database Appliance X8-2S, X8-2M, and X8-2-HA](#)
Review capacity-on-demand licensing for Oracle Database Appliance X8-2S, X8-2M, and X8-2-HA.
- [Capacity-On-Demand Licensing for Oracle Database Appliance X7-2S, X7-2M, and X7-2-HA](#)
Review capacity-on-demand licensing for Oracle Database Appliance X7-2S, X7-2M, and X7-2-HA.

Oracle Database Appliance Capacity-on-Demand Licensing

Capacity-on-Demand software licensing enables you to deploy as few or as many processors in Oracle Database Appliance as your workload requires.

Oracle Database Appliance's Capacity-on-Demand feature enables you to add more processor cores to scale up to increased system demands as you need to, without incurring the excessive costs and downtime usually associated with hardware upgrades. Additional cores are increased in 2-core increments.

Oracle Database Appliance Scaling Capabilities

The following table lists the scaling capacities for Oracle Database Appliance bare metal and virtualized platform deployments. The decision to use bare-metal or Oracle Database Appliance Virtualized Platform depends on your company policies and the benefits that each implementation offers your company.

Table 3-1 Oracle Database Appliance Scaling Capacities

Oracle Database Appliance Platform	Minimum Number Processor Cores	Maximum Number Processor Cores
X10-S	2	32

Table 3-1 (Cont.) Oracle Database Appliance Scaling Capacities

Oracle Database Appliance Platform	Minimum Number Processor Cores	Maximum Number Processor Cores
X10-L	2	64
X10-HA	2	128
X9-2S	2	16
X9-2L	2	32
X9-2-HA	2	64
X8-2S	2	16
X8-2M	2	32
X8-2-HA	2	64
X7-2S	2	10
X7-2M	2	36
X7-2-HA	2	72
X6-2S	2	10
X6-2M	2	20
X6-2L	2	20
X6-2-HA	2	40
X5-2	2	72
X4-2	2	48

Applications Deployed on Oracle Database Appliance and Capacity-on-Demand

All Oracle products that are deployed on Oracle Database Appliance can take advantage of Capacity-on-Demand licensing.



Note:

For the procedure to set the CPU Core Count, see the *Oracle Database Appliance Deployment and User Guide* for your hardware model.

Capacity-On-Demand Licensing for Oracle Database Appliance X10-S, X10-L, and X10-HA

Review capacity-on-demand licensing for Oracle Database Appliance X10-S, X10-L, and X10-HA.

For the procedure to set the CPU Core Count, see the *Oracle Database Appliance Deployment and User Guide* for your hardware model.

The following list describes the number of cores for each Oracle Database Appliance X10-S model:

- Oracle Database Appliance X10-S is a single server consisting of 32 cores.

- Oracle Database Appliance X10-L is a single server consisting of 64 cores.
- Oracle Database Appliance X10-HA consists of two servers. Each server has 64 cores, for a total of 128 cores.

By default, Oracle Database Appliance ships with all cores enabled. You can reduce the number of active cores before or after deployment. You can increase the number of active cores when additional capacity is needed. This is known as **capacity-on-demand**. Additional cores are increased in 2-core increments.

Rules and Restrictions for Setting CPU Core Count on Oracle Database Appliance X10-S, X10-L, and X10-HA Bare Metal Deployments

Review the following rules and restrictions before changing the CPU core count:

- You can change the CPU core count to a value that is a multiple of two between 2 and the maximum number of cores for the hardware model. For example, for X10-HA, between 2 and 64 cores for each server for a maximum of 128 cores.
- If you change the CPU core count, then you can subsequently only increase the CPU core count.
- You should only change the CPU core count if you need less than the maximum number of cores.

For example, if you change the CPU core count to 8, then you can subsequently increase the CPU core count to a higher number (in increments of 2 cores), but you cannot decrease the CPU core count.

- If you want the maximum number of cores for each server (32 cores per server for X10-L and X10-HA), then use the default configuration. There is no need to set the CPU core count.

Note:

If you mistakenly followed the procedure to set your CPU core count to the maximum, thereby preventing you from subsequently decreasing your CPU core count, and the error is caught immediately, then contact Oracle Support.

Capacity-On-Demand Licensing for Oracle Database Appliance X9-2S, X9-2L, and X9-2-HA

Review capacity-on-demand licensing for Oracle Database Appliance X9-2S, X9-2L, and X9-2-HA.

For the procedure to set the CPU Core Count, see the *Oracle Database Appliance Deployment and User Guide* for your hardware model.

The following list describes the number of cores for each Oracle Database Appliance X9-2 model:

- Oracle Database Appliance X9-2S is a single server consisting of 16 cores.
- Oracle Database Appliance X9-2L is a single server consisting of 32 cores.
- Oracle Database Appliance X9-2-HA consists of two servers. Each server has 32 cores, for a total of 64 cores.

By default, Oracle Database Appliance ships with all cores enabled. You can reduce the number of active cores before or after deployment. You can increase the number of active cores when additional capacity is needed. This is known as **capacity-on-demand**. Additional cores are increased in 2-core increments.

Rules and Restrictions for Setting CPU Core Count on Oracle Database Appliance X9-2S, X9-2L, and X9-2-HA Bare Metal Deployments

Review the following rules and restrictions before changing the CPU core count:

- You can change the CPU core count to a value that is a multiple of two between 2 and the maximum number of cores for the hardware model. For example, for X9-2-HA, between 2 and 32 cores for each server for a maximum of 64 cores.
- If you change the CPU core count, then you can subsequently only increase the CPU core count.
- You should only change the CPU core count if you need less than the maximum number of cores.

For example, if you change the CPU core count to 8, then you can subsequently increase the CPU core count to a higher number (in increments of 2 cores), but you cannot decrease the CPU core count.

- If you want the maximum number of cores for each server (32 cores per server for X9-2L and X9-2-HA), then use the default configuration. There is no need to set the CPU core count.

Note:

If you mistakenly followed the procedure to set your CPU core count to the maximum, thereby preventing you from subsequently decreasing your CPU core count, and the error is caught immediately, then contact Oracle Support.

Capacity-On-Demand Licensing for Oracle Database Appliance X8-2S, X8-2M, and X8-2-HA

Review capacity-on-demand licensing for Oracle Database Appliance X8-2S, X8-2M, and X8-2-HA.

For the procedure to set the CPU Core Count, see the *Oracle Database Appliance Deployment and User Guide* for your hardware model.

The following list describes the number of cores for each Oracle Database Appliance X8-2 model:

- Oracle Database Appliance X8-2S is a single server consisting of 16 cores.
- Oracle Database Appliance X8-2M is a single server consisting of 32 cores.
- Oracle Database Appliance X8-2-HA consists of two servers. Each server has 32 cores, for a total of 64 cores.

By default, Oracle Database Appliance ships with all cores enabled. You can reduce the number of active cores before or after deployment. You can increase the number of active cores when additional capacity is needed. This is known as **capacity-on-demand**. Additional cores are increased in 2-core increments.

Rules and Restrictions for Setting CPU Core Count on Oracle Database Appliance X8-2S, X8-2M, and X8-2-HA Bare Metal Deployments

Review the following rules and restrictions before changing the CPU core count:

- You can change the CPU core count to a value that is a multiple of two between 2 and the maximum number of cores for the hardware model. For example, for X8-2-HA, between 2 and 32 cores for each server for a maximum of 64 cores.
- If you change the CPU core count, then you can subsequently only increase the CPU core count.
- You should only change the CPU core count if you need less than the maximum number of cores.

For example, if you change the CPU core count to 8, then you can subsequently increase the CPU core count to a higher number (in increments of 2 cores), but you cannot decrease the CPU core count.

- If you want the maximum number of cores for each server (32 cores per server for X8-2M and X8-2-HA), then use the default configuration. There is no need to set the CPU core count.



Note:

If you mistakenly followed the procedure to set your CPU core count to the maximum, thereby preventing you from subsequently decreasing your CPU core count, and the error is caught immediately, then contact Oracle Support.

Capacity-On-Demand Licensing for Oracle Database Appliance X7-2S, X7-2M, and X7-2-HA

Review capacity-on-demand licensing for Oracle Database Appliance X7-2S, X7-2M, and X7-2-HA.

For the procedure to set the CPU Core Count, see the *Oracle Database Appliance Deployment and User Guide* for your hardware model.

The following list describes the number of cores for each Oracle Database Appliance X7-2 model:

- Oracle Database Appliance X7-2S is a single server consisting of 10 cores.
- Oracle Database Appliance X7-2M is a single server consisting of 36 cores.
- Oracle Database Appliance X7-2-HA consists of two servers. Each server has 36 cores, for a total of 72 cores.

By default, Oracle Database Appliance ships with all cores enabled. You can reduce the number of active cores before or after deployment. You can increase the number of active cores when additional capacity is needed. This is known as **capacity-on-demand**. Additional cores are increased in 2-core increments.

Rules and Restrictions for Setting CPU Core Count on Oracle Database Appliance X7-2S, X7-2M, and X7-2-HA Bare Metal Deployments

Review the following rules and restrictions before changing the CPU core count:

- You can change the CPU core count to a value that is a multiple of two between 2 and the maximum number of cores for the hardware model. For example, for X7-2-HA, between 2 and 36 cores for each server for a maximum of 72 cores.
- If you change the CPU core count, then you can subsequently only increase the CPU core count.
- You should only change the CPU core count if you need less than the maximum number of cores.

For example, if you change the CPU core count to 8, then you can subsequently increase the CPU core count to a higher number (in increments of 2 cores), but you cannot decrease the CPU core count.

- If you want the maximum number of cores for each server (36 cores per server for X7-2M and X7-2-HA), then use the default configuration. There is no need to set the CPU core count.

 **Note:**

If you mistakenly followed the procedure to set your CPU core count to the maximum, thereby preventing you from subsequently decreasing your CPU core count, and the error is caught immediately, then contact Oracle Support.

4

Third-Party Product Licenses for This Release

This chapter contains the licenses for the third-party products that are included with Oracle Database Appliance for this release.

- [Oracle Database Third-Party Licensing Information](#)
Understand licensing information about third-party products used by Oracle Database.
- [Other Third-Party Product Licenses](#)
Review other third-party product licenses for Oracle Database Appliance.
- [Written Offer for Source Code](#)

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Related Topics

- [Licensing Information User Manual Oracle Database 21c](#)
- [Licensing Information User Manual Oracle Database 19c](#)

Other Third-Party Product Licenses

Review other third-party product licenses for Oracle Database Appliance.

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		unuran-1.8.1/configure
		unuran-1.8.1/doc/Makefile.am
		unuran-1.8.1/doc/Makefile.in
		unuran-1.8.1/doc/figures/Makefile.am
		unuran-1.8.1/doc/figures/Makefile.in
		unuran-1.8.1/doc/figures/
		discrete_inversion.eps
		unuran-1.8.1/doc/figures/
		discrete_inversion.pdf
		unuran-1.8.1/doc/figures/
		discrete_inversion.png
		unuran-1.8.1/doc/figures/
		discrete_inversion.txt
		unuran-1.8.1/doc/figures/inversion.eps
		unuran-1.8.1/doc/figures/inversion.pdf
		unuran-1.8.1/doc/figures/inversion.png
		unuran-1.8.1/doc/figures/inversion.txt
		unuran-1.8.1/doc/figures/
		rejection_from_constant_hat.eps
		unuran-1.8.1/doc/figures/
		rejection_from_constant_hat.pdf
		unuran-1.8.1/doc/figures/
		rejection_from_constant_hat.png
		unuran-1.8.1/doc/figures/
		rejection_from_constant_hat.txt
		unuran-1.8.1/doc/figures/rou-cauchy.eps
		unuran-1.8.1/doc/figures/rou-cauchy.pdf
		unuran-1.8.1/doc/figures/rou-cauchy.png
		unuran-1.8.1/doc/figures/rou-cauchy.txt
		unuran-1.8.1/doc/figures/texpict2img.pl
		unuran-1.8.1/doc/src/Makefile.am
		unuran-1.8.1/doc/src/Makefile.in
		unuran-1.8.1/doc/src/README
		unuran-1.8.1/doc/src/arvag.dh
		unuran-1.8.1/doc/src/error_debug.dh
		unuran-1.8.1/doc/src/examples.dh
		unuran-1.8.1/doc/src/glossary.dh
		unuran-1.8.1/doc/src/index.dh
		unuran-1.8.1/doc/src/installation.dh
		unuran-1.8.1/doc/src/intro.dh
		unuran-1.8.1/doc/src/make_texti.pl
		unuran-1.8.1/doc/src/methods.dh
		unuran-1.8.1/doc/src/methods_cemp.texti
		unuran-1.8.1/doc/src/methods_cont.texti
		unuran-1.8.1/doc/src/methods_cvec.texti
		unuran-1.8.1/doc/src/methods_cvemp.texti
		unuran-1.8.1/doc/src/methods_discr.texti
		unuran-1.8.1/doc/src/methods_matr.texti
		unuran-1.8.1/doc/src/methods_mcmc.texti
		unuran-1.8.1/doc/src/misc.dh
		unuran-1.8.1/doc/src/ref_example0.texti

Table 4-1 (Cont.) Open Source or Other Separately Licensed Software

Provider	Component	Licensing Information
		unuran-1.8.1/doc/src/ref_example0_str.texi
		unuran-1.8.1/doc/src/ref_example1.texi
		unuran-1.8.1/doc/src/ref_example1_str.texi
		unuran-1.8.1/doc/src/ref_example2.texi
		unuran-1.8.1/doc/src/ref_example2_str.texi
		unuran-1.8.1/doc/src/ref_example3.texi
		unuran-1.8.1/doc/src/ref_example3_str.texi
		unuran-1.8.1/doc/src/ref_example_FuncStr.texi
		unuran-1.8.1/doc/src/ref_example_anti.texi
		unuran-1.8.1/doc/src/ ref_example_anti_str.texi
		unuran-1.8.1/doc/src/ref_example_cext.texi
		unuran-1.8.1/doc/src/ref_example_cont.texi
		unuran-1.8.1/doc/src/ ref_example_cont_str.texi
		unuran-1.8.1/doc/src/ref_example_dext.texi
		unuran-1.8.1/doc/src/ref_example_discr.texi
		unuran-1.8.1/doc/src/ ref_example_discr_str.texi
		unuran-1.8.1/doc/src/ref_example_emp.texi
		unuran-1.8.1/doc/src/ref_example_emp_str.texi
		unuran-1.8.1/doc/src/ref_example_gsl.texi
		unuran-1.8.1/doc/src/ref_example_mixt.texi
		unuran-1.8.1/doc/src/ ref_example_mixt_inv.texi
		unuran-1.8.1/doc/src/ ref_example_rngstreams.texi
		unuran-1.8.1/doc/src/ref_example_vemp.texi
		unuran-1.8.1/doc/src/ref_example_win32_1.texi
		unuran-1.8.1/doc/src/ref_example_win32_2.texi
		unuran-1.8.1/doc/src/references.dh
		unuran-1.8.1/doc/src/stddist.dh
		unuran-1.8.1/doc/src/texinfo.tex
		unuran-1.8.1/doc/src/top.dh
		unuran-1.8.1/doc/src/unuran_src.texi
		unuran-1.8.1/doc/stamp-1
		unuran-1.8.1/doc/stamp-vti
		unuran-1.8.1/doc/unuran.html
		unuran-1.8.1/doc/unuran.info
		unuran-1.8.1/doc/unuran.pdf
		unuran-1.8.1/doc/unuran.texi
		unuran-1.8.1/doc/unuran.txt
		unuran-1.8.1/doc/unuran_win32.html
		unuran-1.8.1/doc/unuran_win32.info
		unuran-1.8.1/doc/unuran_win32.pdf
		unuran-1.8.1/doc/unuran_win32.texi
		unuran-1.8.1/doc/unuran_win32_libname.texi
		unuran-1.8.1/doc/version.texi
		unuran-1.8.1/doc/version_win32.texi
		unuran-1.8.1/examples/Makefile.am
		unuran-1.8.1/examples/Makefile.in
		unuran-1.8.1/examples/example0.c
		unuran-1.8.1/examples/example0_str.c
		unuran-1.8.1/examples/example1.c
		unuran-1.8.1/examples/example1_str.c

Table 4-1 (Cont.) Open Source or Other Separately Licensed Software

Provider	Component	Licensing Information
		unuran-1.8.1/examples/example2.c
		unuran-1.8.1/examples/example2_str.c
		unuran-1.8.1/examples/example3.c
		unuran-1.8.1/examples/example3_str.c
		unuran-1.8.1/examples/example_FuncStr.c
		unuran-1.8.1/examples/example_anti.c
		unuran-1.8.1/examples/example_anti_str.c
		unuran-1.8.1/examples/example_cext.c
		unuran-1.8.1/examples/example_cont.c
		unuran-1.8.1/examples/example_cont_str.c
		unuran-1.8.1/examples/example_cpp.cpp
		unuran-1.8.1/examples/example_dext.c
		unuran-1.8.1/examples/example_discr.c
		unuran-1.8.1/examples/example_discr_str.c
		unuran-1.8.1/examples/example_emp.c
		unuran-1.8.1/examples/example_emp_str.c
		unuran-1.8.1/examples/example_errorhandler.c
		unuran-1.8.1/examples/example_gsl.c
		unuran-1.8.1/examples/example_mcorr.c
		unuran-1.8.1/examples/example_mixt.c
		unuran-1.8.1/examples/example_mixt_inv.c
		unuran-1.8.1/examples/example_prng.c
		unuran-1.8.1/examples/example_prng_str.c
		unuran-1.8.1/examples/example_rngstreams.c
		unuran-1.8.1/examples/example_vemp.c
		unuran-1.8.1/experiments/Makefile.am
		unuran-1.8.1/experiments/Makefile.in
		unuran-1.8.1/experiments/make_timing_table.c
		unuran-1.8.1/experiments/timings_chap_10.conf
		unuran-1.8.1/experiments/timings_chap_8.conf
		unuran-1.8.1/scripts/Makefile.am
		unuran-1.8.1/scripts/Makefile.in
		unuran-1.8.1/scripts/
		compute_machine_constants.c
		unuran-1.8.1/scripts/make_urng_header.pl
		unuran-1.8.1/scripts/merge_h.pl
		unuran-1.8.1/scripts/read_PDF.pl
		unuran-1.8.1/scripts/remove_comments.pl
		unuran-1.8.1/scripts/run_valgrind.sh
		unuran-1.8.1/scripts/win32/Makefile.win32
		unuran-1.8.1/scripts/win32/build.sh
		unuran-1.8.1/scripts/win32/example1.c
		unuran-1.8.1/scripts/win32/example2.c
		unuran-1.8.1/scripts/win32/make_dll_def.pl
		unuran-1.8.1/src/Makefile.am
		unuran-1.8.1/src/Makefile.in
		unuran-1.8.1/src/README
		unuran-1.8.1/src/distr/Makefile.am
		unuran-1.8.1/src/distr/Makefile.in
		unuran-1.8.1/src/distributions/Makefile.am
		unuran-1.8.1/src/distributions/Makefile.in
		unuran-1.8.1/src/distributions/README
		unuran-1.8.1/src/methods/Makefile.am
		unuran-1.8.1/src/methods/Makefile.in
		unuran-1.8.1/src/parser/Makefile.am

Table 4-1 (Cont.) Open Source or Other Separately Licensed Software

Provider	Component	Licensing Information
		unuran-1.8.1/src/parser/Makefile.in
		unuran-1.8.1/src/parser/functparser_doc.dh
		unuran-1.8.1/src/parser/make_stringparser.pl
		unuran-1.8.1/src/parser/stringparser_doc.dh
		unuran-1.8.1/src/specfunct/Makefile.am
		unuran-1.8.1/src/specfunct/Makefile.in
		unuran-1.8.1/src/tests/Makefile.am
		unuran-1.8.1/src/tests/Makefile.in
		unuran-1.8.1/src/uniform/Makefile.am
		unuran-1.8.1/src/uniform/Makefile.in
		unuran-1.8.1/src/uniform/fish.c
		unuran-1.8.1/src/uniform/mrg31k3p.c
		unuran-1.8.1/src/uniform/mstd.c
		unuran-1.8.1/src/uniform/unuran_urng_gsl.h
		unuran-1.8.1/src/uniform/unuran_urng_prng.h
		unuran-1.8.1/src/uniform/ unuran_urng_rngstreams.h
		unuran-1.8.1/src/uniform/urng_builtin.c
		unuran-1.8.1/src/uniform/urng_builtin.h
		unuran-1.8.1/src/uniform/urng_fvoid.c
		unuran-1.8.1/src/uniform/urng_fvoid.h
		unuran-1.8.1/src/uniform/urng_gsl.c
		unuran-1.8.1/src/uniform/urng_gsl.h
		unuran-1.8.1/src/uniform/urng_gslqrng.c
		unuran-1.8.1/src/uniform/urng_gslqrng.h
		unuran-1.8.1/src/uniform/urng_prng.c
		unuran-1.8.1/src/uniform/urng_prng.h
		unuran-1.8.1/src/uniform/urng_randomshift.c
		unuran-1.8.1/src/uniform/urng_randomshift.h
		unuran-1.8.1/src/uniform/urng_rngstreams.c
		unuran-1.8.1/src/uniform/urng_rngstreams.h
		unuran-1.8.1/src/unuran.h.in
		unuran-1.8.1/src/urng/Makefile.am
		unuran-1.8.1/src/urng/Makefile.in
		unuran-1.8.1/src/utills/Makefile.am
		unuran-1.8.1/src/utills/Makefile.in
		unuran-1.8.1/tests/Makefile.am
		unuran-1.8.1/tests/Makefile.in
		unuran-1.8.1/tests/README.conf
		unuran-1.8.1/tests/make_test_files.pl
		unuran-1.8.1/tests/t_arou.c
		unuran-1.8.1/tests/t_arou.conf
		unuran-1.8.1/tests/t_ars.c
		unuran-1.8.1/tests/t_ars.conf
		unuran-1.8.1/tests/t_auto.c
		unuran-1.8.1/tests/t_auto.conf
		unuran-1.8.1/tests/t_cext.c
		unuran-1.8.1/tests/t_cext.conf
		unuran-1.8.1/tests/t_cstd.c
		unuran-1.8.1/tests/t_cstd.conf
		unuran-1.8.1/tests/t_dari.c
		unuran-1.8.1/tests/t_dari.conf
		unuran-1.8.1/tests/t_dau.c
		unuran-1.8.1/tests/t_dau.conf
		unuran-1.8.1/tests/t_deprecated_tdrwg.c

Table 4-1 (Cont.) Open Source or Other Separately Licensed Software

Provider	Component	Licensing Information
		unuran-1.8.1/tests/t_deprecated_vmt.c
		unuran-1.8.1/tests/t_deprecated_vmt.conf
		unuran-1.8.1/tests/t_dext.c
		unuran-1.8.1/tests/t_dext.conf
		unuran-1.8.1/tests/t_dgt.c
		unuran-1.8.1/tests/t_dgt.conf
		unuran-1.8.1/tests/t_distr.c
		unuran-1.8.1/tests/t_distr.conf
		unuran-1.8.1/tests/t_distr_cemp.c
		unuran-1.8.1/tests/t_distr_cemp.conf
		unuran-1.8.1/tests/t_distr_condi.c
		unuran-1.8.1/tests/t_distr_condi.conf
		unuran-1.8.1/tests/t_distr_cont.c
		unuran-1.8.1/tests/t_distr_cont.conf
		unuran-1.8.1/tests/t_distr_corder.c
		unuran-1.8.1/tests/t_distr_corder.conf
		unuran-1.8.1/tests/t_distr_cvec.c
		unuran-1.8.1/tests/t_distr_cvec.conf
		unuran-1.8.1/tests/t_distr_cvemp.c
		unuran-1.8.1/tests/t_distr_cvemp.conf
		unuran-1.8.1/tests/t_distr_cxtrans.c
		unuran-1.8.1/tests/t_distr_cxtrans.conf
		unuran-1.8.1/tests/t_distr_discr.c
		unuran-1.8.1/tests/t_distr_discr.conf
		unuran-1.8.1/tests/t_distr_matr.c
		unuran-1.8.1/tests/t_distr_matr.conf
		unuran-1.8.1/tests/t_dsrou.c
		unuran-1.8.1/tests/t_dsrou.conf
		unuran-1.8.1/tests/t_dss.c
		unuran-1.8.1/tests/t_dss.conf
		unuran-1.8.1/tests/t_dstd.c
		unuran-1.8.1/tests/t_dstd.conf
		unuran-1.8.1/tests/t_empk.c
		unuran-1.8.1/tests/t_empk.conf
		unuran-1.8.1/tests/t_empl.c
		unuran-1.8.1/tests/t_empl.conf
		unuran-1.8.1/tests/t_gibbs.c
		unuran-1.8.1/tests/t_gibbs.conf
		unuran-1.8.1/tests/t_hinv.c
		unuran-1.8.1/tests/t_hinv.conf
		unuran-1.8.1/tests/t_hist.c
		unuran-1.8.1/tests/t_hist.conf
		unuran-1.8.1/tests/t_hitro.c
		unuran-1.8.1/tests/t_hrb.c
		unuran-1.8.1/tests/t_hrb.conf
		unuran-1.8.1/tests/t_hrd.c
		unuran-1.8.1/tests/t_hrd.conf
		unuran-1.8.1/tests/t_hri.c
		unuran-1.8.1/tests/t_hri.conf
		unuran-1.8.1/tests/t_info.c
		unuran-1.8.1/tests/t_info.conf
		unuran-1.8.1/tests/t_itdr.c
		unuran-1.8.1/tests/t_itdr.conf
		unuran-1.8.1/tests/t_mcorr.c
		unuran-1.8.1/tests/t_mcorr.conf

Table 4-1 (Cont.) Open Source or Other Separately Licensed Software

Provider	Component	Licensing Information
		unuran-1.8.1/tests/t_mixt.c
		unuran-1.8.1/tests/t_mvstd.c
		unuran-1.8.1/tests/t_mvstd.conf
		unuran-1.8.1/tests/t_mvtdr.c
		unuran-1.8.1/tests/t_ninv.c
		unuran-1.8.1/tests/t_ninv.conf
		unuran-1.8.1/tests/t_norta.c
		unuran-1.8.1/tests/t_norta.conf
		unuran-1.8.1/tests/t_nrou.c
		unuran-1.8.1/tests/t_nrou.conf
		unuran-1.8.1/tests/t_pinv
		unuran-1.8.1/tests/t_pinv.c
		unuran-1.8.1/tests/t_srou.c
		unuran-1.8.1/tests/t_srou.conf
		unuran-1.8.1/tests/t_ssr.c
		unuran-1.8.1/tests/t_ssr.conf
		unuran-1.8.1/tests/t_stringparser.c
		unuran-1.8.1/tests/t_stringparser.conf
		unuran-1.8.1/tests/t_tabl.c
		unuran-1.8.1/tests/t_tabl.conf
		unuran-1.8.1/tests/t_tdr.c
		unuran-1.8.1/tests/t_tdr.conf
		unuran-1.8.1/tests/t_tdr_gw.c
		unuran-1.8.1/tests/t_tdr_gw.conf
		unuran-1.8.1/tests/t_tdr_ia.c
		unuran-1.8.1/tests/t_tdr_ia.conf
		unuran-1.8.1/tests/t_tdr_ps.c
		unuran-1.8.1/tests/t_tdr_ps.conf
		unuran-1.8.1/tests/t_unif.c
		unuran-1.8.1/tests/t_unif.conf
		unuran-1.8.1/tests/t_unur_error.c
		unuran-1.8.1/tests/t_unur_error.conf
		unuran-1.8.1/tests/t_unur_tests.c
		unuran-1.8.1/tests/t_unur_tests.conf
		unuran-1.8.1/tests/t_urng.c
		unuran-1.8.1/tests/t_urng.conf
		unuran-1.8.1/tests/t_utdr.c
		unuran-1.8.1/tests/t_utdr.conf
		unuran-1.8.1/tests/t_util_matrix.c
		unuran-1.8.1/tests/t_util_matrix.conf
		unuran-1.8.1/tests/t_vempk.c
		unuran-1.8.1/tests/t_vempk.conf
		unuran-1.8.1/tests/t_vnrou.c
		unuran-1.8.1/tests/t_vnrou.conf
		unuran-1.8.1/tests/t_x_gen.c
		unuran-1.8.1/tests/t_x_gen.conf
		unuran-1.8.1/tests/test_StdDistr.c
		unuran-1.8.1/tests/test_StdDistr.m
		unuran-1.8.1/tests/test_functionparser.c
		unuran-1.8.1/tests/test_functionparser.m
		unuran-1.8.1/tests/testcounter.c
		unuran-1.8.1/tests/testdistributions/
		Makefile.am
		unuran-1.8.1/tests/testdistributions/
		Makefile.in

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Provider	Component	Licensing Information
	unuran-1.8.1/tests/testdistributions/ c_sawtooth_contpdf.c unuran-1.8.1/tests/testdistributions/ c_sawtooth_discpdf.c unuran-1.8.1/tests/testdistributions/ c_w_pdf_from_logpdf.c unuran-1.8.1/tests/testdistributions/ c_with_multiple_of_pdf.c unuran-1.8.1/tests/testdistributions/ c_wo_logpdf.c unuran-1.8.1/tests/testdistributions/ testdistributions.h unuran-1.8.1/tests/testdistributions/ vc_cauchy_RoU_ball.c unuran-1.8.1/tests/testdistributions/ vc_special_correlations_ar1.c unuran-1.8.1/tests/testdistributions/ vc_special_correlations_constantrho.c unuran-1.8.1/tests/testdistributions/ vc_w_marginals.c unuran-1.8.1/tests/testdistributions/ vc_wo_logpdf.c unuran-1.8.1/tests/testroutines.c unuran-1.8.1/tests/testunuran.h unuran-1.8.1/src/distr/deprecated_distr.c unuran-1.8.1/src/distr/deprecated_distr.h unuran-1.8.1/src/methods/deprecated_methods.c unuran-1.8.1/src/methods/deprecated_methods.h unuran-1.8.1/src/methods/deprecated_tdrwg.h unuran-1.8.1/src/methods/deprecated_vmt.c unuran-1.8.1/src/methods/deprecated_vmt.h unuran-1.8.1/src/methods/ deprecated_vmt_struct.h unuran-1.8.1/src/parser/ stringparser_lists.ch.in	<p>UNU.RAN relies on a modified version of the library Cephes, see ./src/src/specfunct/cephes_source.h</p> <p>----- numpy (BSD 3-Clause "New" or "Revised" License) [https://github.com/numpy/numpy] ----- Copyright notice(s):</p> <p>Copyright (c) 1992-2013 The University of Tennessee and The University of Tennessee Research Foundation. All rights reserved.</p> <p>Copyright (c) 2000-2013 The University of California Berkeley. All rights reserved.</p> <p>Copyright (c) 2006-2013 The University of</p>

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		unuran-1.8.1/doc/figures/ discrete_inversion.txt
		unuran-1.8.1/doc/figures/inversion.eps
		unuran-1.8.1/doc/figures/inversion.pdf
		unuran-1.8.1/doc/figures/inversion.png
		unuran-1.8.1/doc/figures/inversion.txt
		unuran-1.8.1/doc/figures/ rejection_from_constant_hat.eps
		unuran-1.8.1/doc/figures/ rejection_from_constant_hat.pdf
		unuran-1.8.1/doc/figures/ rejection_from_constant_hat.png
		unuran-1.8.1/doc/figures/ rejection_from_constant_hat.txt
		unuran-1.8.1/doc/figures/rou-cauchy.eps
		unuran-1.8.1/doc/figures/rou-cauchy.pdf
		unuran-1.8.1/doc/figures/rou-cauchy.png
		unuran-1.8.1/doc/figures/rou-cauchy.txt
		unuran-1.8.1/doc/figures/texpict2img.pl
		unuran-1.8.1/doc/src/Makefile.am
		unuran-1.8.1/doc/src/Makefile.in
		unuran-1.8.1/doc/src/README
		unuran-1.8.1/doc/src/arvag.dh
		unuran-1.8.1/doc/src/error_debug.dh
		unuran-1.8.1/doc/src/examples.dh
		unuran-1.8.1/doc/src/glossary.dh
		unuran-1.8.1/doc/src/index.dh
		unuran-1.8.1/doc/src/installation.dh
		unuran-1.8.1/doc/src/intro.dh
		unuran-1.8.1/doc/src/make_texi.pl
		unuran-1.8.1/doc/src/methods.dh
		unuran-1.8.1/doc/src/methods_cemp.texi
		unuran-1.8.1/doc/src/methods_cont.texi
		unuran-1.8.1/doc/src/methods_cvec.texi
		unuran-1.8.1/doc/src/methods_cvemp.texi
		unuran-1.8.1/doc/src/methods_discr.texi
		unuran-1.8.1/doc/src/methods_matr.texi
		unuran-1.8.1/doc/src/methods_mcmc.texi
		unuran-1.8.1/doc/src/misc.dh
		unuran-1.8.1/doc/src/ref_example0.texi
		unuran-1.8.1/doc/src/ref_example0_str.texi
		unuran-1.8.1/doc/src/ref_example1.texi
		unuran-1.8.1/doc/src/ref_example1_str.texi
		unuran-1.8.1/doc/src/ref_example2.texi
		unuran-1.8.1/doc/src/ref_example2_str.texi
		unuran-1.8.1/doc/src/ref_example3.texi
		unuran-1.8.1/doc/src/ref_example3_str.texi
		unuran-1.8.1/doc/src/ref_example_FuncStr.texi
		unuran-1.8.1/doc/src/ref_example_anti.texi
		unuran-1.8.1/doc/src/ ref_example_anti_str.texi

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		unuran-1.8.1/doc/src/ref_example_cont.texi
		unuran-1.8.1/doc/src/
	ref_example_cont_str.texi	
		unuran-1.8.1/doc/src/ref_example_dext.texi
		unuran-1.8.1/doc/src/ref_example_discr.texi
		unuran-1.8.1/doc/src/
	ref_example_discr_str.texi	
		unuran-1.8.1/doc/src/ref_example_emp.texi
		unuran-1.8.1/doc/src/ref_example_emp_str.texi
		unuran-1.8.1/doc/src/ref_example_gsl.texi
		unuran-1.8.1/doc/src/ref_example_mixt.texi
		unuran-1.8.1/doc/src/
	ref_example_mixt_inv.texi	
		unuran-1.8.1/doc/src/
	ref_example_rngstreams.texi	
		unuran-1.8.1/doc/src/ref_example_vemp.texi
		unuran-1.8.1/doc/src/ref_example_win32_1.texi
		unuran-1.8.1/doc/src/ref_example_win32_2.texi
		unuran-1.8.1/doc/src/references.dh
		unuran-1.8.1/doc/src/stddist.dh
		unuran-1.8.1/doc/src/texinfo.tex
		unuran-1.8.1/doc/src/top.dh
		unuran-1.8.1/doc/src/unuran_src.texi
		unuran-1.8.1/doc/stamp-1
		unuran-1.8.1/doc/stamp-vti
		unuran-1.8.1/doc/unuran.html
		unuran-1.8.1/doc/unuran.info
		unuran-1.8.1/doc/unuran.pdf
		unuran-1.8.1/doc/unuran.texi
		unuran-1.8.1/doc/unuran.txt
		unuran-1.8.1/doc/unuran_win32.html
		unuran-1.8.1/doc/unuran_win32.info
		unuran-1.8.1/doc/unuran_win32.pdf
		unuran-1.8.1/doc/unuran_win32.texi
		unuran-1.8.1/doc/unuran_win32_libname.texi
		unuran-1.8.1/doc/version.texi
		unuran-1.8.1/doc/version_win32.texi
		unuran-1.8.1/examples/Makefile.am
		unuran-1.8.1/examples/Makefile.in
		unuran-1.8.1/examples/example0.c
		unuran-1.8.1/examples/example0_str.c
		unuran-1.8.1/examples/example1.c
		unuran-1.8.1/examples/example1_str.c
		unuran-1.8.1/examples/example2.c
		unuran-1.8.1/examples/example2_str.c
		unuran-1.8.1/examples/example3.c
		unuran-1.8.1/examples/example3_str.c
		unuran-1.8.1/examples/example_FuncStr.c
		unuran-1.8.1/examples/example_anti.c
		unuran-1.8.1/examples/example_anti_str.c
		unuran-1.8.1/examples/example_cext.c
		unuran-1.8.1/examples/example_cont.c
		unuran-1.8.1/examples/example_cont_str.c
		unuran-1.8.1/examples/example_cpp.cpp

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		unuran-1.8.1/examples/example_discr.c
		unuran-1.8.1/examples/example_discr_str.c
		unuran-1.8.1/examples/example_emp.c
		unuran-1.8.1/examples/example_emp_str.c
		unuran-1.8.1/examples/example_errorhandler.c
		unuran-1.8.1/examples/example_gsl.c
		unuran-1.8.1/examples/example_mcorr.c
		unuran-1.8.1/examples/example_mixt.c
		unuran-1.8.1/examples/example_mixt_inv.c
		unuran-1.8.1/examples/example_prng.c
		unuran-1.8.1/examples/example_prng_str.c
		unuran-1.8.1/examples/example_rngstreams.c
		unuran-1.8.1/examples/example_vemp.c
		unuran-1.8.1/experiments/Makefile.am
		unuran-1.8.1/experiments/Makefile.in
		unuran-1.8.1/experiments/make_timing_table.c
		unuran-1.8.1/experiments/timings_chap_10.conf
		unuran-1.8.1/experiments/timings_chap_8.conf
		unuran-1.8.1/scripts/Makefile.am
		unuran-1.8.1/scripts/Makefile.in
		unuran-1.8.1/scripts/
		compute_machine_constants.c
		unuran-1.8.1/scripts/make_urng_header.pl
		unuran-1.8.1/scripts/merge_h.pl
		unuran-1.8.1/scripts/read_PDF.pl
		unuran-1.8.1/scripts/remove_comments.pl
		unuran-1.8.1/scripts/run_valgrind.sh
		unuran-1.8.1/scripts/win32/Makefile.win32
		unuran-1.8.1/scripts/win32/build.sh
		unuran-1.8.1/scripts/win32/example1.c
		unuran-1.8.1/scripts/win32/example2.c
		unuran-1.8.1/scripts/win32/make_dll_def.pl
		unuran-1.8.1/src/Makefile.am
		unuran-1.8.1/src/Makefile.in
		unuran-1.8.1/src/README
		unuran-1.8.1/src/distr/Makefile.am
		unuran-1.8.1/src/distr/Makefile.in
		unuran-1.8.1/src/distributions/Makefile.am
		unuran-1.8.1/src/distributions/Makefile.in
		unuran-1.8.1/src/distributions/README
		unuran-1.8.1/src/methods/Makefile.am
		unuran-1.8.1/src/methods/Makefile.in
		unuran-1.8.1/src/parser/Makefile.am
		unuran-1.8.1/src/parser/Makefile.in
		unuran-1.8.1/src/parser/funcparser_doc.dh
		unuran-1.8.1/src/parser/make_stringparser.pl
		unuran-1.8.1/src/parser/stringparser_doc.dh
		unuran-1.8.1/src/specfunct/Makefile.am
		unuran-1.8.1/src/specfunct/Makefile.in
		unuran-1.8.1/src/tests/Makefile.am
		unuran-1.8.1/src/tests/Makefile.in
		unuran-1.8.1/src/uniform/Makefile.am
		unuran-1.8.1/src/uniform/Makefile.in
		unuran-1.8.1/src/uniform/fish.c

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		unuran-1.8.1/tests/t_distr_cont.c
		unuran-1.8.1/tests/t_distr_cont.conf
		unuran-1.8.1/tests/t_distr_corder.c
		unuran-1.8.1/tests/t_distr_corder.conf
		unuran-1.8.1/tests/t_distr_cvec.c
		unuran-1.8.1/tests/t_distr_cvec.conf
		unuran-1.8.1/tests/t_distr_cvemp.c
		unuran-1.8.1/tests/t_distr_cvemp.conf
		unuran-1.8.1/tests/t_distr_cxtrans.c
		unuran-1.8.1/tests/t_distr_cxtrans.conf
		unuran-1.8.1/tests/t_distr_discr.c
		unuran-1.8.1/tests/t_distr_discr.conf
		unuran-1.8.1/tests/t_distr_matr.c
		unuran-1.8.1/tests/t_distr_matr.conf
		unuran-1.8.1/tests/t_dsrou.c
		unuran-1.8.1/tests/t_dsrou.conf
		unuran-1.8.1/tests/t_dss.c
		unuran-1.8.1/tests/t_dss.conf
		unuran-1.8.1/tests/t_dstd.c
		unuran-1.8.1/tests/t_dstd.conf
		unuran-1.8.1/tests/t_empk.c
		unuran-1.8.1/tests/t_empk.conf
		unuran-1.8.1/tests/t_empl.c
		unuran-1.8.1/tests/t_empl.conf
		unuran-1.8.1/tests/t_gibbs.c
		unuran-1.8.1/tests/t_gibbs.conf
		unuran-1.8.1/tests/t_hinv.c
		unuran-1.8.1/tests/t_hinv.conf
		unuran-1.8.1/tests/t_hist.c
		unuran-1.8.1/tests/t_hist.conf
		unuran-1.8.1/tests/t_hitro.c
		unuran-1.8.1/tests/t_hrb.c
		unuran-1.8.1/tests/t_hrb.conf
		unuran-1.8.1/tests/t_hrd.c
		unuran-1.8.1/tests/t_hrd.conf
		unuran-1.8.1/tests/t_hri.c
		unuran-1.8.1/tests/t_hri.conf
		unuran-1.8.1/tests/t_info.c
		unuran-1.8.1/tests/t_info.conf
		unuran-1.8.1/tests/t_itdr.c
		unuran-1.8.1/tests/t_itdr.conf
		unuran-1.8.1/tests/t_mcorr.c
		unuran-1.8.1/tests/t_mcorr.conf
		unuran-1.8.1/tests/t_mixt.c
		unuran-1.8.1/tests/t_mvstd.c
		unuran-1.8.1/tests/t_mvstd.conf
		unuran-1.8.1/tests/t_mvtdr.c
		unuran-1.8.1/tests/t_ninv.c
		unuran-1.8.1/tests/t_ninv.conf
		unuran-1.8.1/tests/t_norta.c
		unuran-1.8.1/tests/t_norta.conf
		unuran-1.8.1/tests/t_nrou.c
		unuran-1.8.1/tests/t_nrou.conf
		unuran-1.8.1/tests/t_pinv

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		unuran-1.8.1/tests/t_pinv.c
		unuran-1.8.1/tests/t_srou.c
		unuran-1.8.1/tests/t_srou.conf
		unuran-1.8.1/tests/t_ssr.c
		unuran-1.8.1/tests/t_ssr.conf
		unuran-1.8.1/tests/t_stringparser.c
		unuran-1.8.1/tests/t_stringparser.conf
		unuran-1.8.1/tests/t_tabl.c
		unuran-1.8.1/tests/t_tabl.conf
		unuran-1.8.1/tests/t_tdr.c
		unuran-1.8.1/tests/t_tdr.conf
		unuran-1.8.1/tests/t_tdr_gw.c
		unuran-1.8.1/tests/t_tdr_gw.conf
		unuran-1.8.1/tests/t_tdr_ia.c
		unuran-1.8.1/tests/t_tdr_ia.conf
		unuran-1.8.1/tests/t_tdr_ps.c
		unuran-1.8.1/tests/t_tdr_ps.conf
		unuran-1.8.1/tests/t_unif.c
		unuran-1.8.1/tests/t_unif.conf
		unuran-1.8.1/tests/t_unur_error.c
		unuran-1.8.1/tests/t_unur_error.conf
		unuran-1.8.1/tests/t_unur_tests.c
		unuran-1.8.1/tests/t_unur_tests.conf
		unuran-1.8.1/tests/t_urng.c
		unuran-1.8.1/tests/t_urng.conf
		unuran-1.8.1/tests/t_utdr.c
		unuran-1.8.1/tests/t_utdr.conf
		unuran-1.8.1/tests/t_util_matrix.c
		unuran-1.8.1/tests/t_util_matrix.conf
		unuran-1.8.1/tests/t_vempk.c
		unuran-1.8.1/tests/t_vempk.conf
		unuran-1.8.1/tests/t_vnrou.c
		unuran-1.8.1/tests/t_vnrou.conf
		unuran-1.8.1/tests/t_x_gen.c
		unuran-1.8.1/tests/t_x_gen.conf
		unuran-1.8.1/tests/test_StdDistr.c
		unuran-1.8.1/tests/test_StdDistr.m
		unuran-1.8.1/tests/test_functionparser.c
		unuran-1.8.1/tests/test_functionparser.m
		unuran-1.8.1/tests/testcounter.c
		unuran-1.8.1/tests/testdistributions/
		Makefile.am
		unuran-1.8.1/tests/testdistributions/
		Makefile.in
		unuran-1.8.1/tests/testdistributions/
		c_sawtooth_contpdf.c
		unuran-1.8.1/tests/testdistributions/
		c_sawtooth_discpdf.c
		unuran-1.8.1/tests/testdistributions/
		c_w_pdf_from_logpdf.c
		unuran-1.8.1/tests/testdistributions/
		c_with_multiple_of_pdf.c
		unuran-1.8.1/tests/testdistributions/
		c_wo_logpdf.c
		unuran-1.8.1/tests/testdistributions/

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		testdistributions.h unuran-1.8.1/tests/testdistributions/ vc_cauchy_RoU_ball.c unuran-1.8.1/tests/testdistributions/ vc_special_correlations_ar1.c unuran-1.8.1/tests/testdistributions/ vc_special_correlations_constantrho.c unuran-1.8.1/tests/testdistributions/ vc_w_marginals.c unuran-1.8.1/tests/testdistributions/ vc_wo_logpdf.c unuran-1.8.1/tests/testroutines.c unuran-1.8.1/tests/testunuran.h unuran-1.8.1/src/distr/deprecated_distr.c unuran-1.8.1/src/distr/deprecated_distr.h unuran-1.8.1/src/methods/deprecated_methods.c unuran-1.8.1/src/methods/deprecated_methods.h unuran-1.8.1/src/methods/deprecated_tdrwg.h unuran-1.8.1/src/methods/deprecated_vmt.c unuran-1.8.1/src/methods/deprecated_vmt.h unuran-1.8.1/src/methods/ deprecated_vmt_struct.h unuran-1.8.1/src/parser/ stringparser_lists.ch.in UNU.RAN relies on a modified version of the library Cephes, see ./src/src/specfunct/ cephes_source.h

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		+-----+-----+-----+ +-----+-----+-----+
		2.1.3 2.1.2 2002 PSF yes
		+-----+-----+-----+ +-----+-----+-----+
		2.2 and above 2.1.1 2001-now PSF yes
		+-----+-----+-----+ +-----+-----+-----+

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